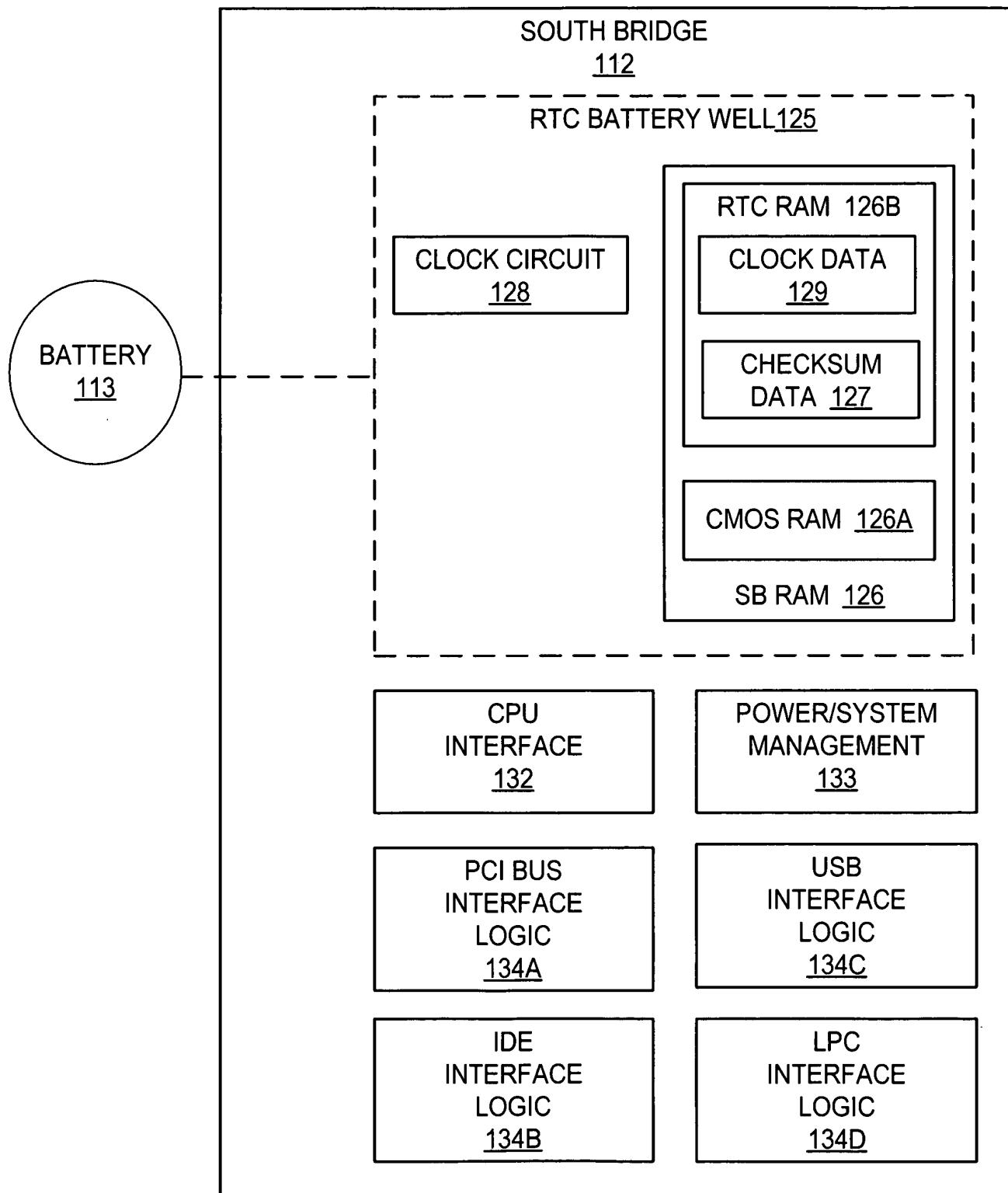
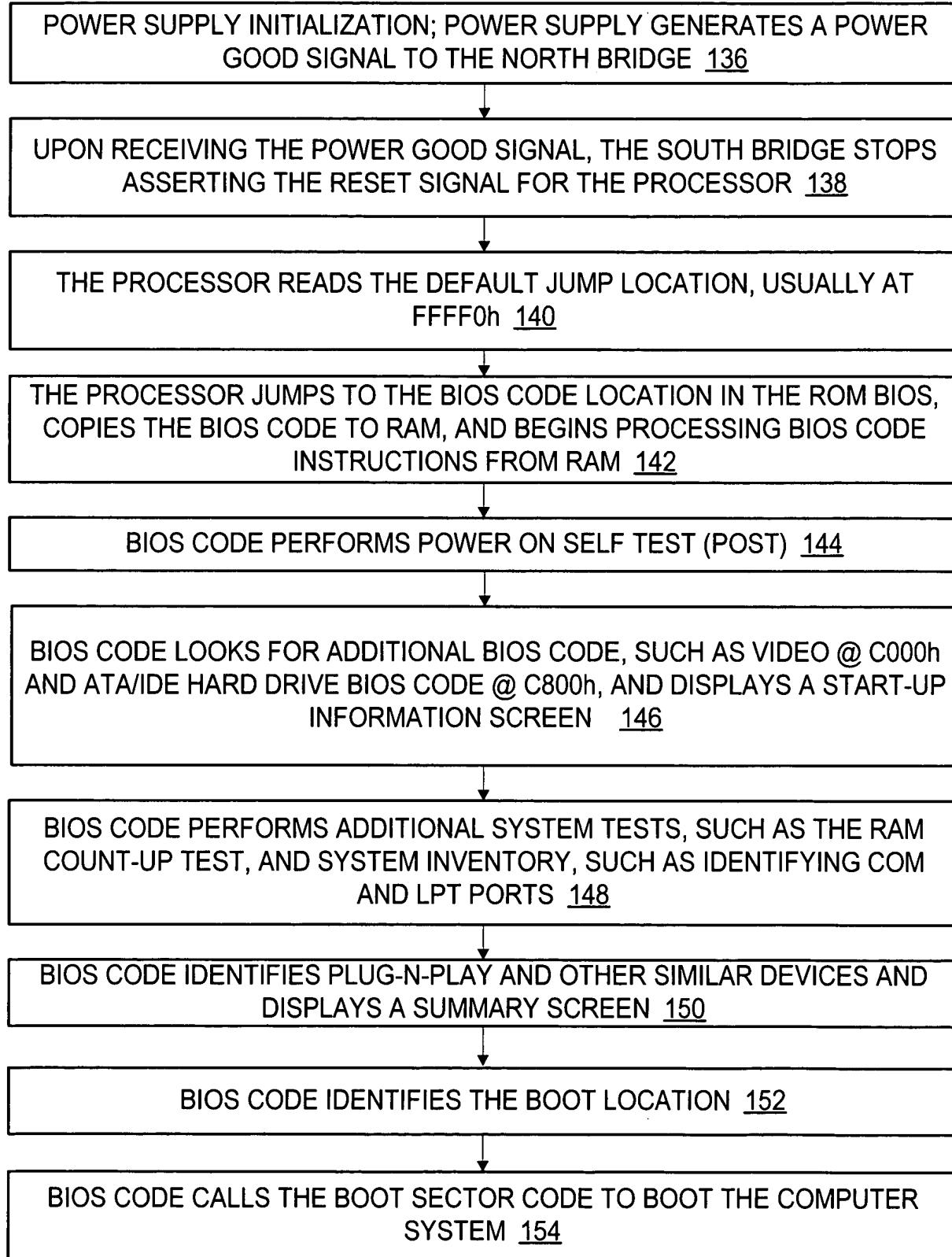


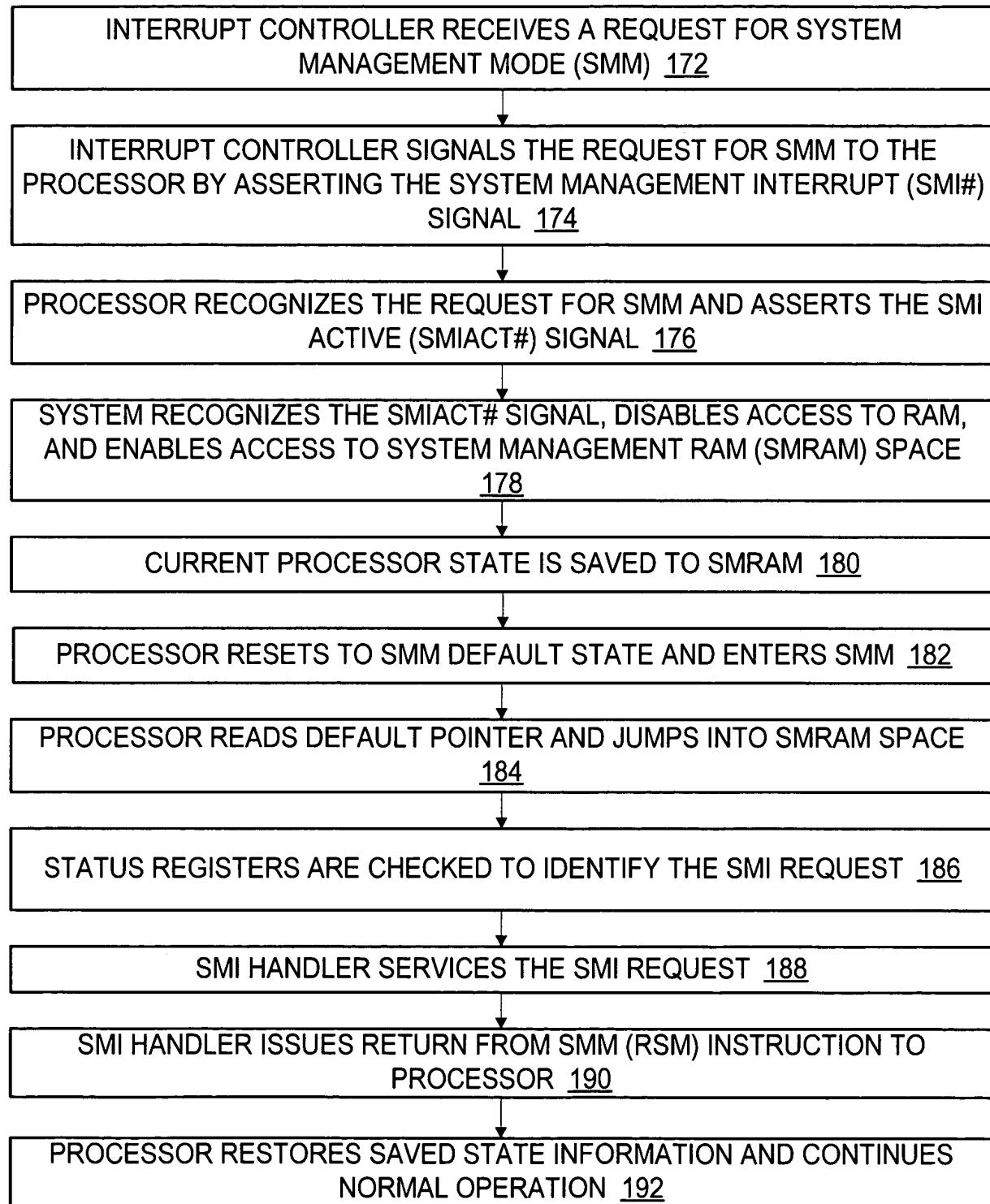
**Fig. 1A**  
**(Prior Art)**



**Fig. 1B**  
**(Prior Art)**



**Fig. 2A**  
**(Prior Art)**



**Fig. 2B  
(Prior Art)**

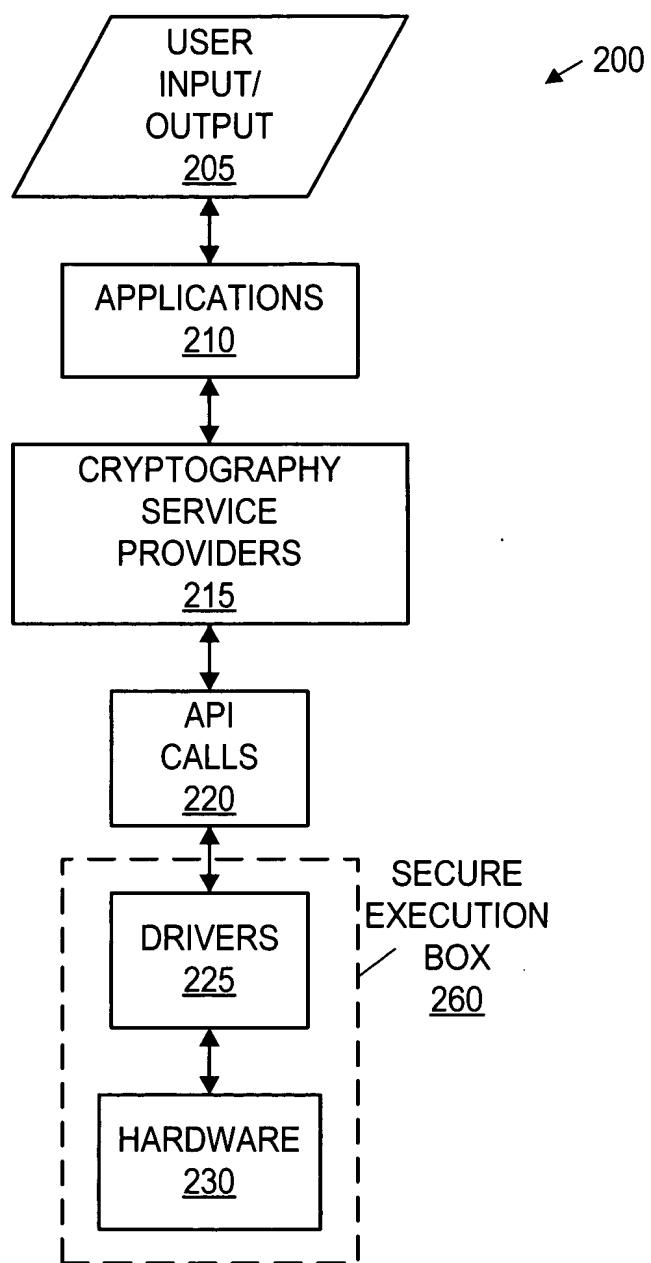


Fig. 3

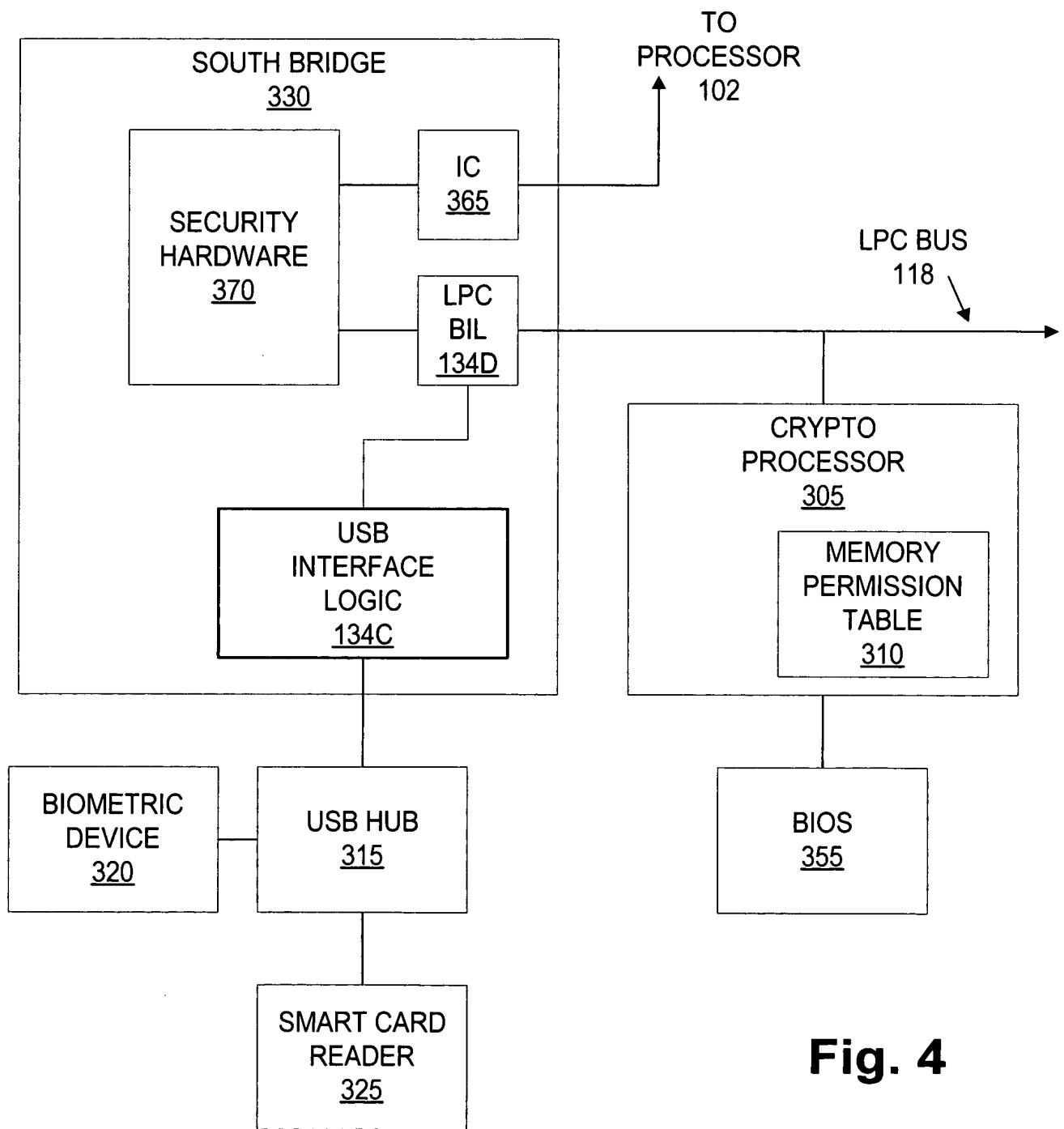
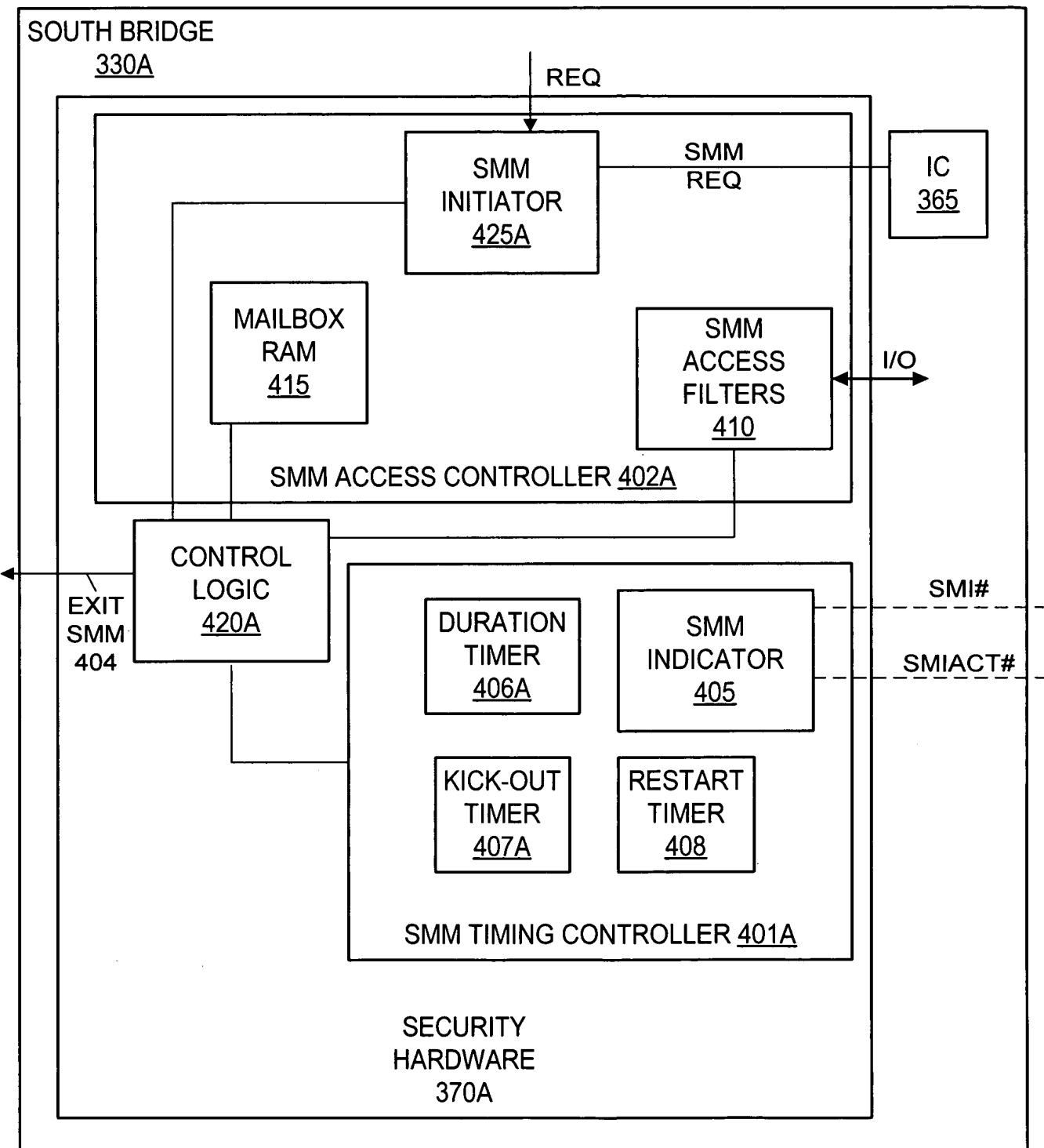
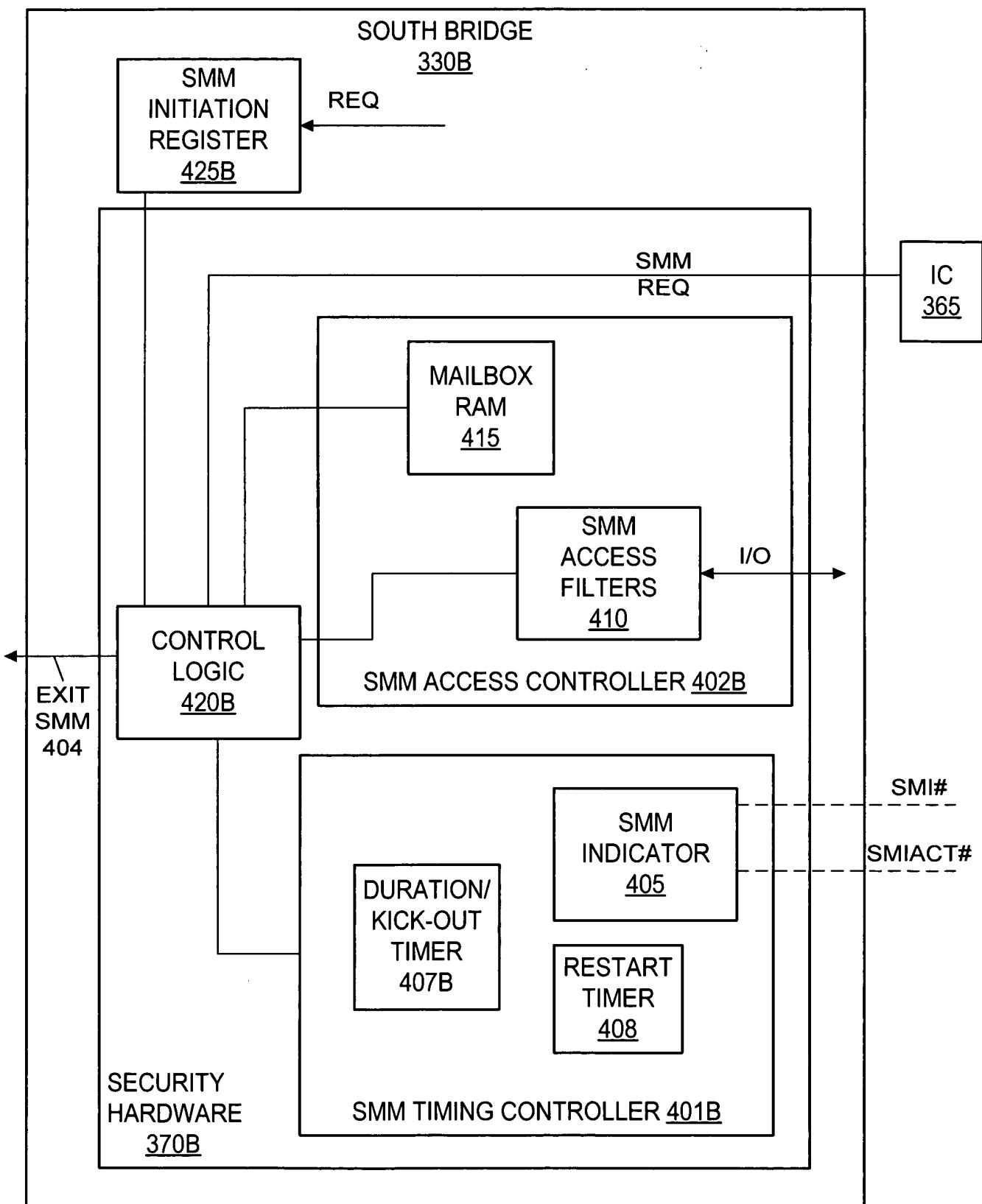
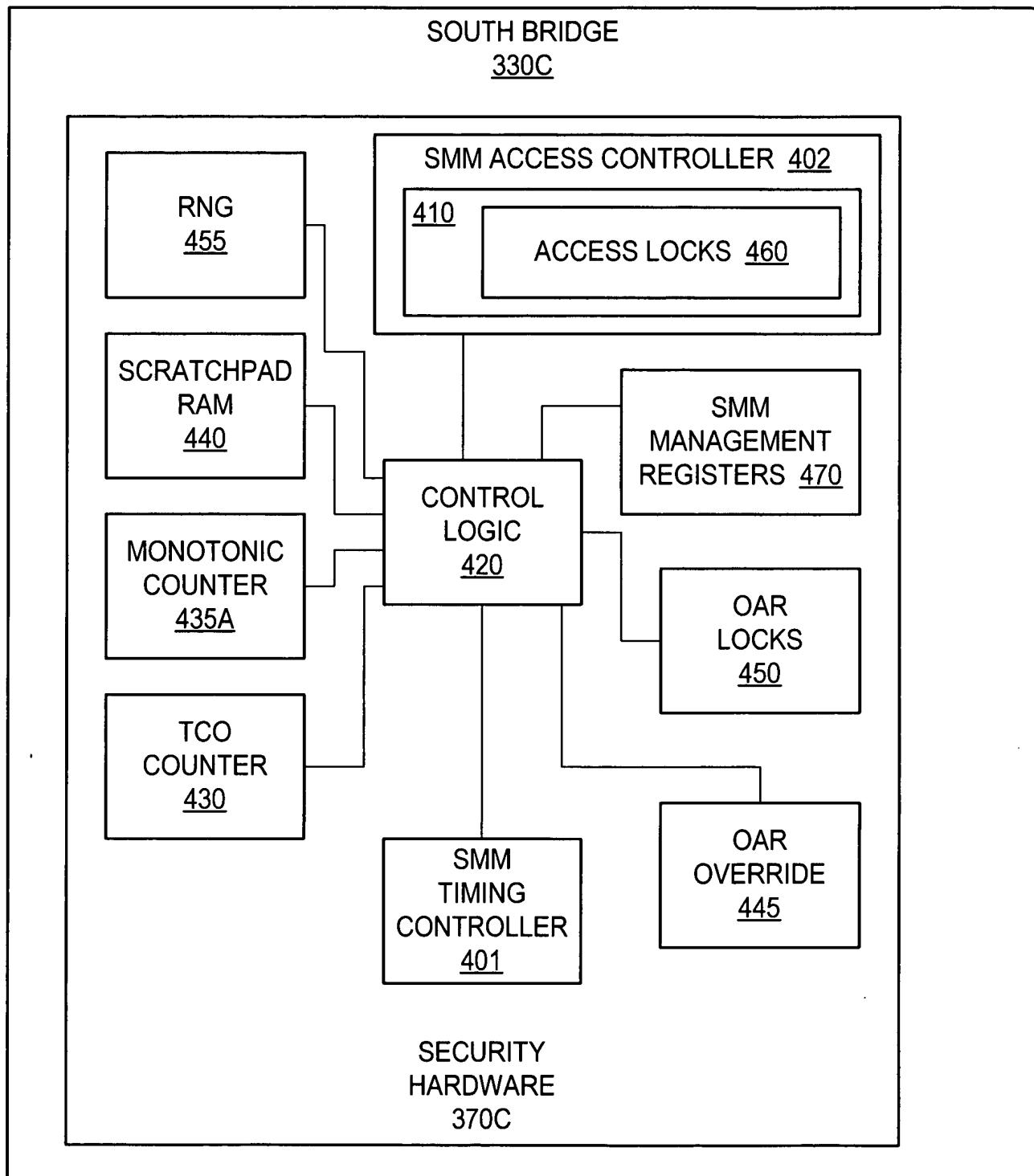


Fig. 4

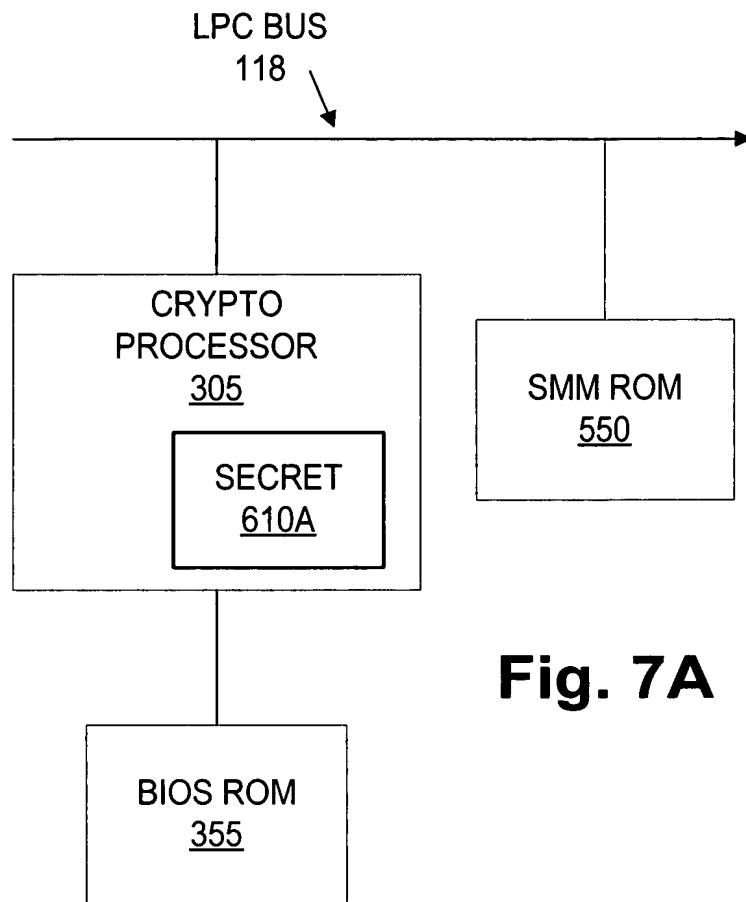
**Fig. 5A**

**Fig. 5B**

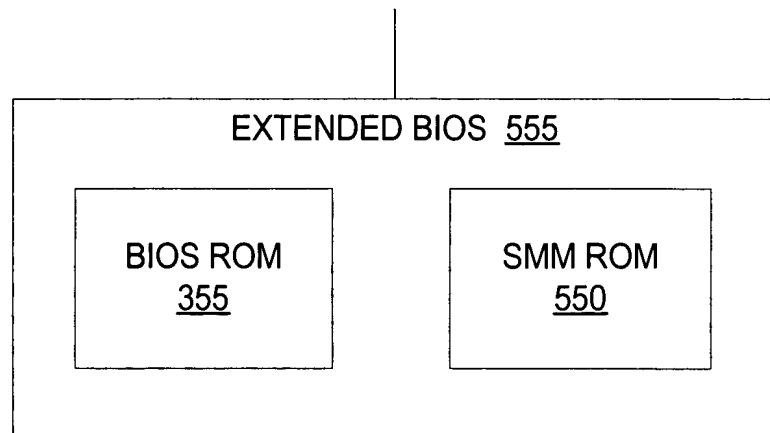


**Fig. 6**

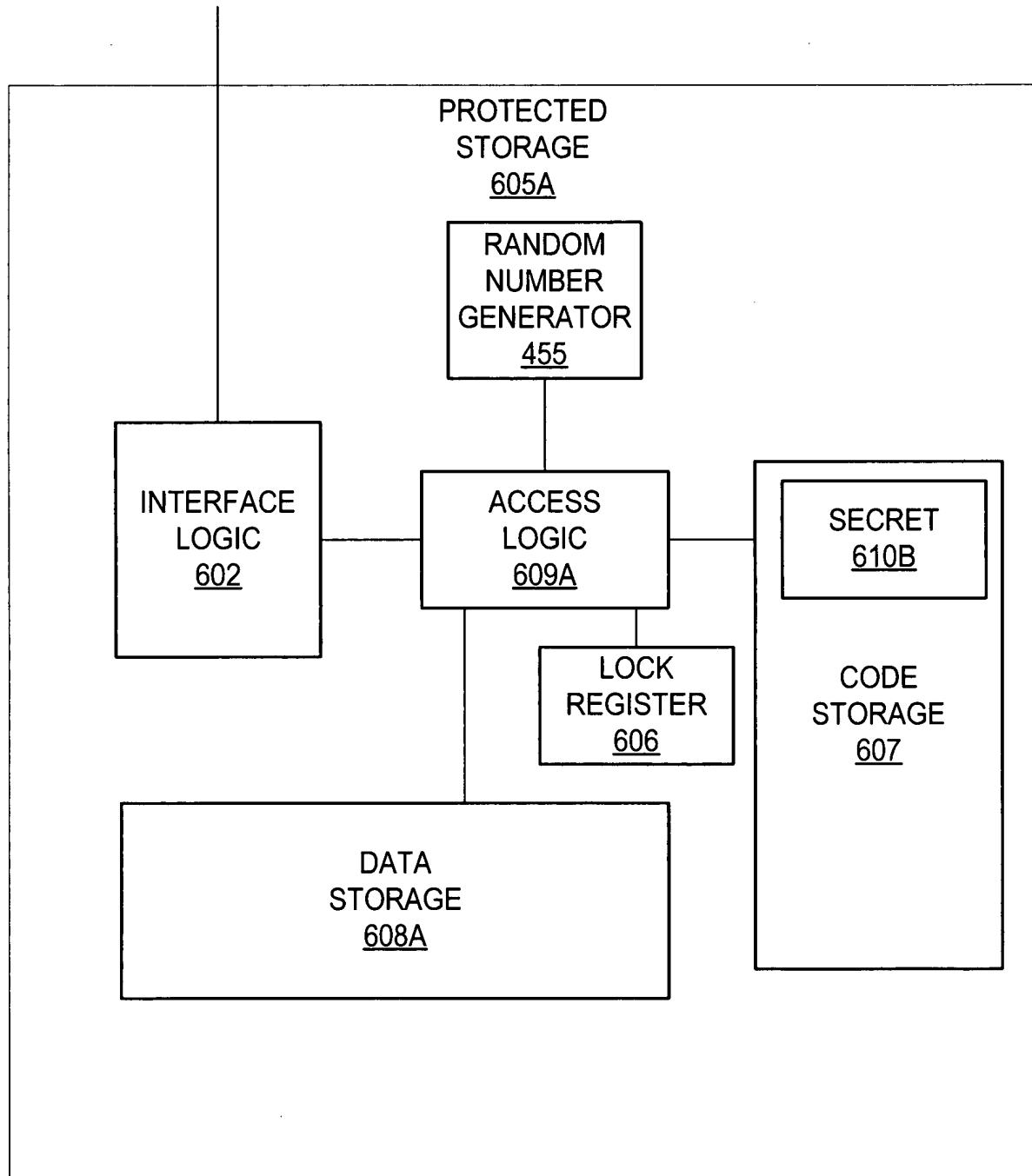
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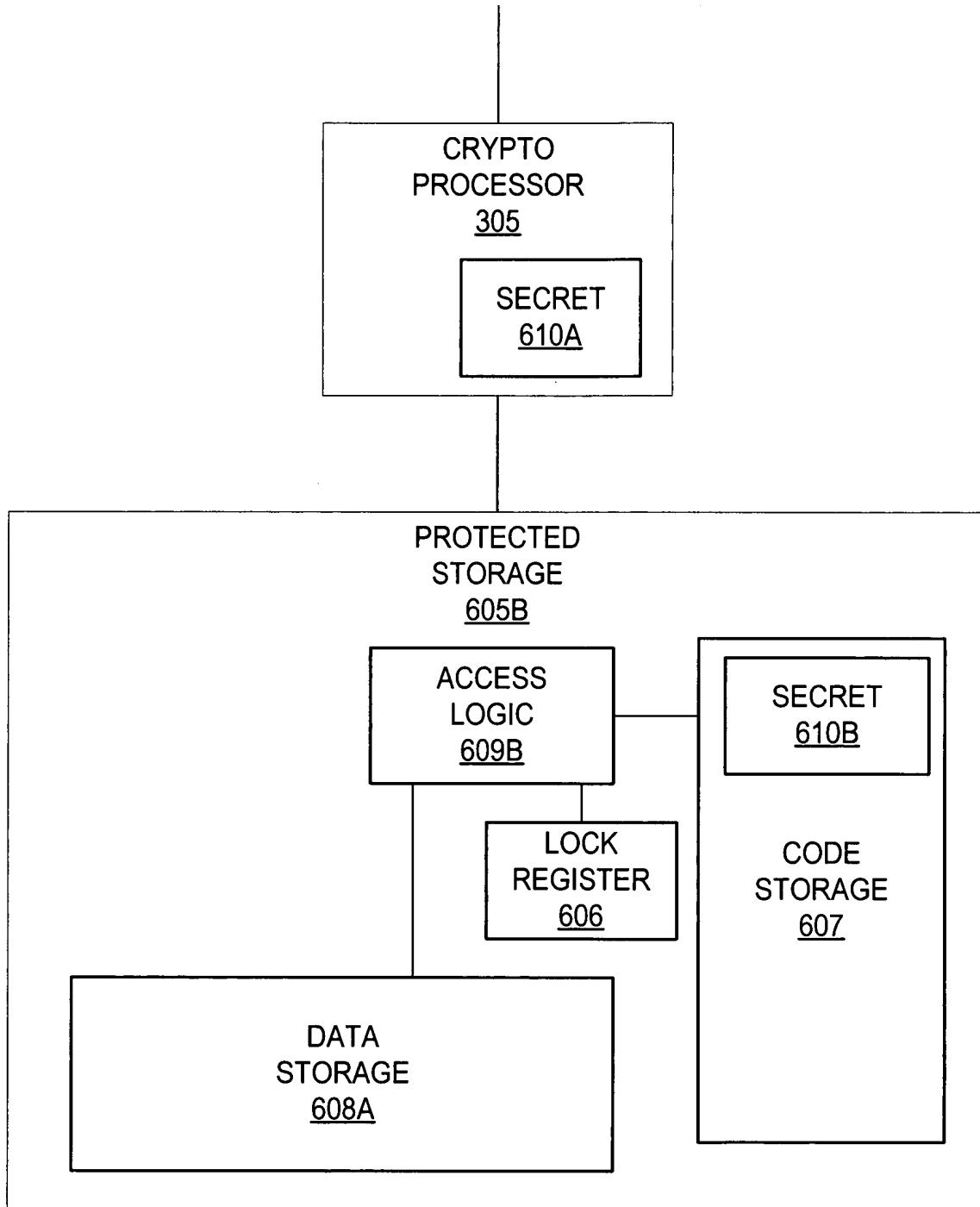
**Fig. 7A**



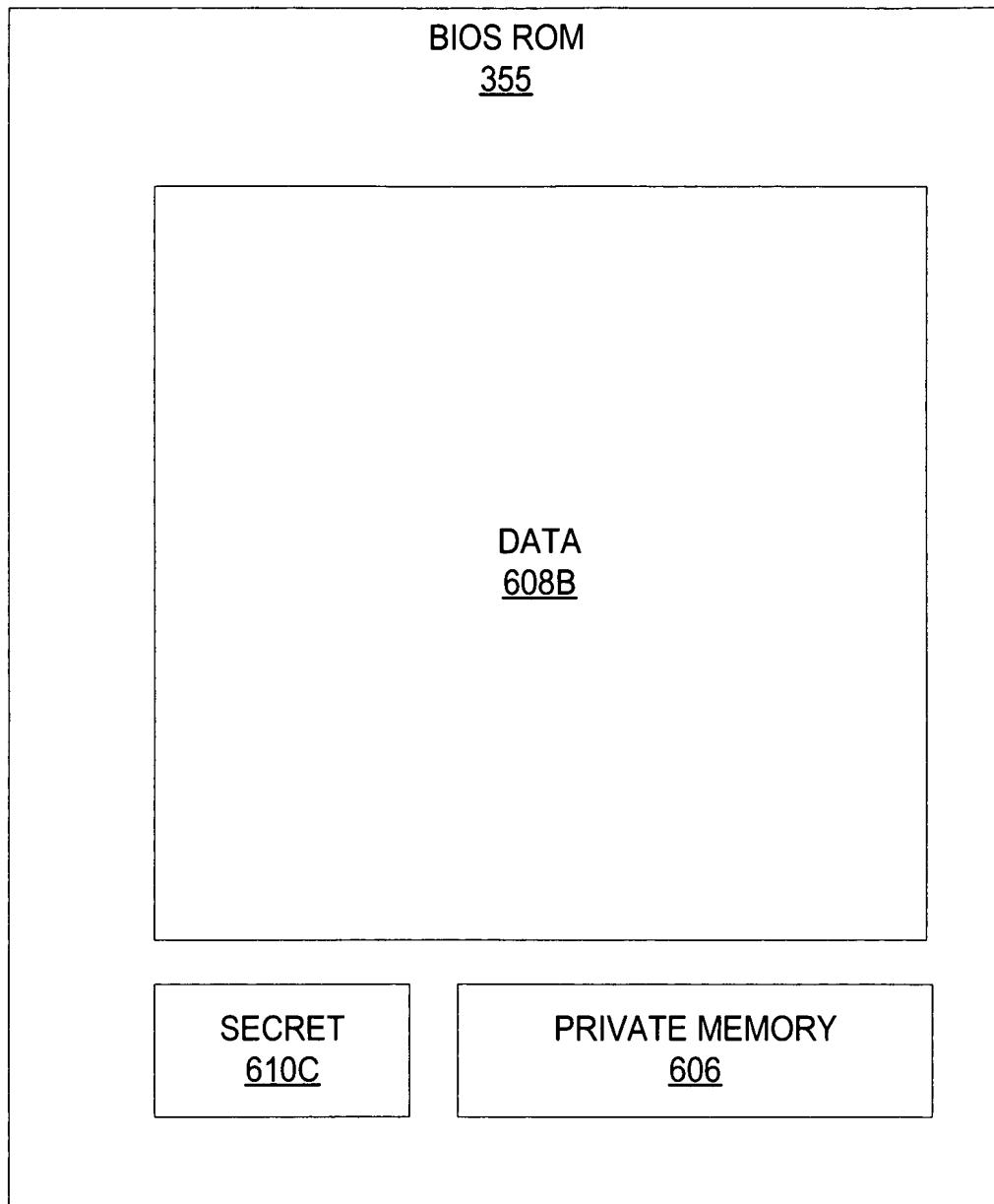
**Fig. 7B**



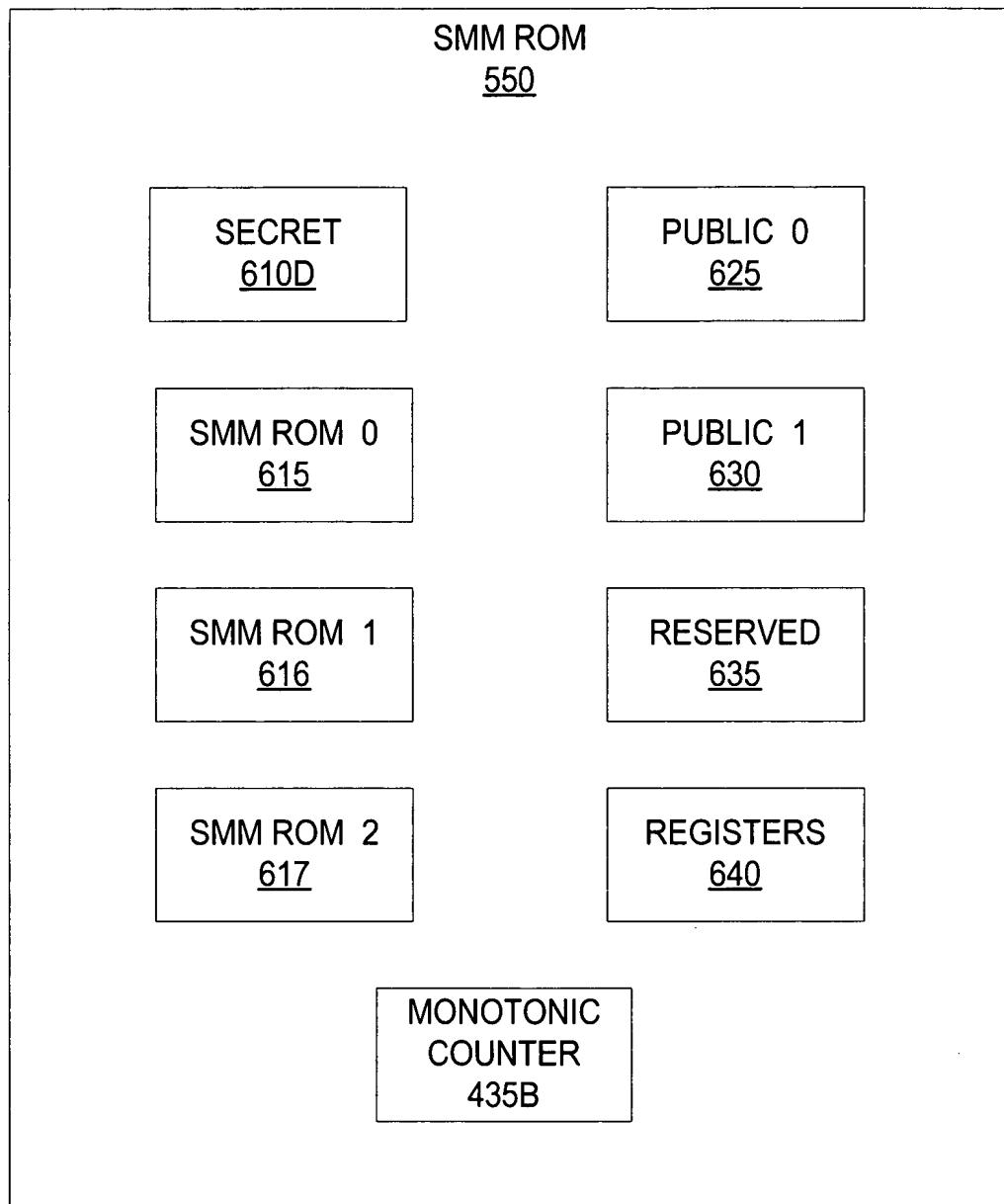
**Fig. 7C**



**Fig. 7D**



**Fig. 8A**



**Fig. 8B**

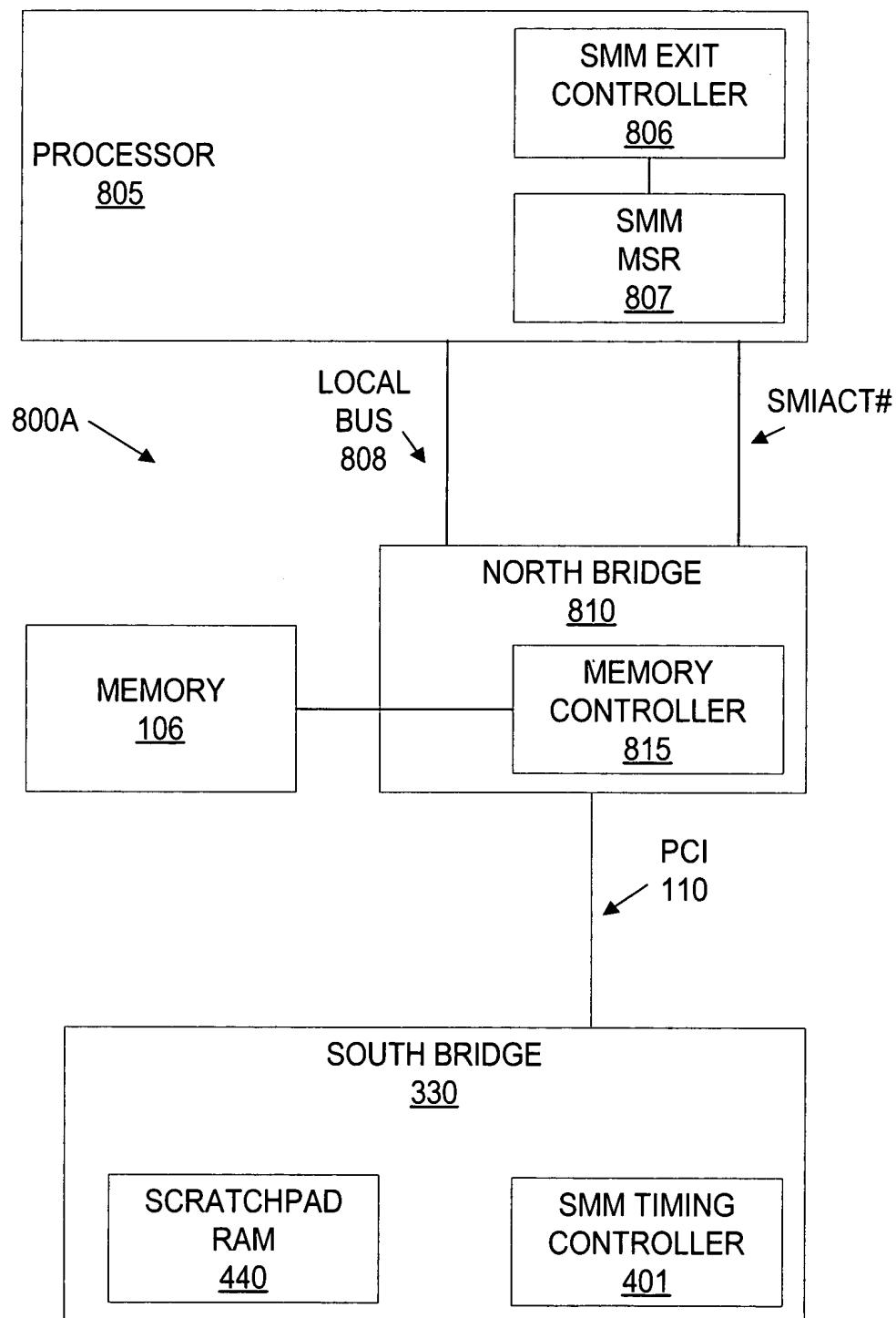


Fig. 9A

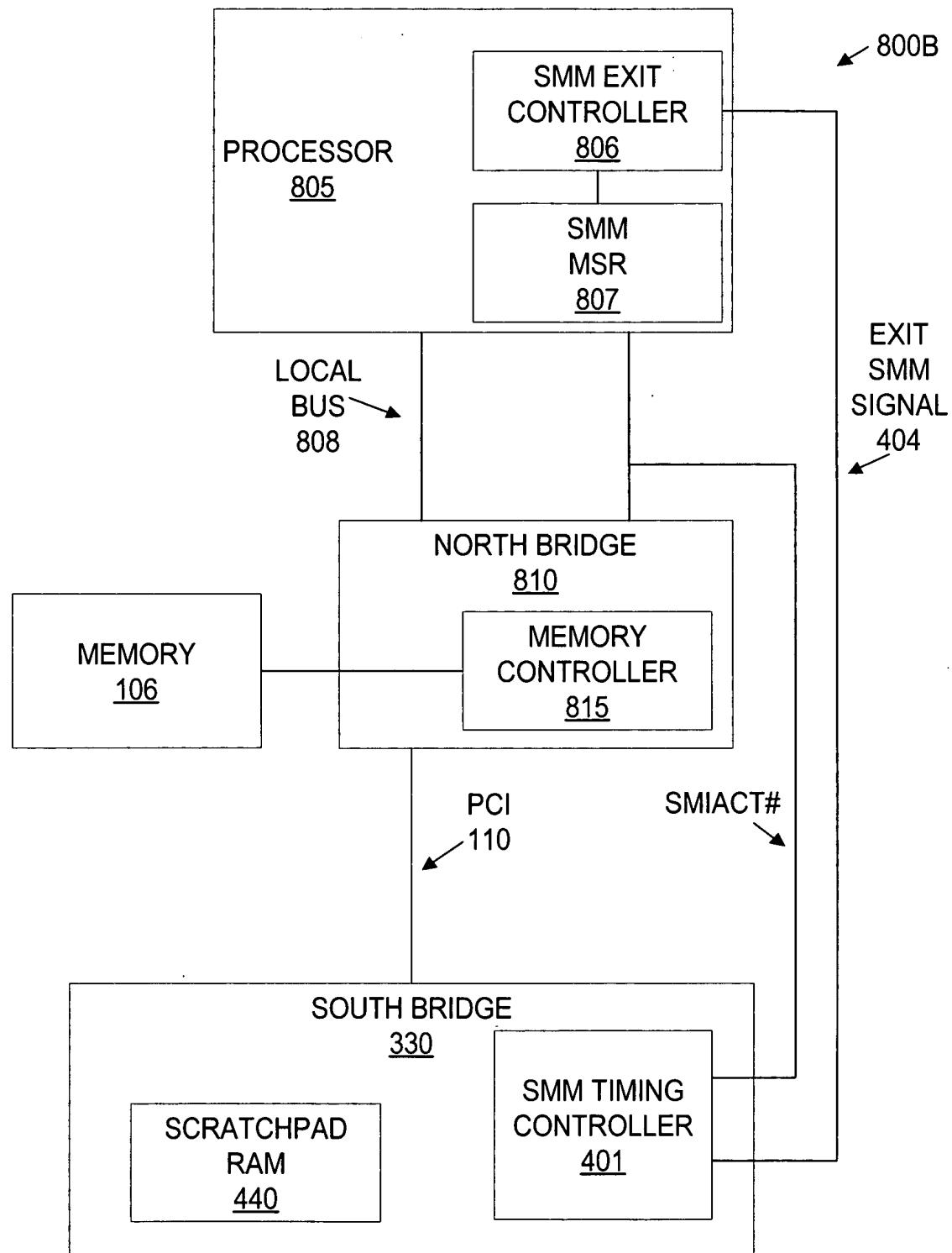


Fig. 9B

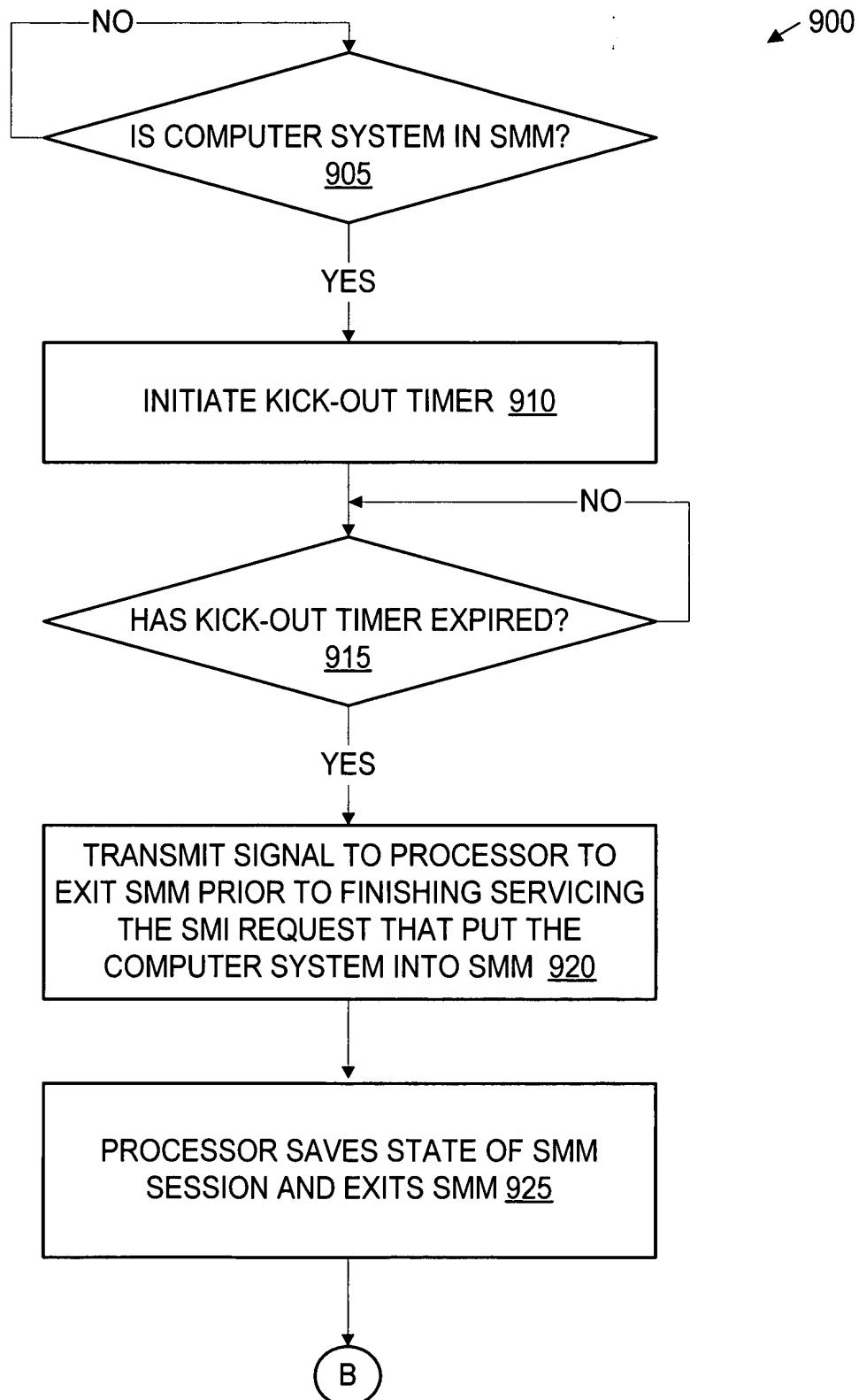


Fig. 10A

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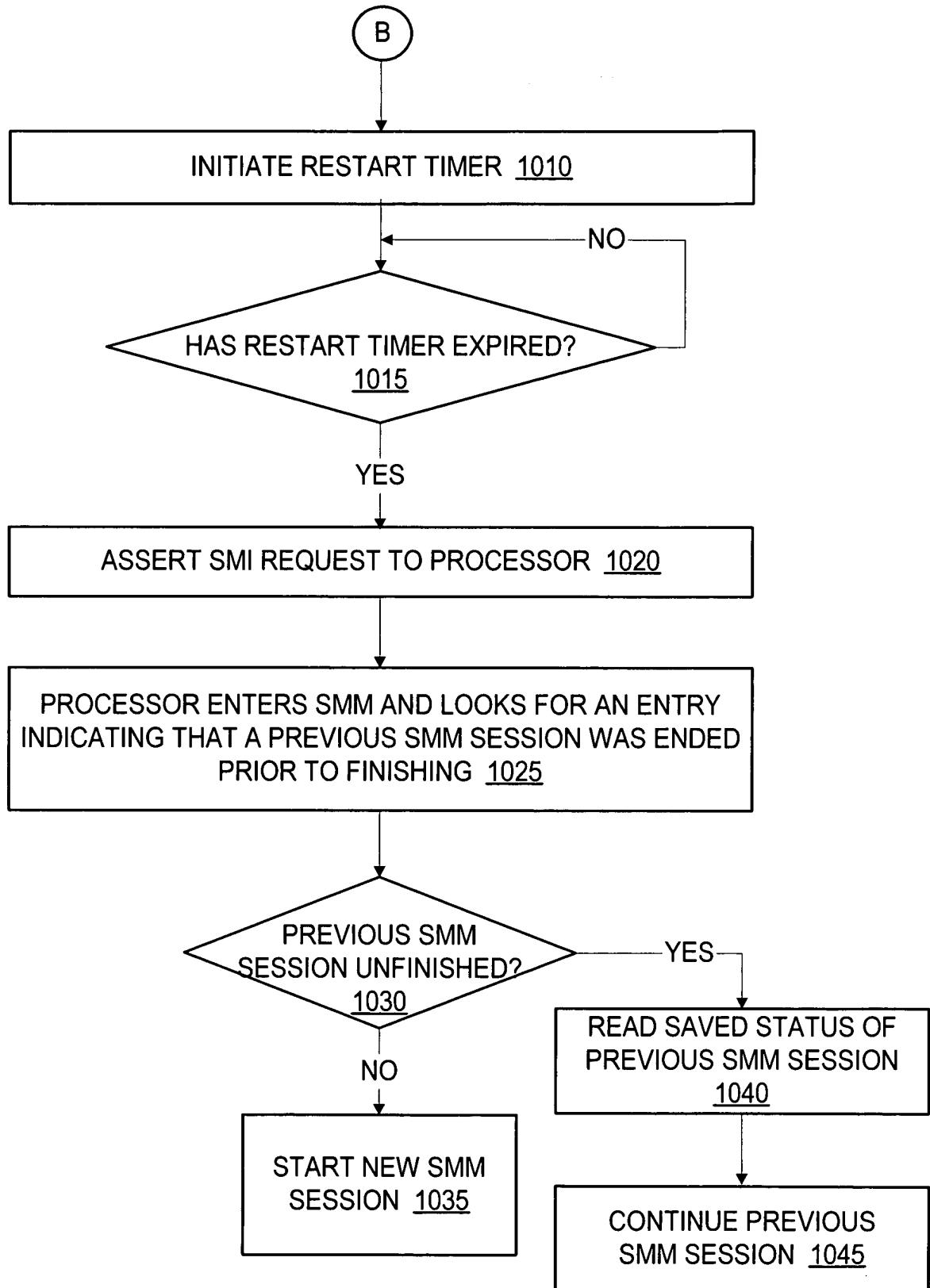


Fig. 10B

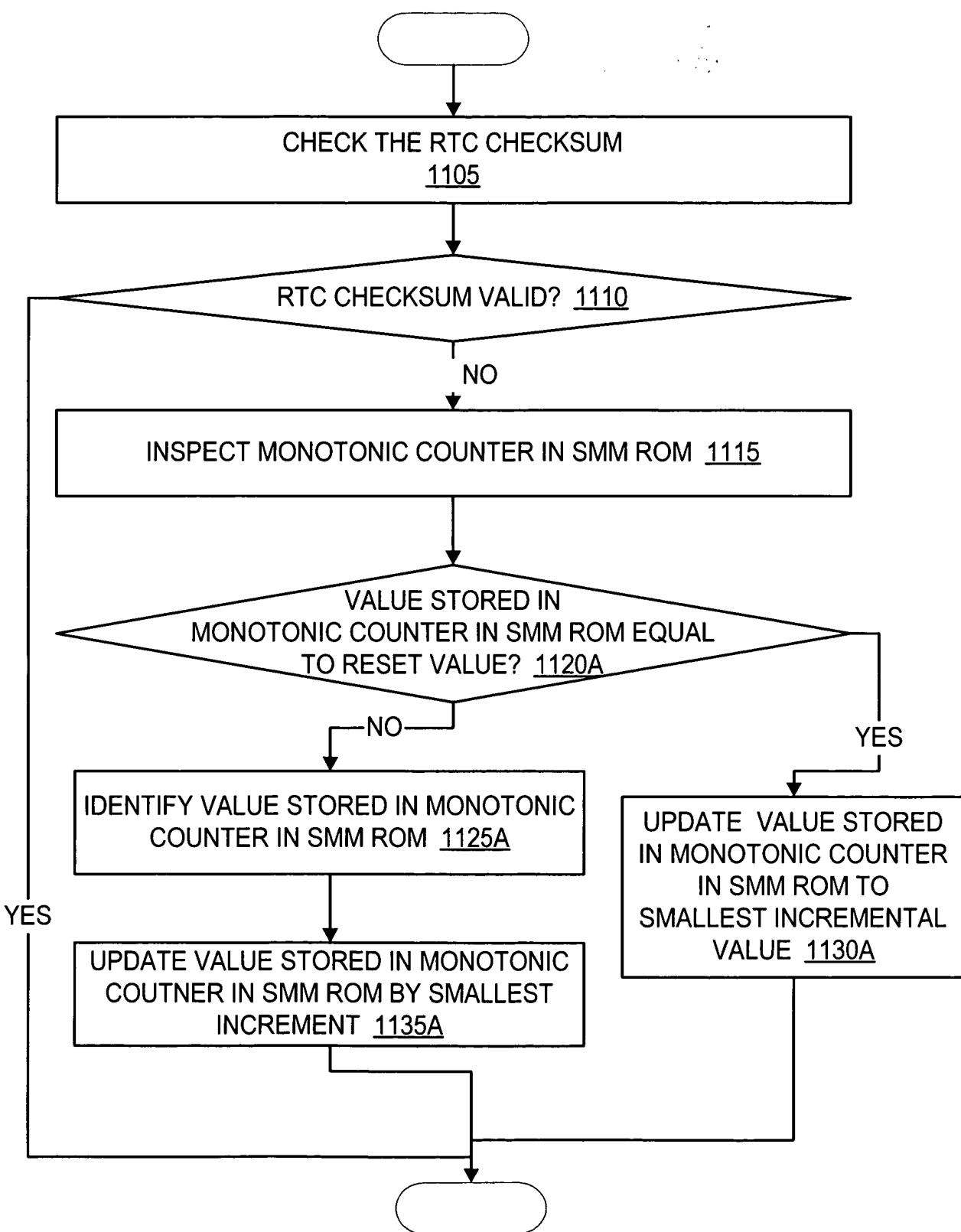


Fig. 11A

1100B

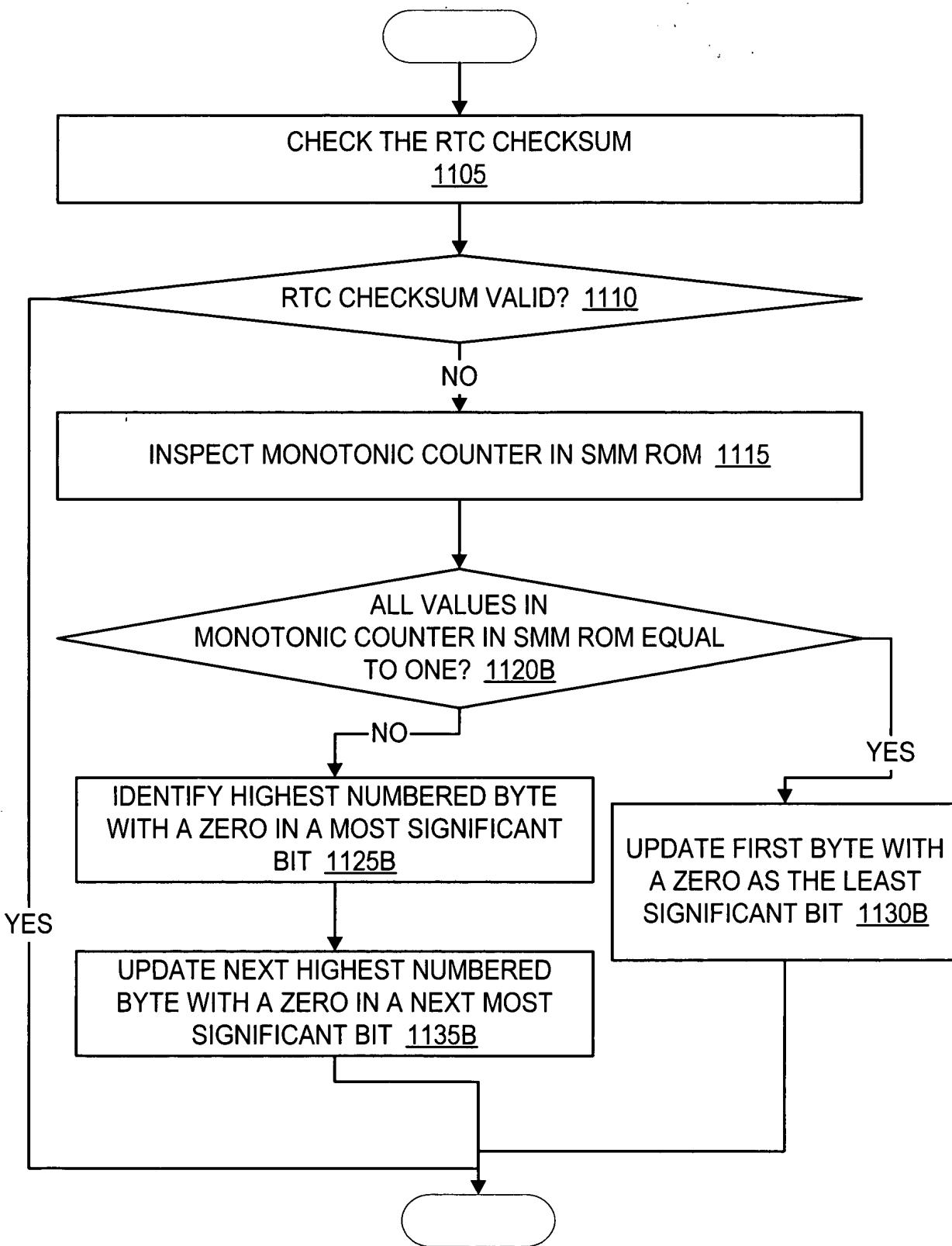


Fig. 11B

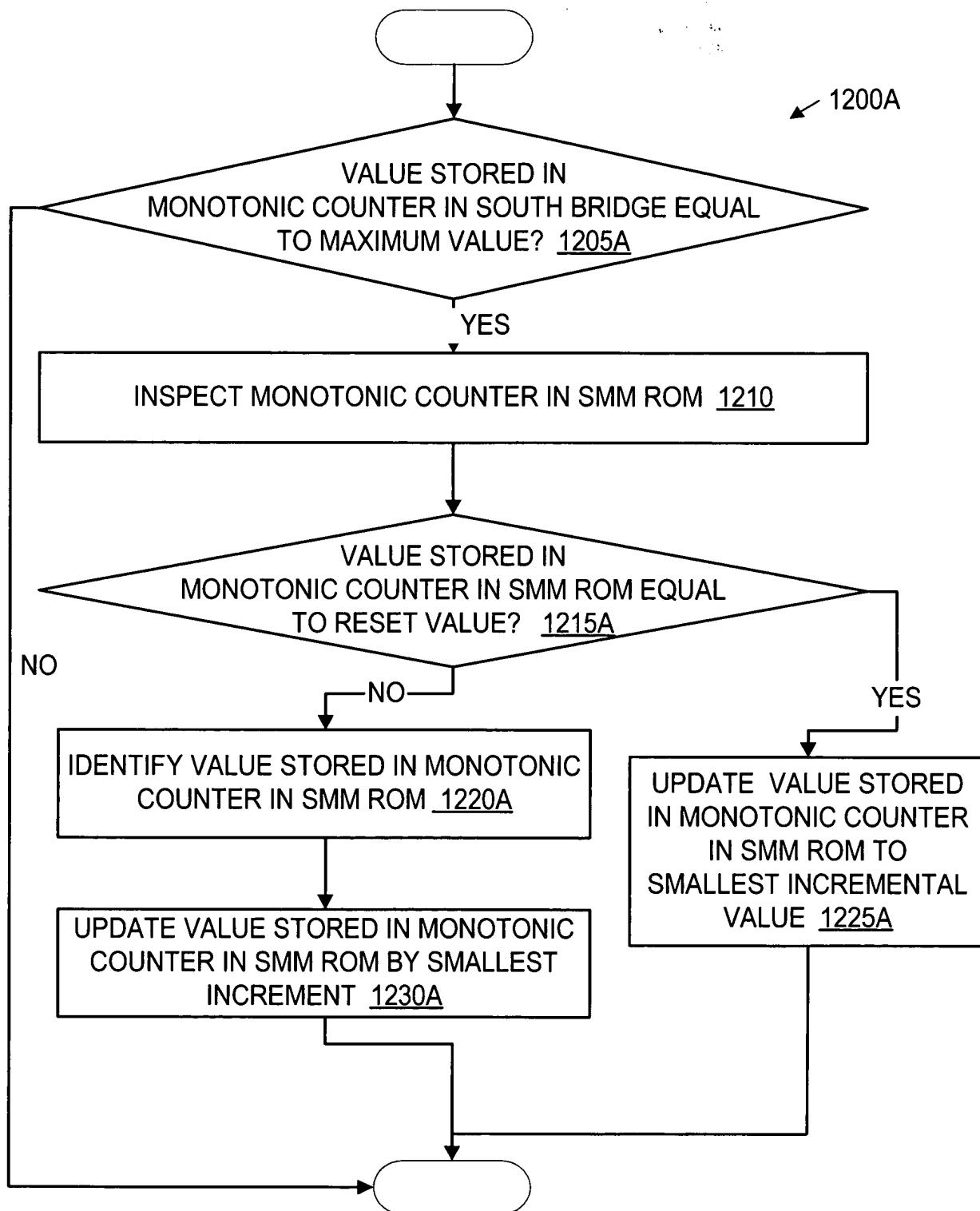


Fig. 12A

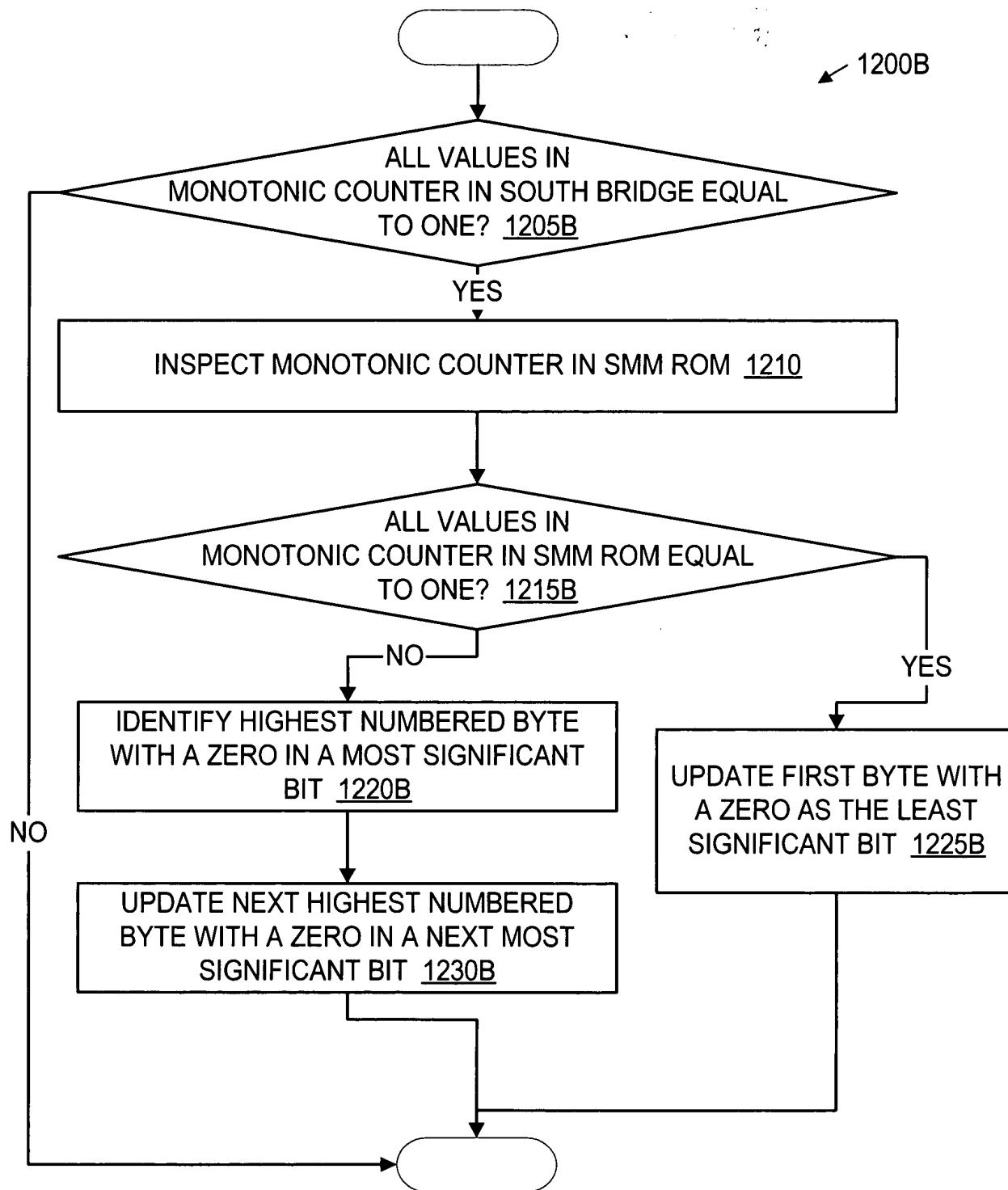


Fig. 12B

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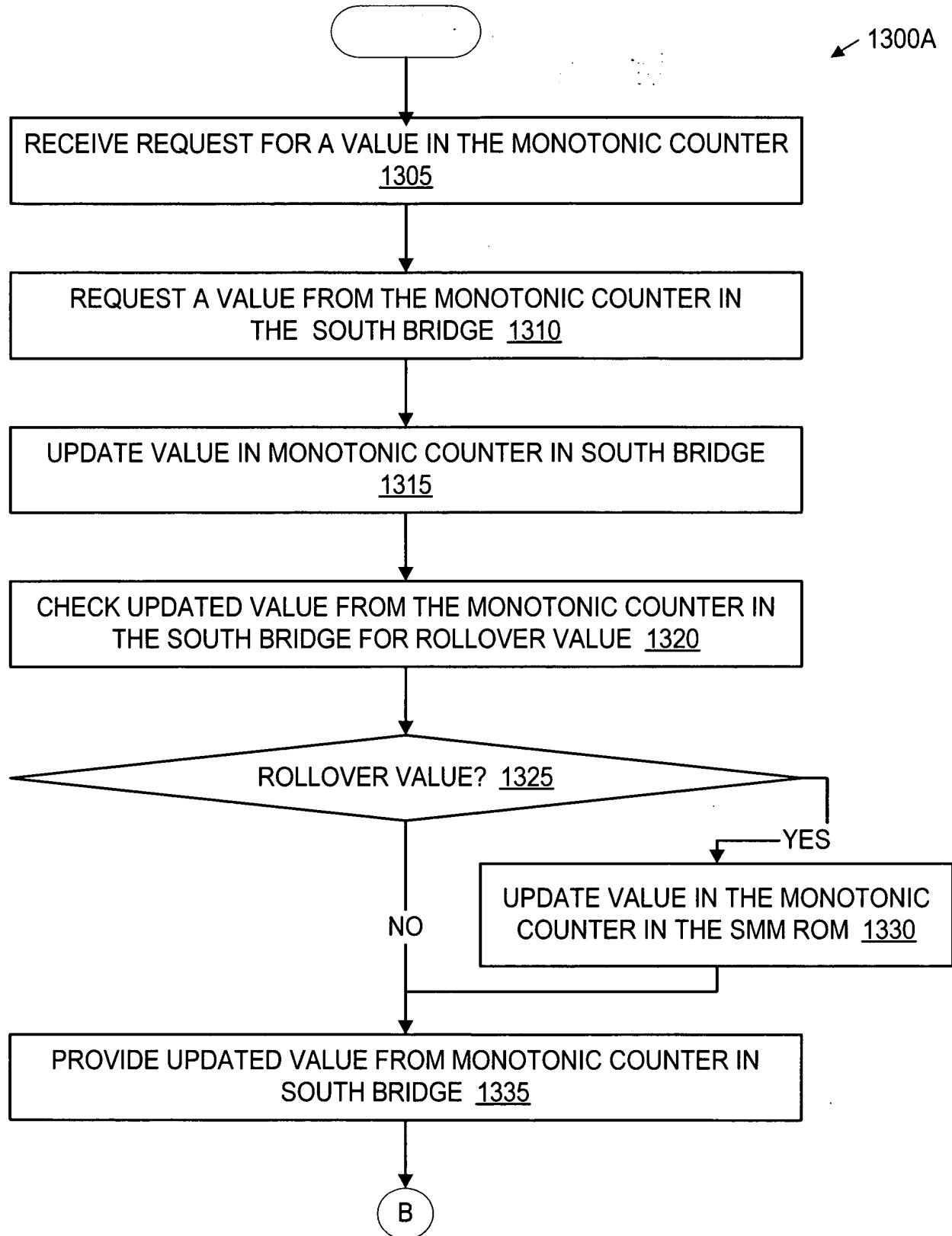


Fig. 13A

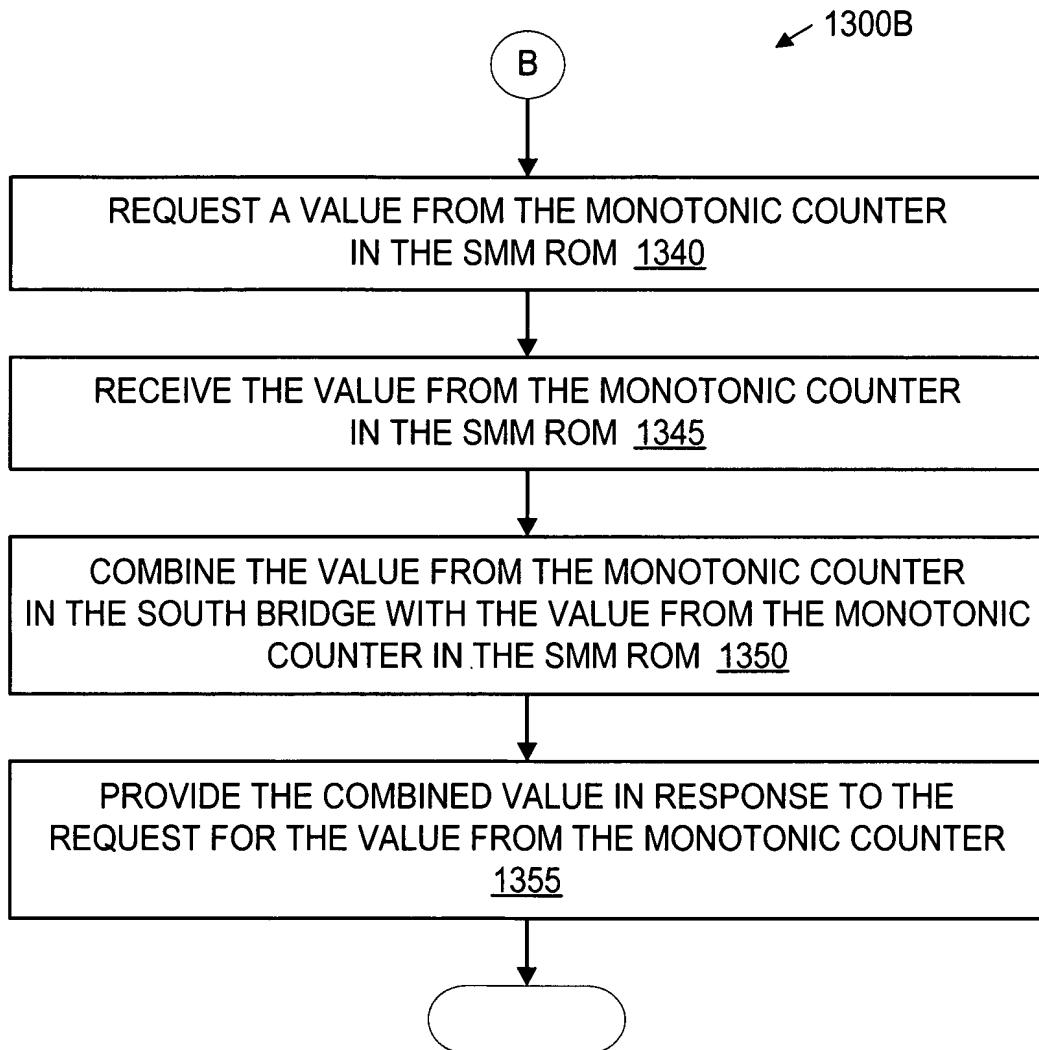


Fig. 13B

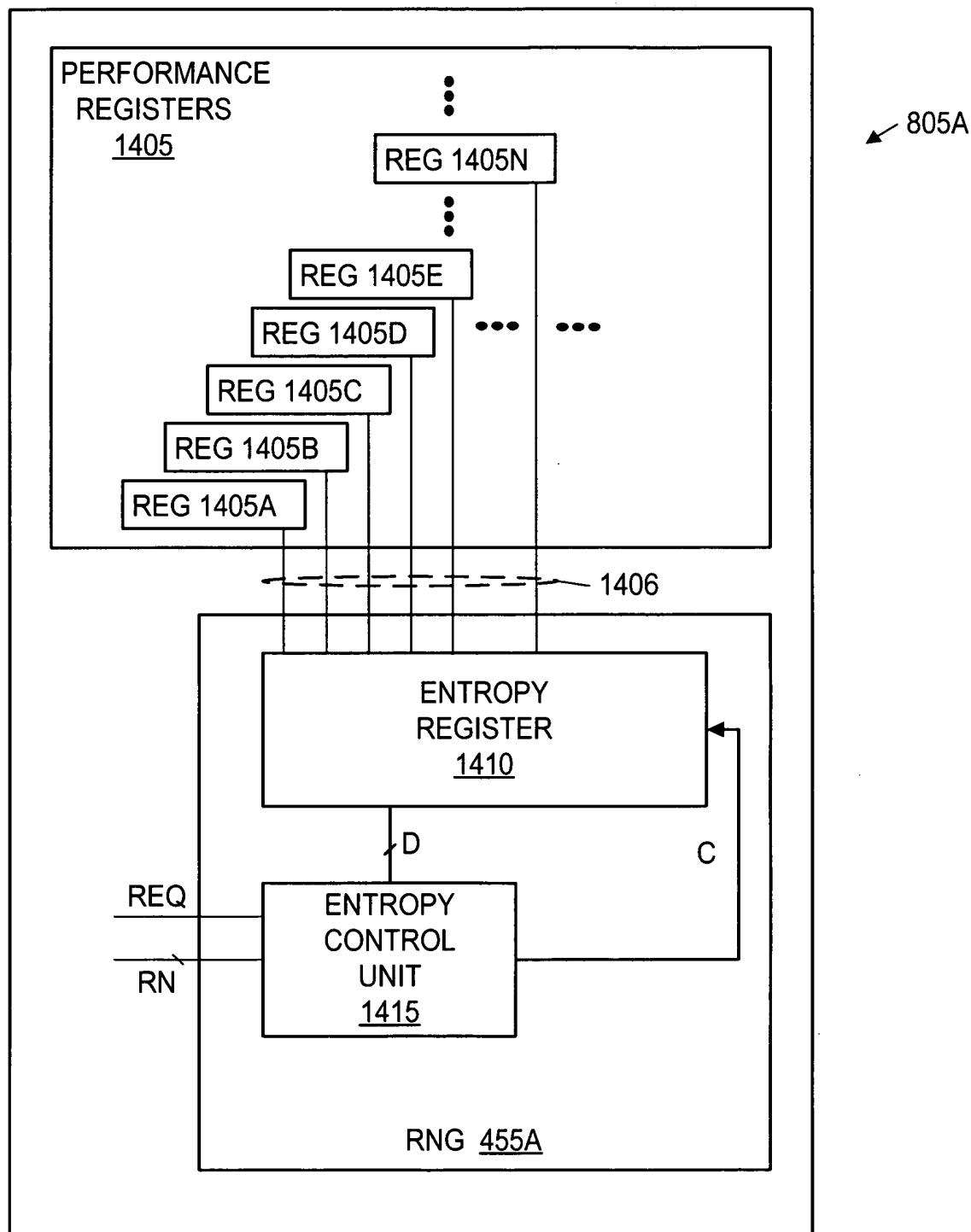


Fig. 14A

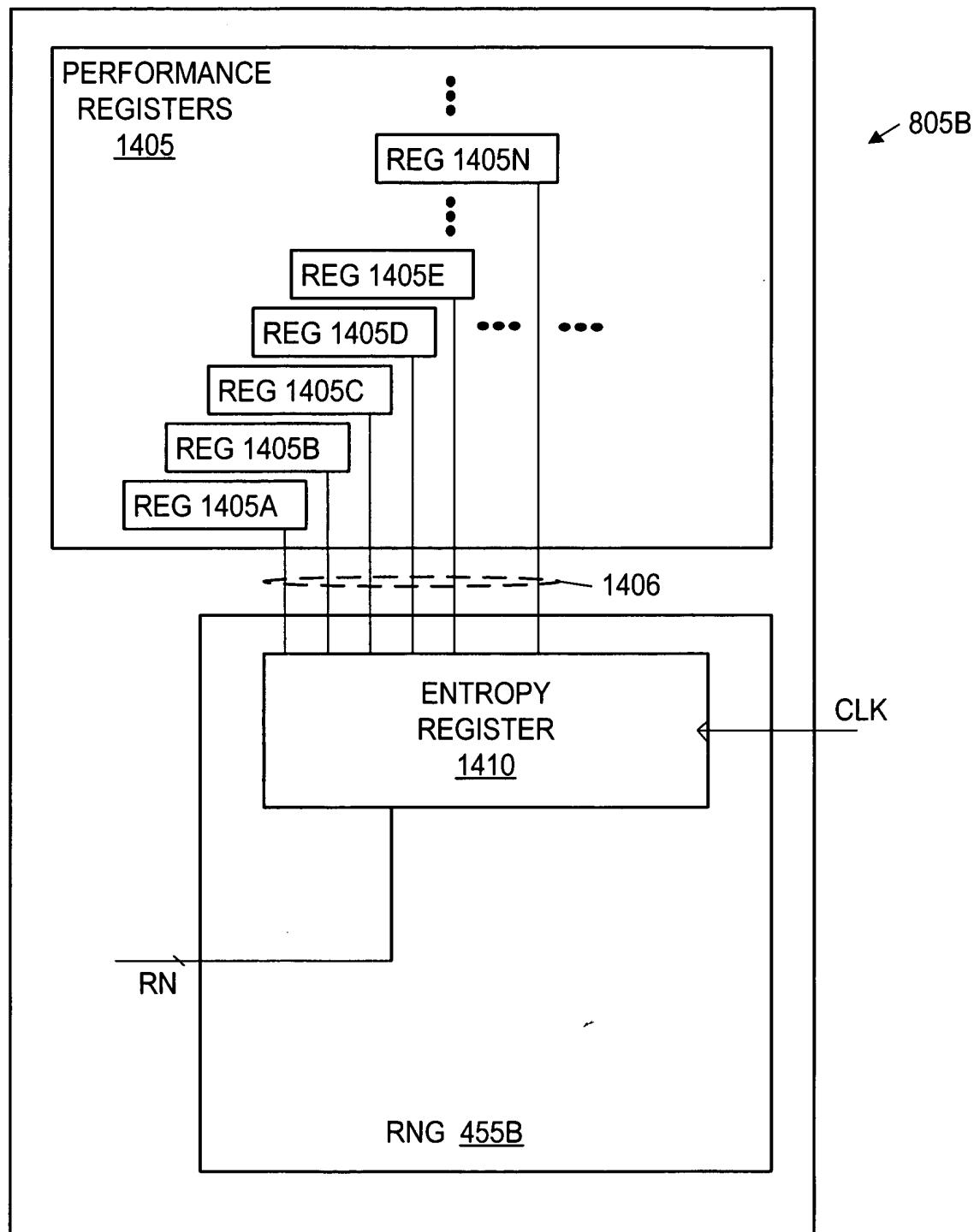


Fig. 14B

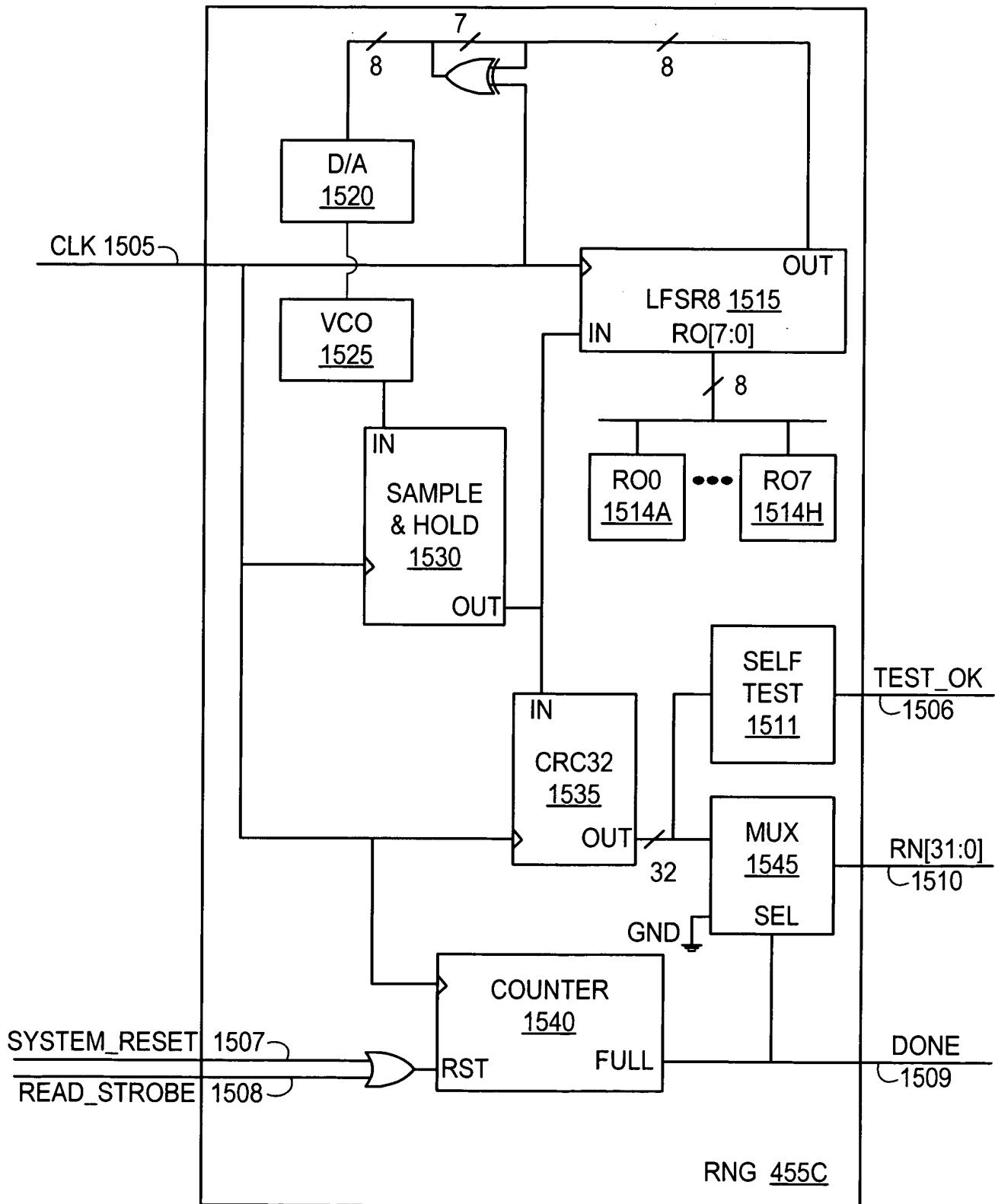


Fig. 15

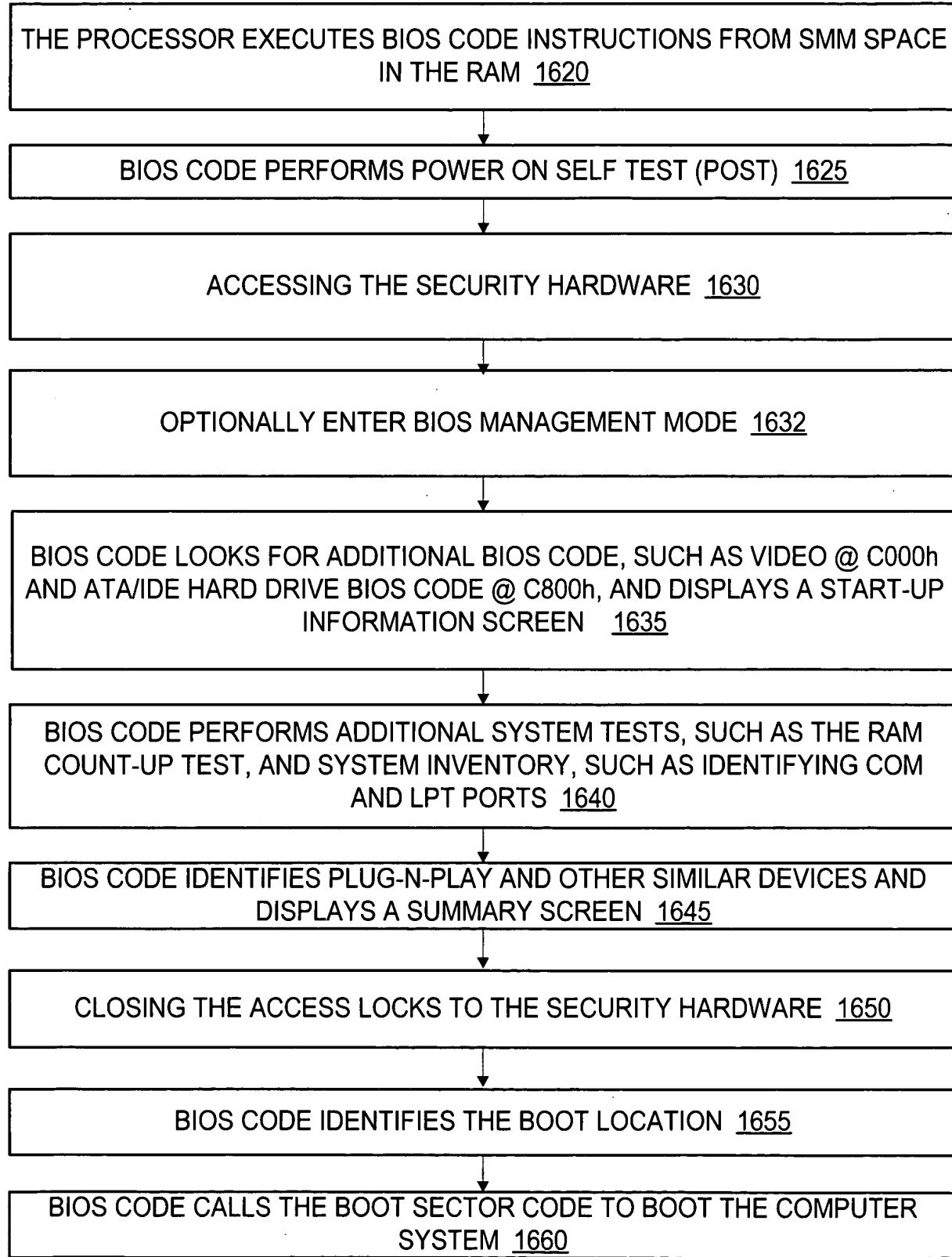


Fig. 16A

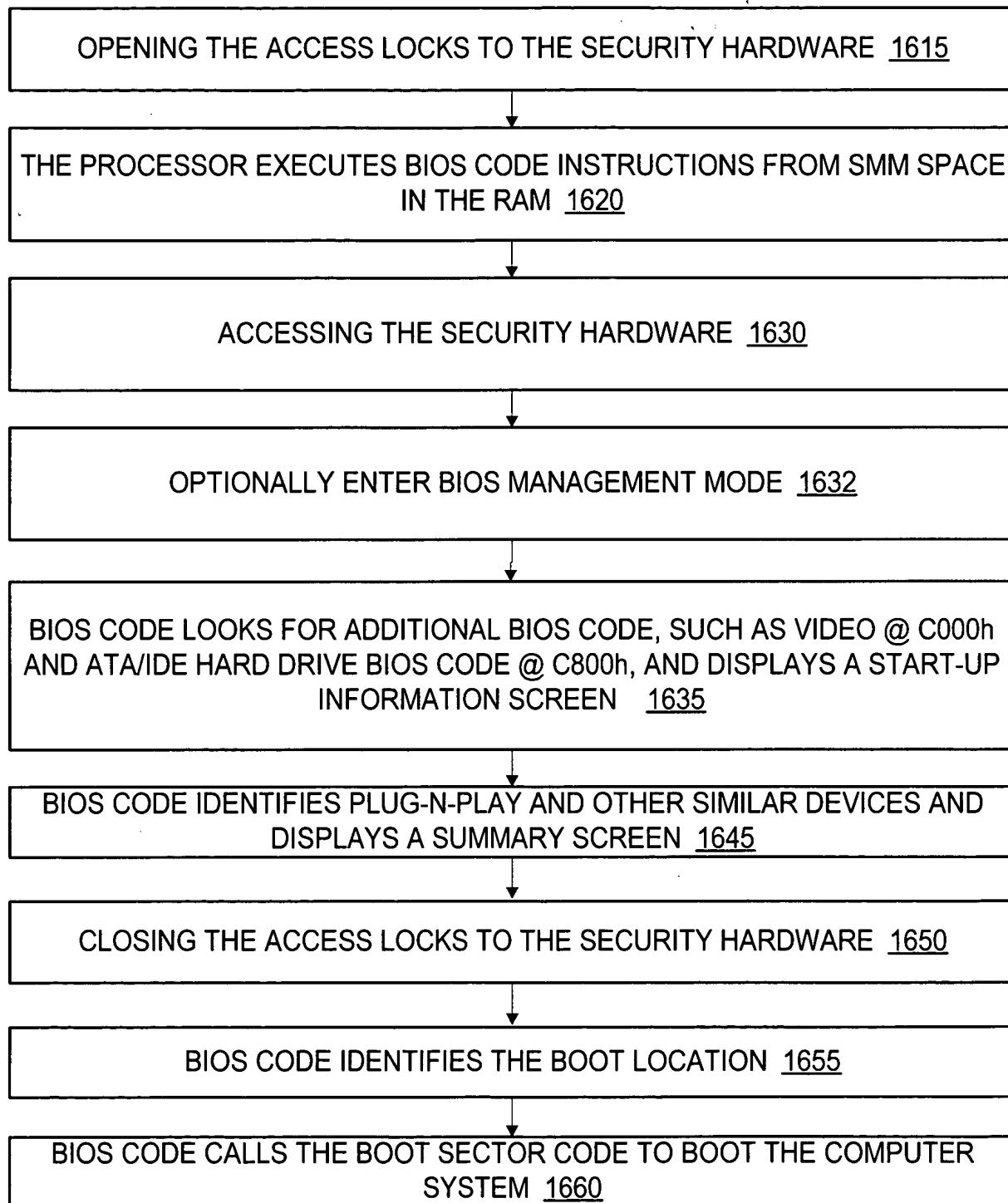


Fig. 16B

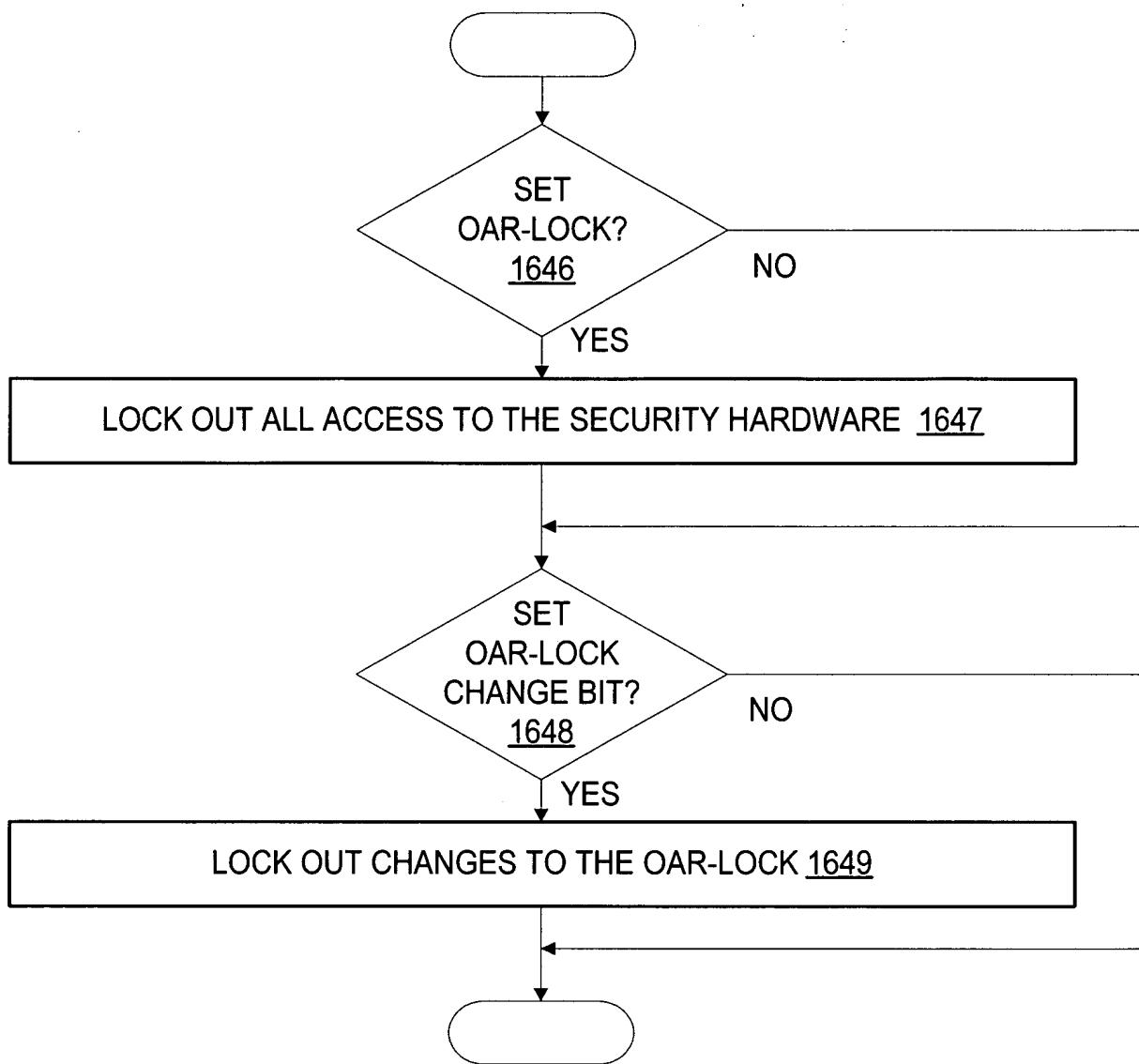


Fig. 16C

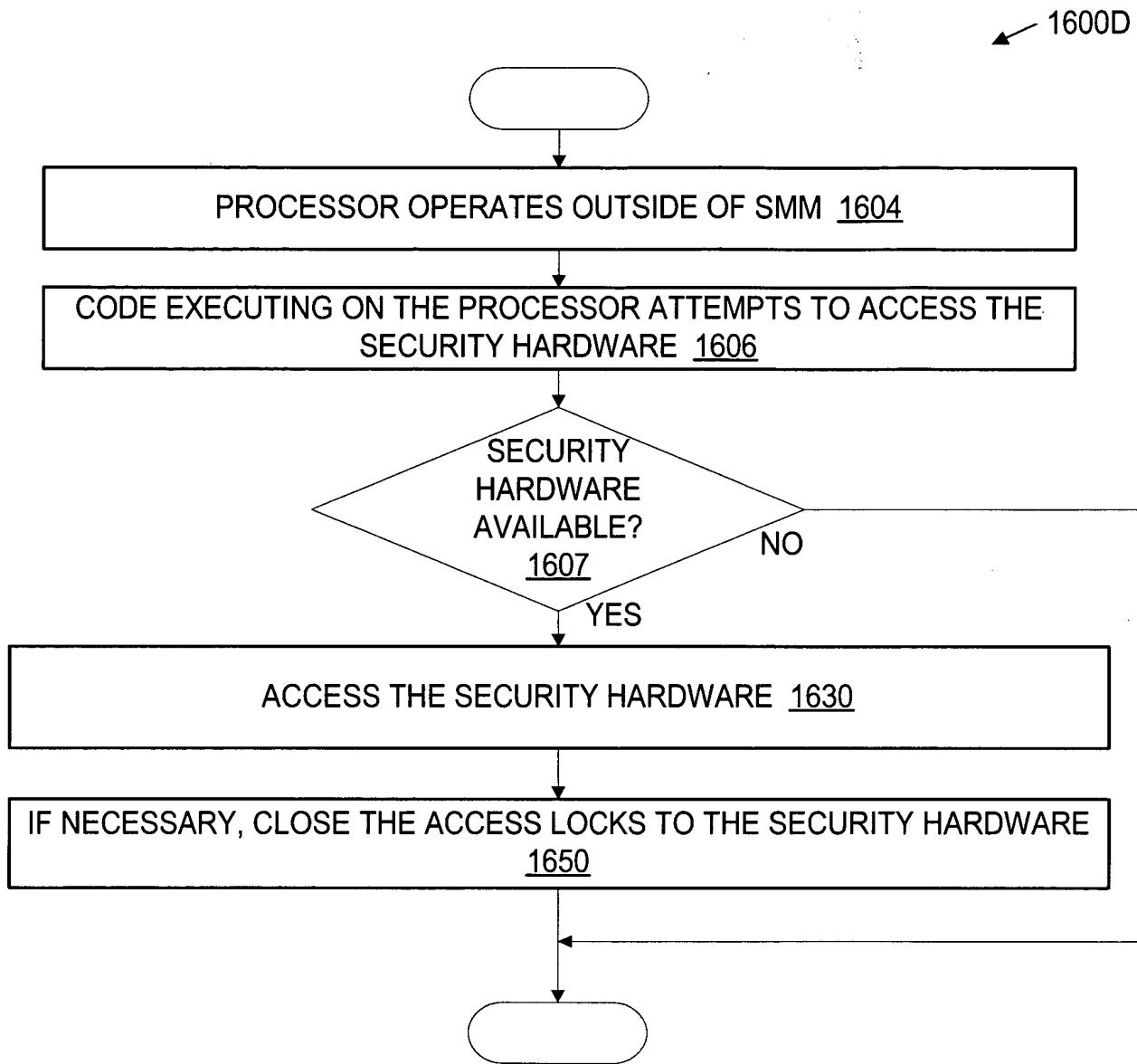
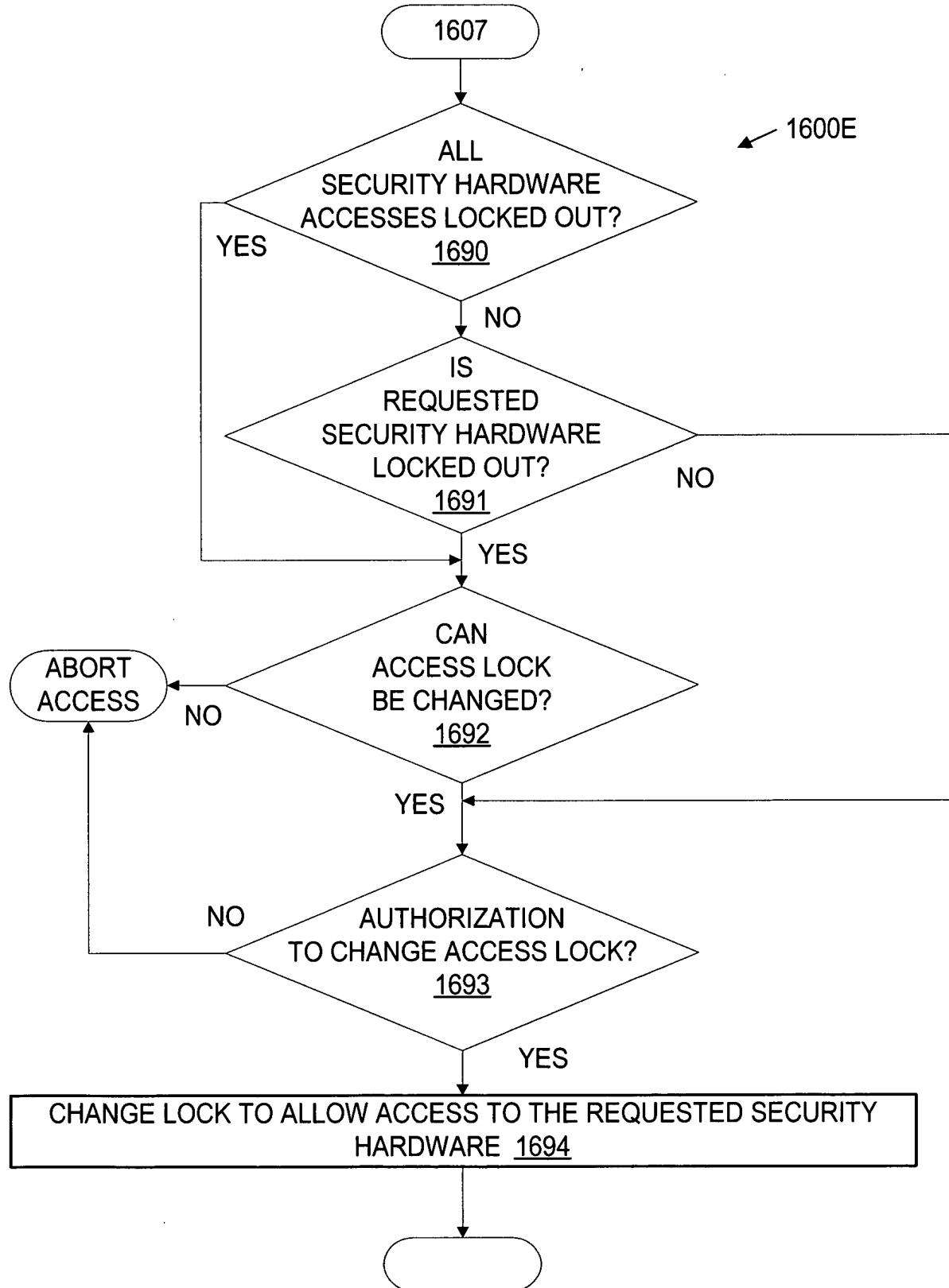


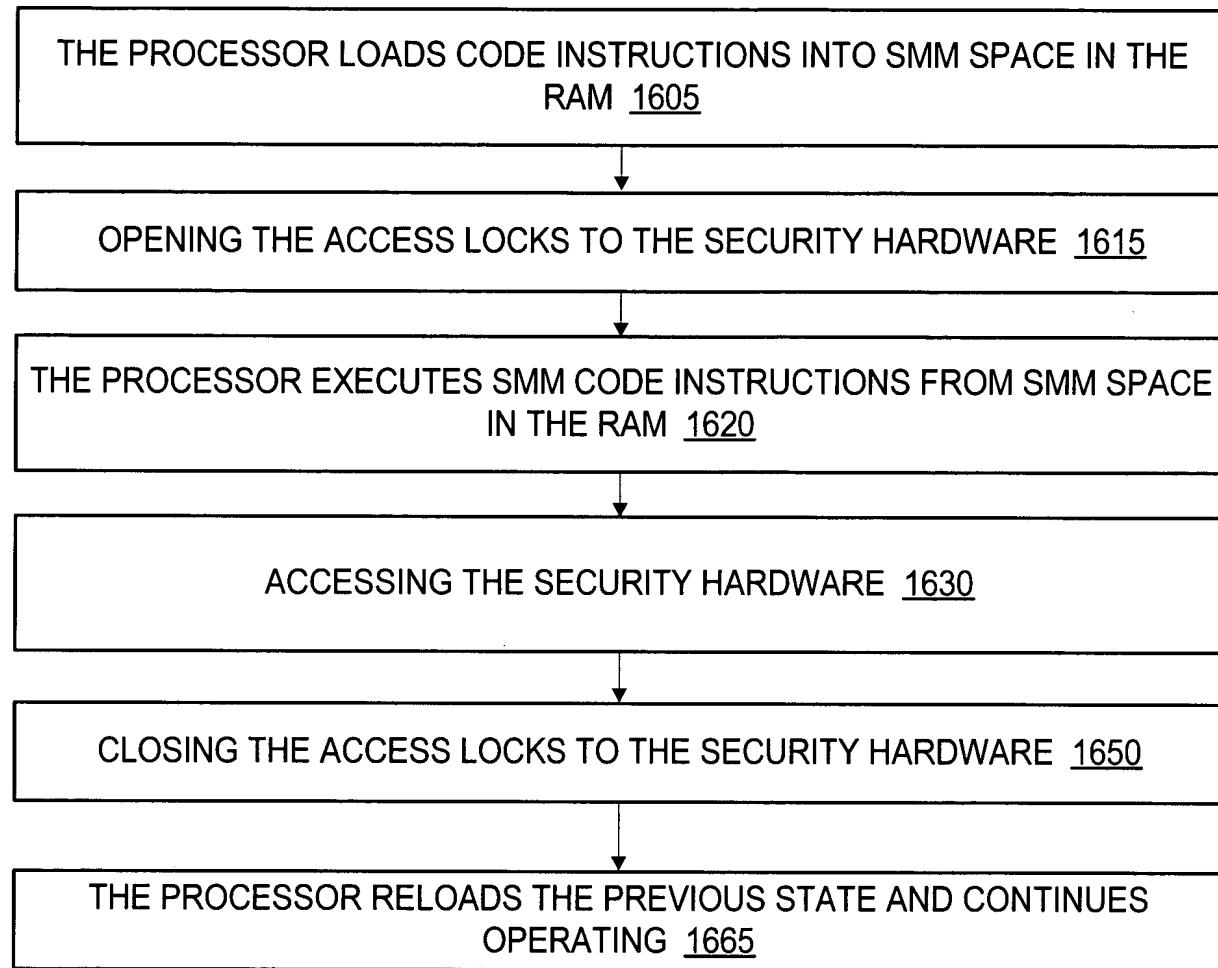
Fig. 16D

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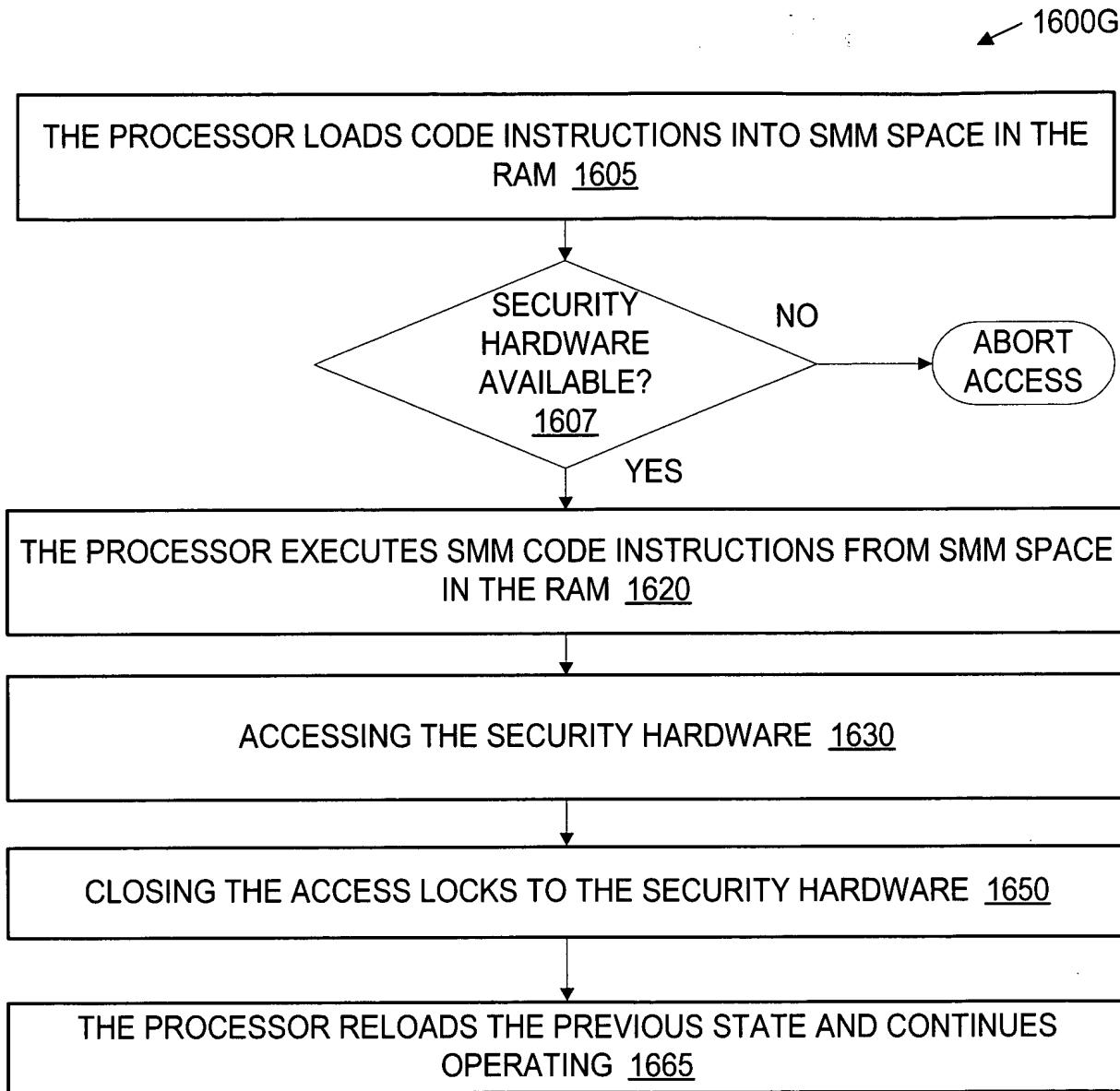


**Fig. 16E**

1600F

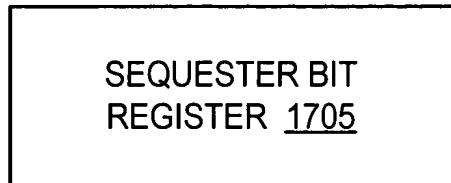


**Fig. 16F**



1600G

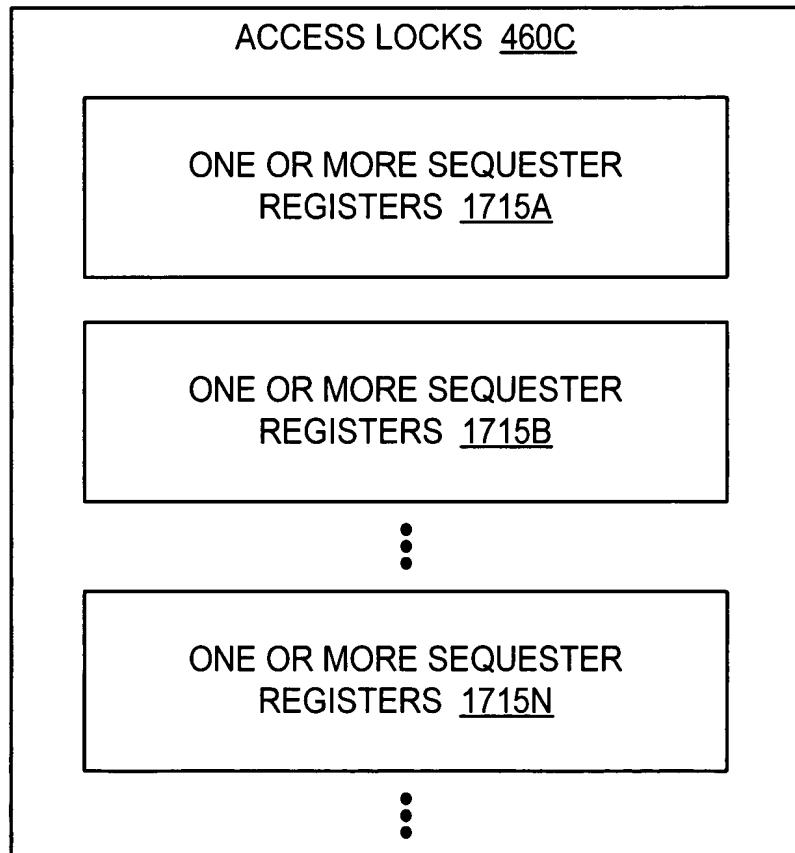
**Fig. 16G**



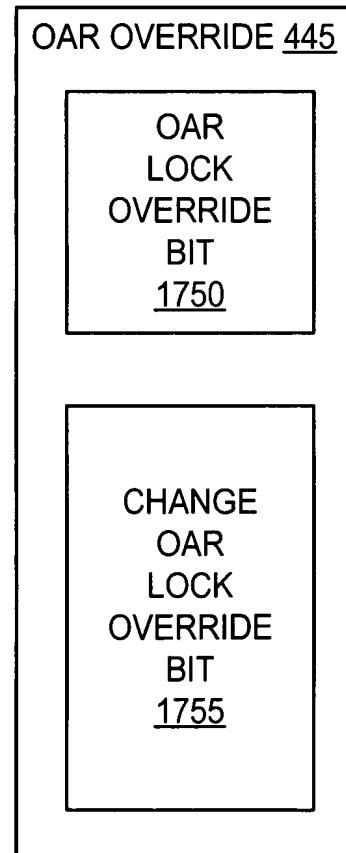
**Fig. 17A**



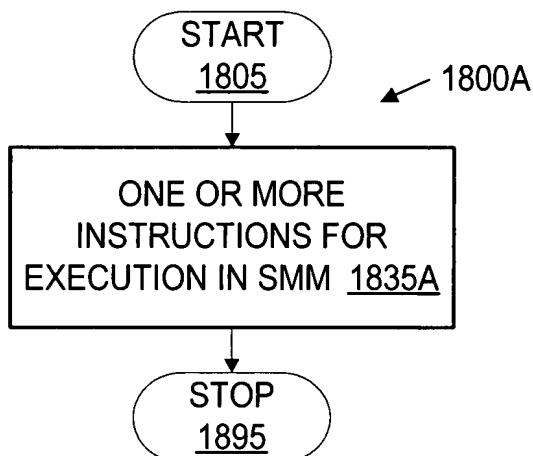
**Fig. 17B**



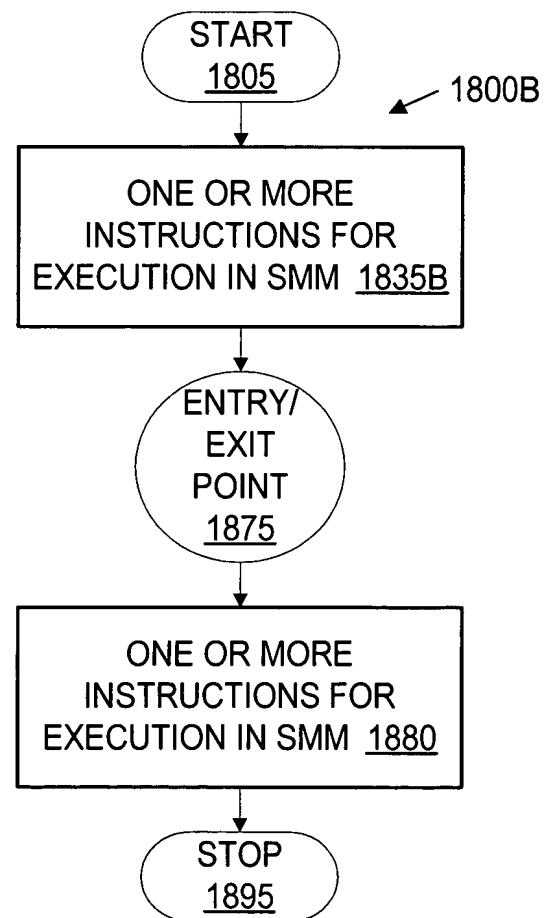
**Fig. 17C**



**Fig. 17D**



**Fig. 18A  
PRIOR ART**



**Fig. 18B**

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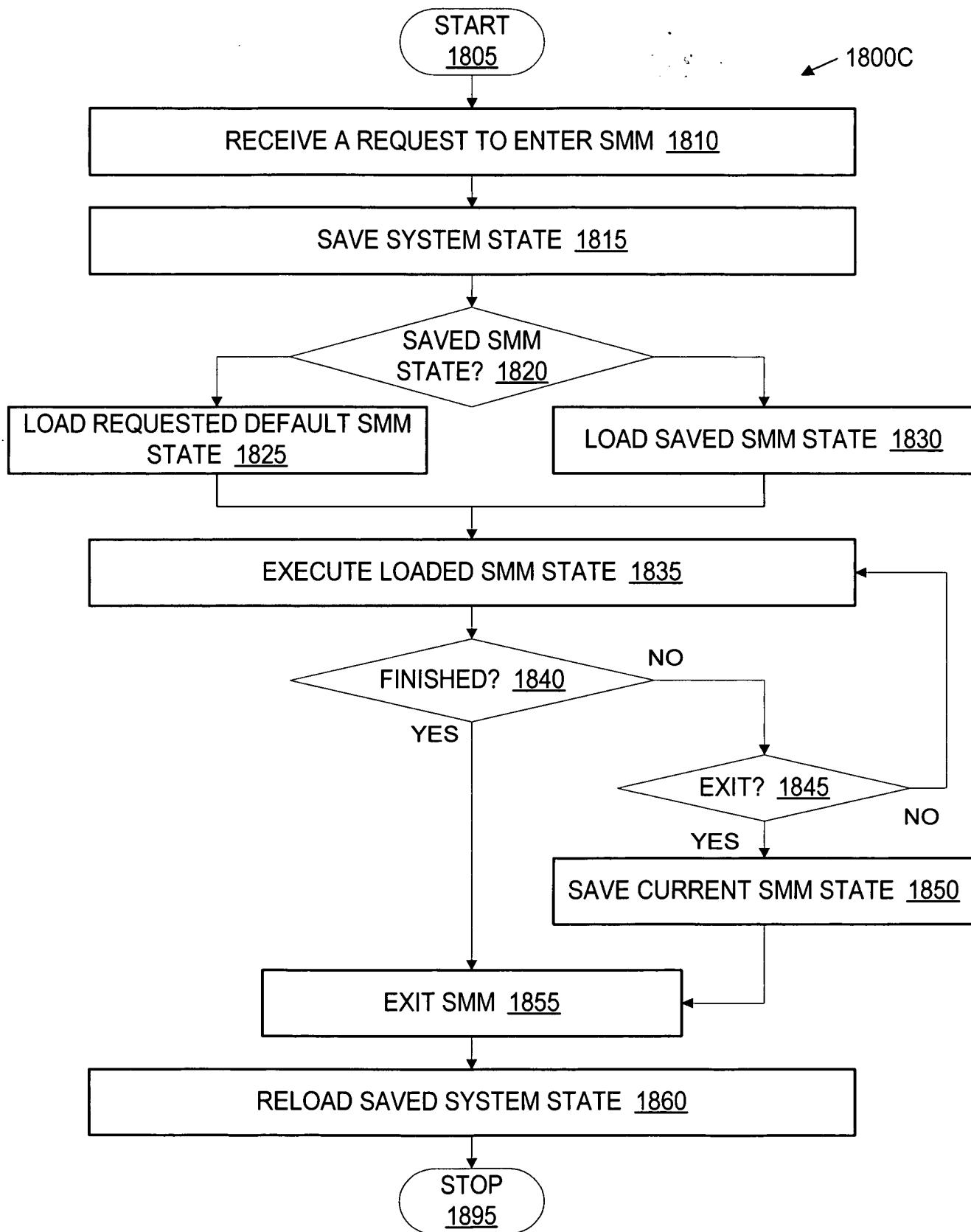


Fig. 18C

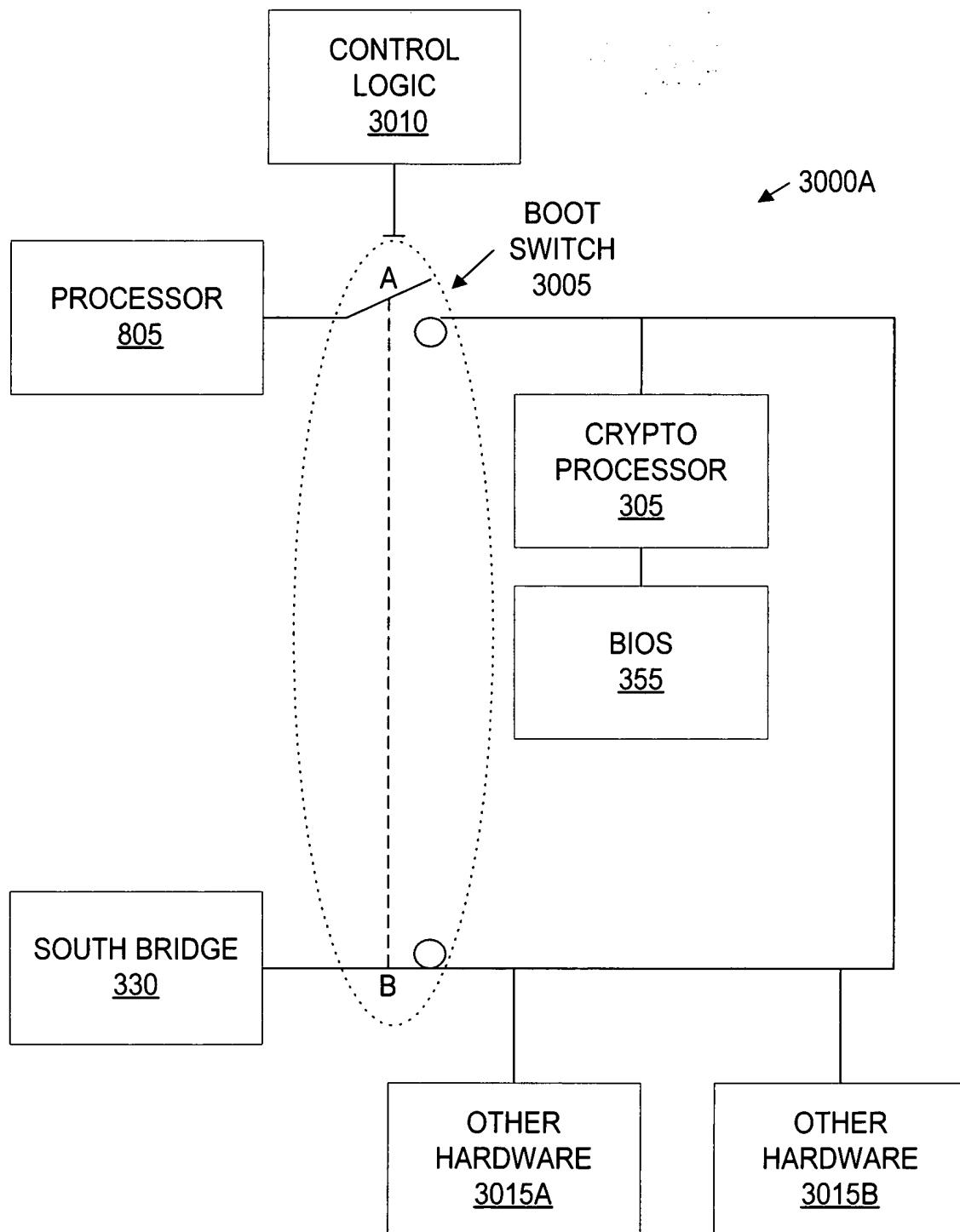


Fig. 19A

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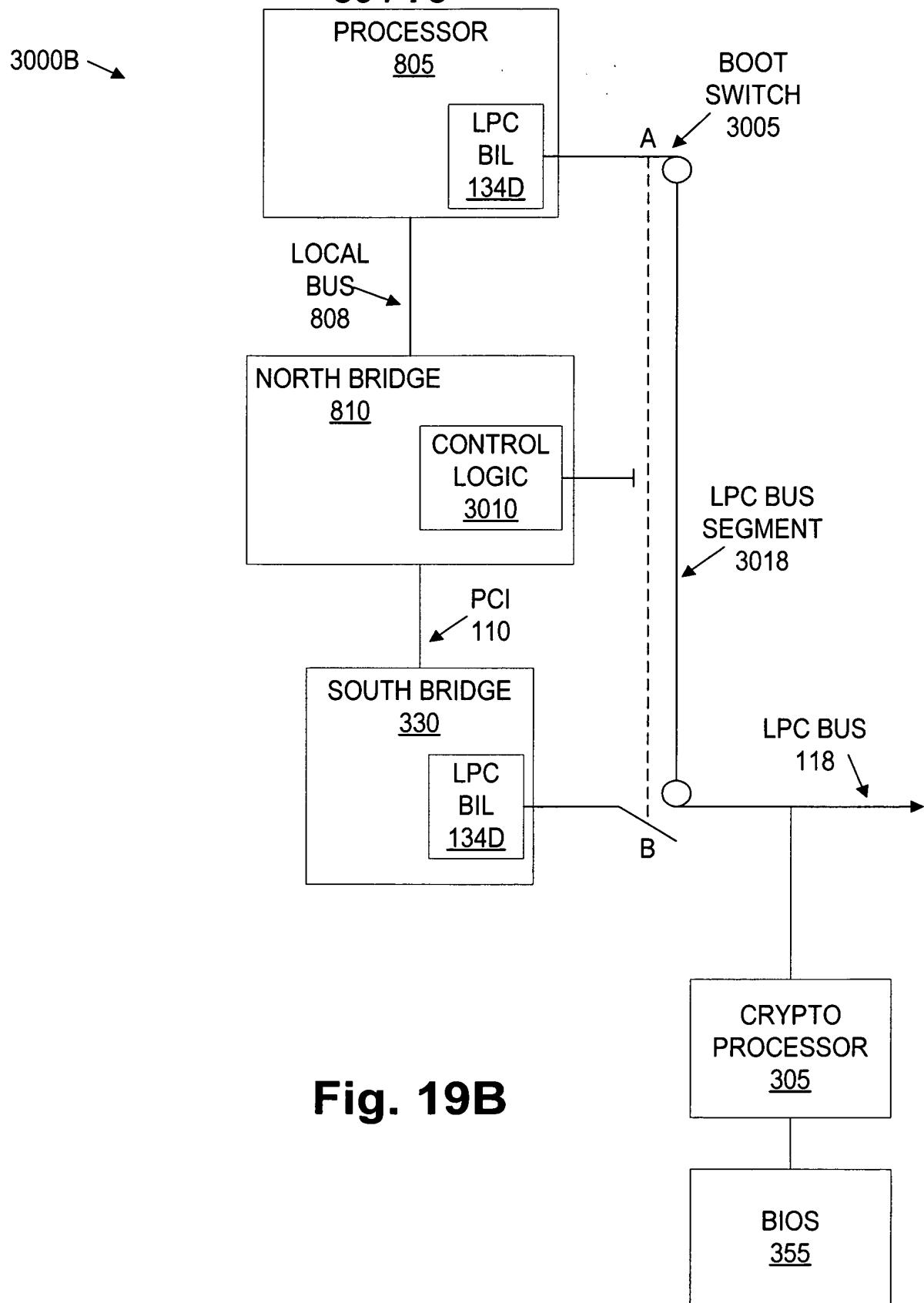
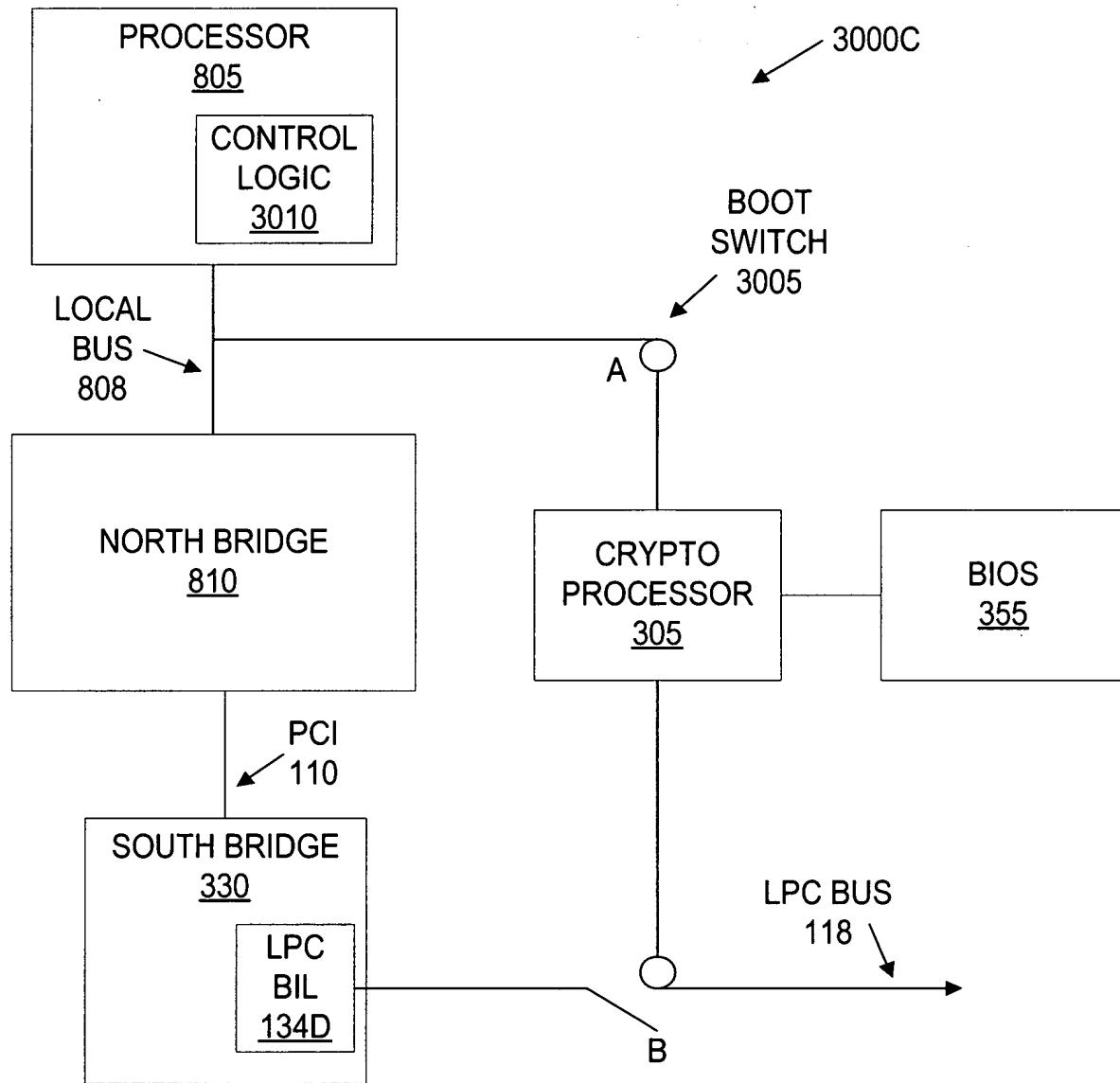


Fig. 19B

**Fig. 19C**

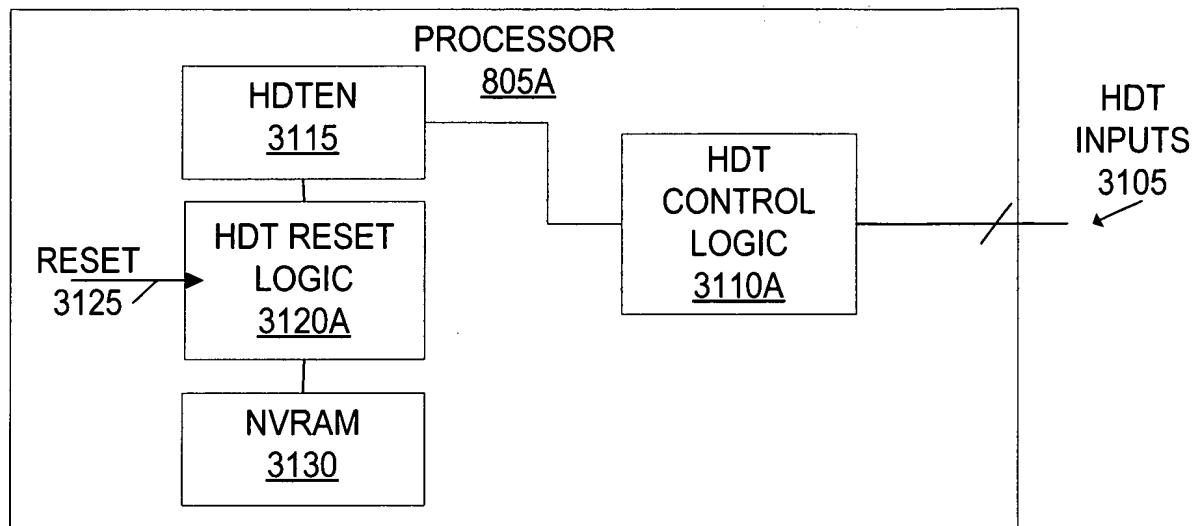


Fig. 20A

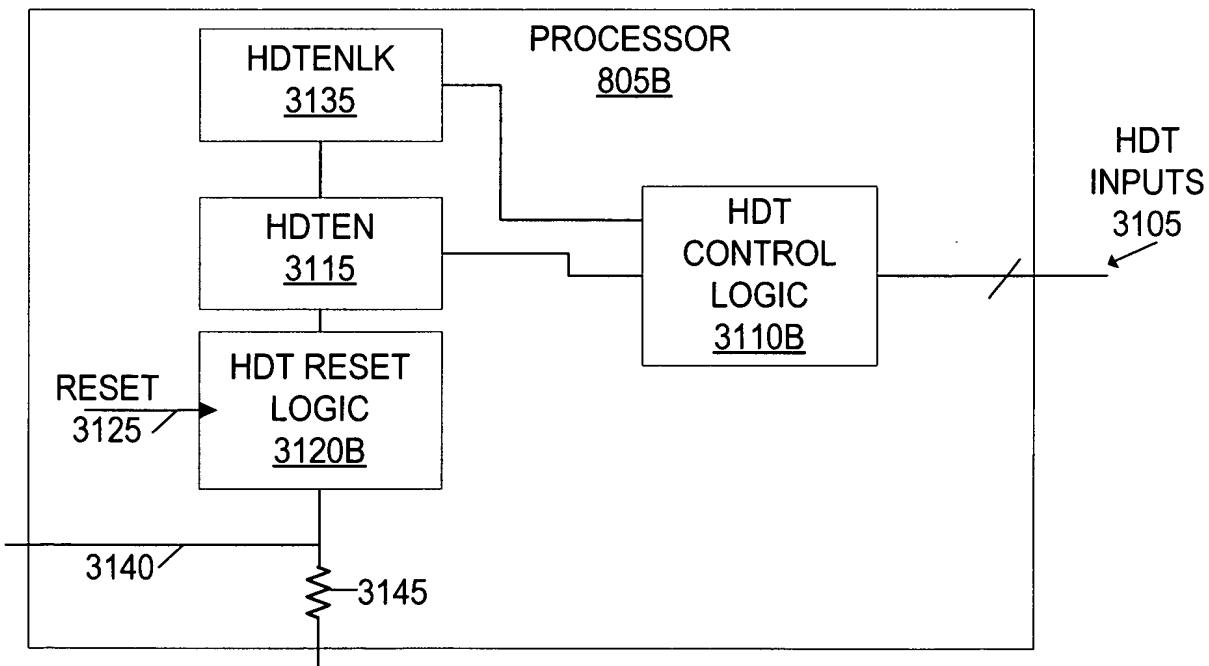


Fig. 20B

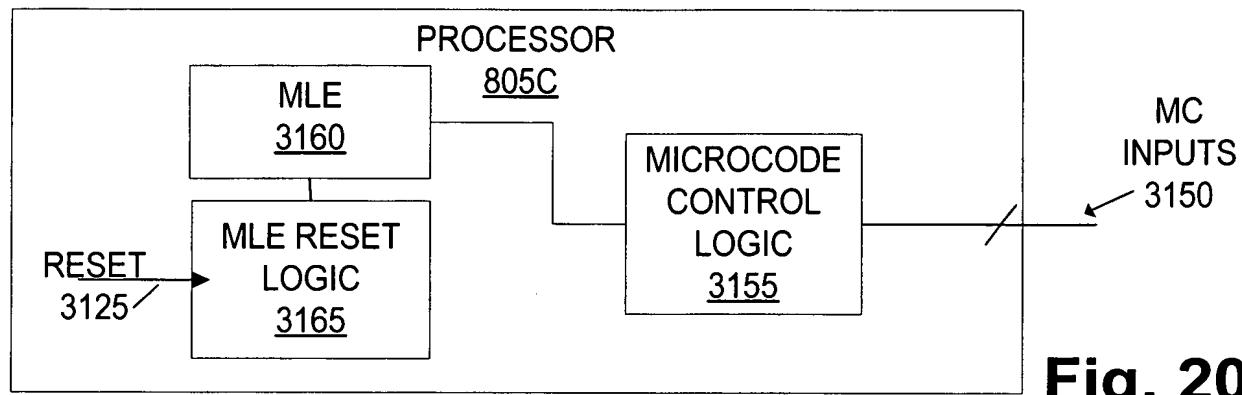


Fig. 20C

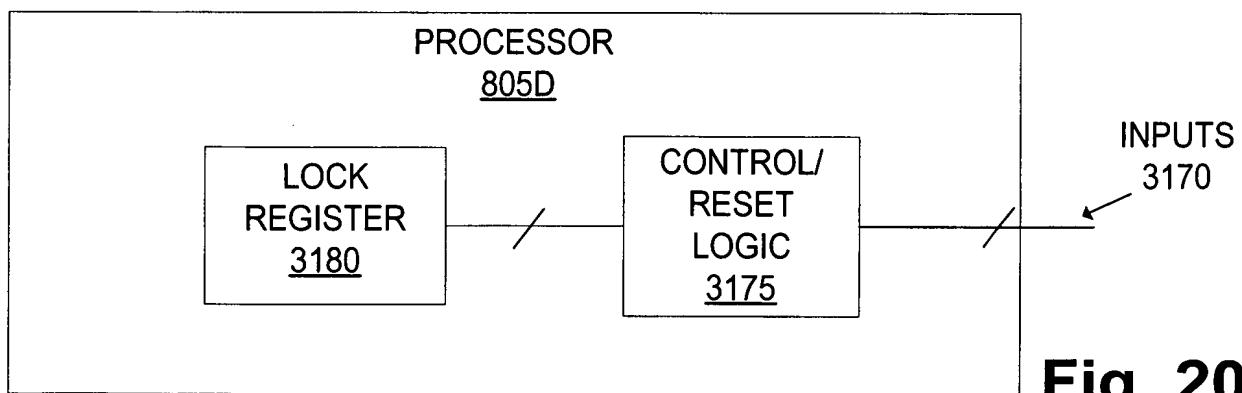
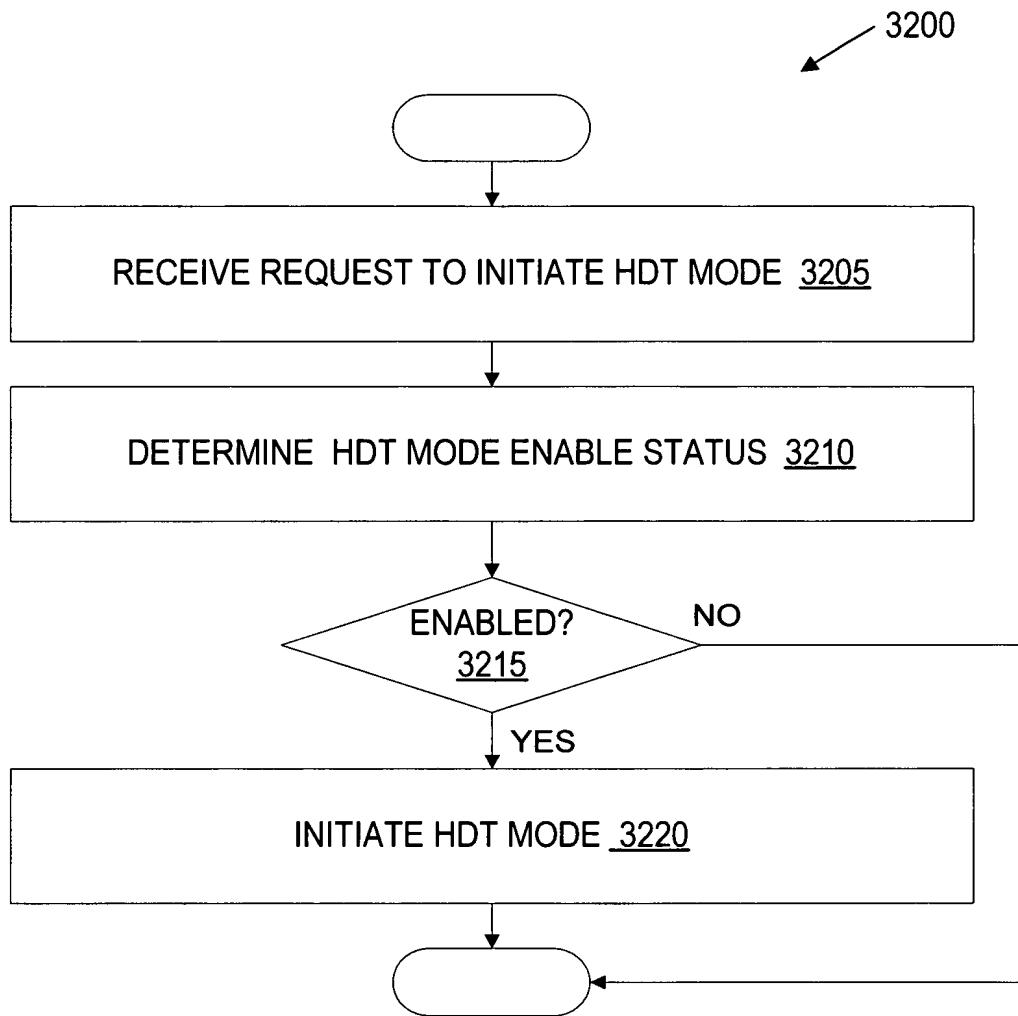


Fig. 20D



**Fig. 21**

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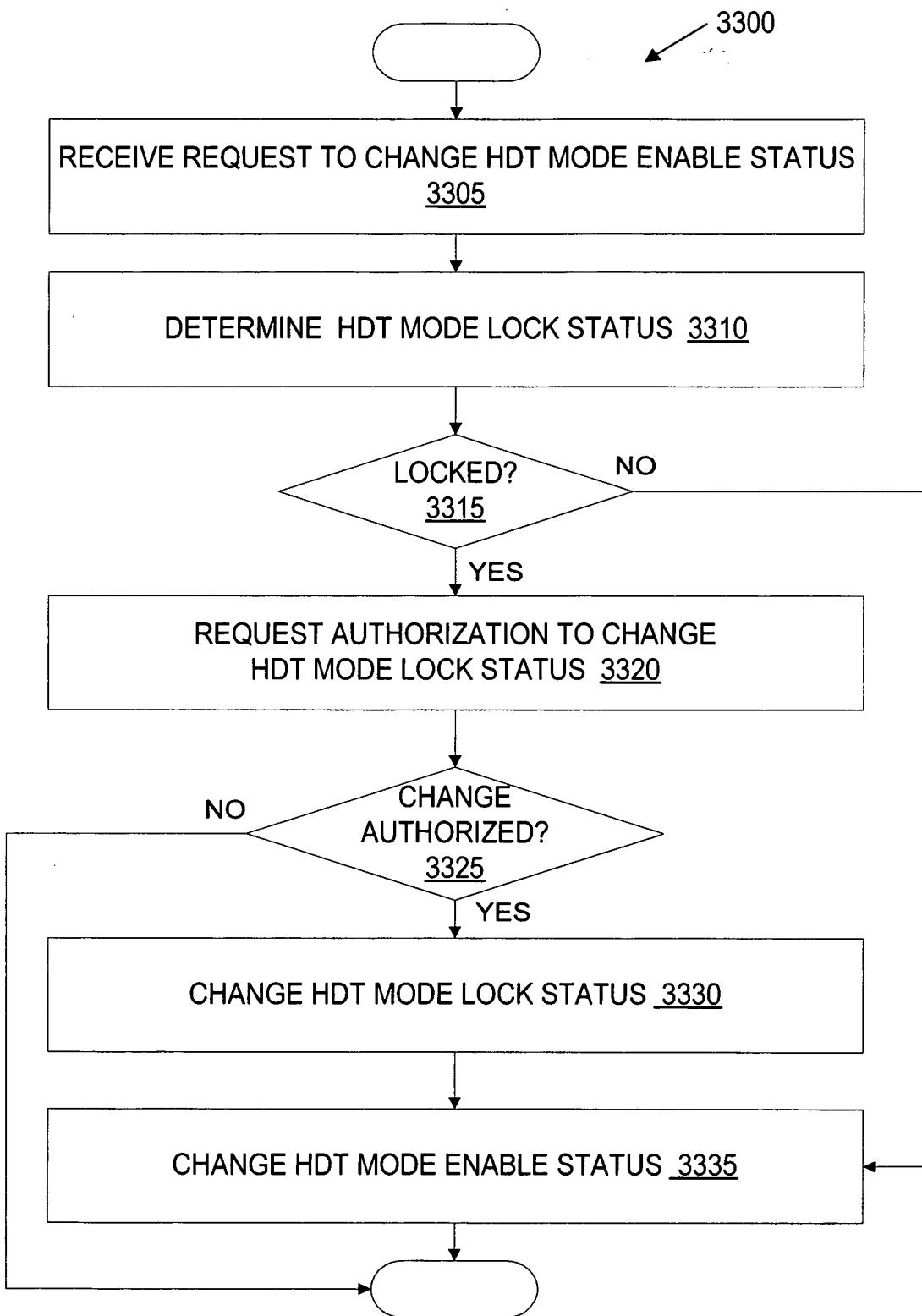
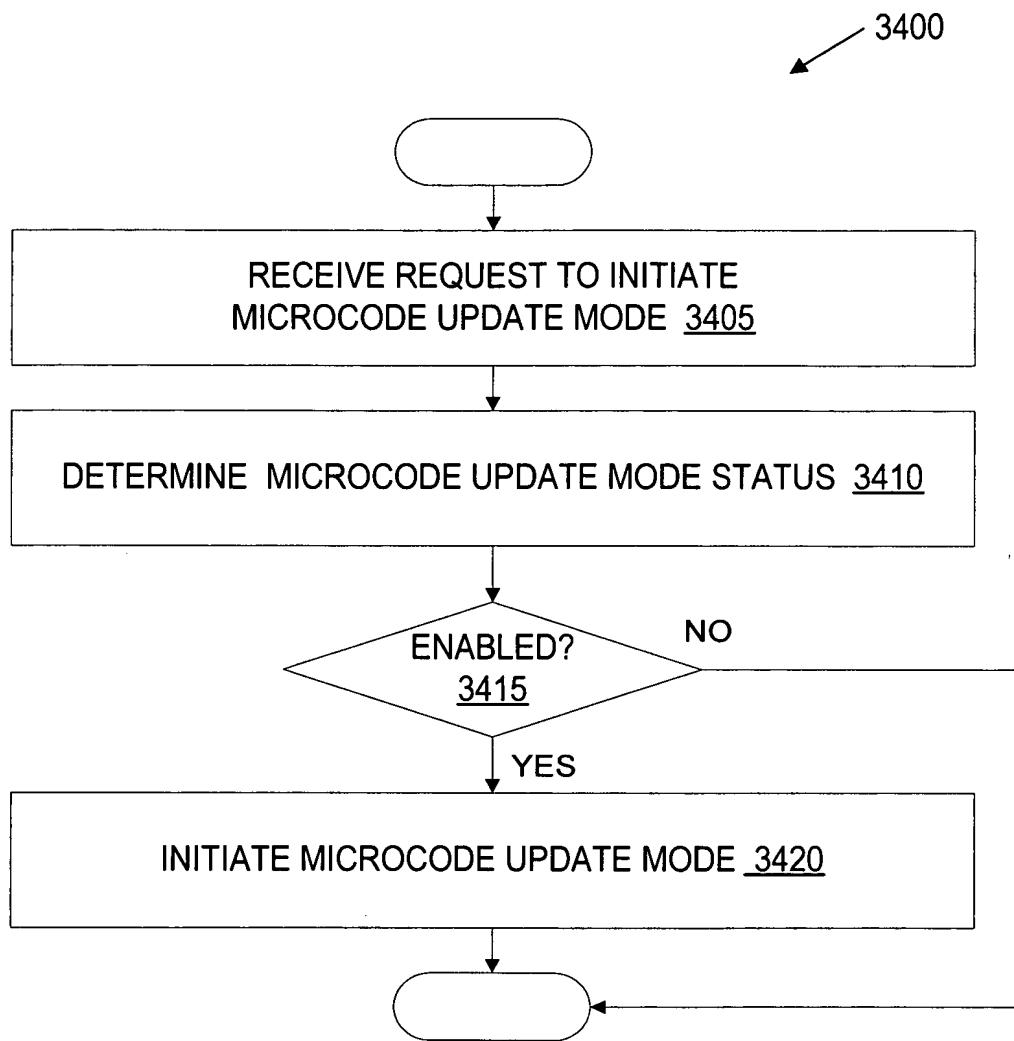


Fig. 22



**Fig. 23**

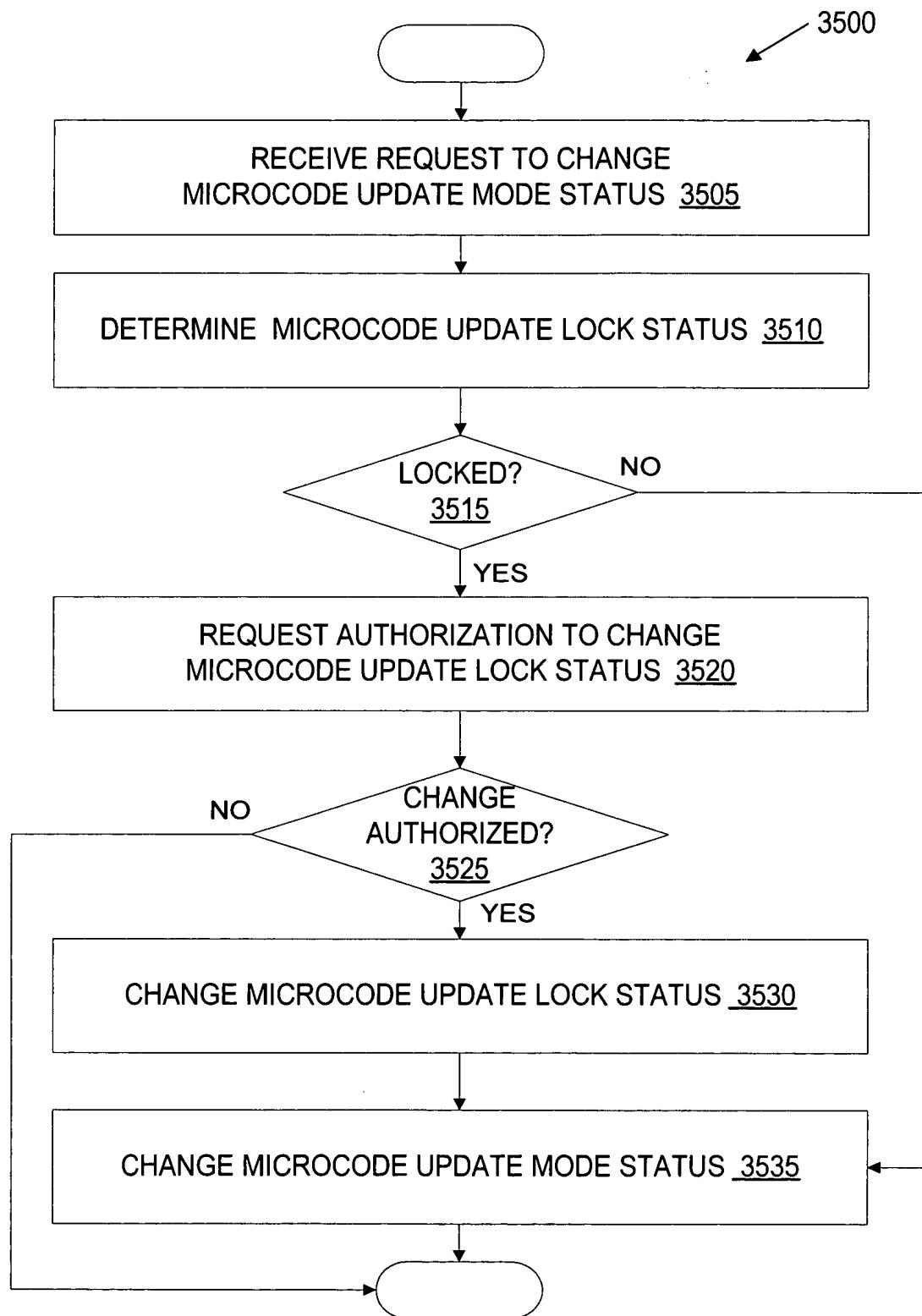
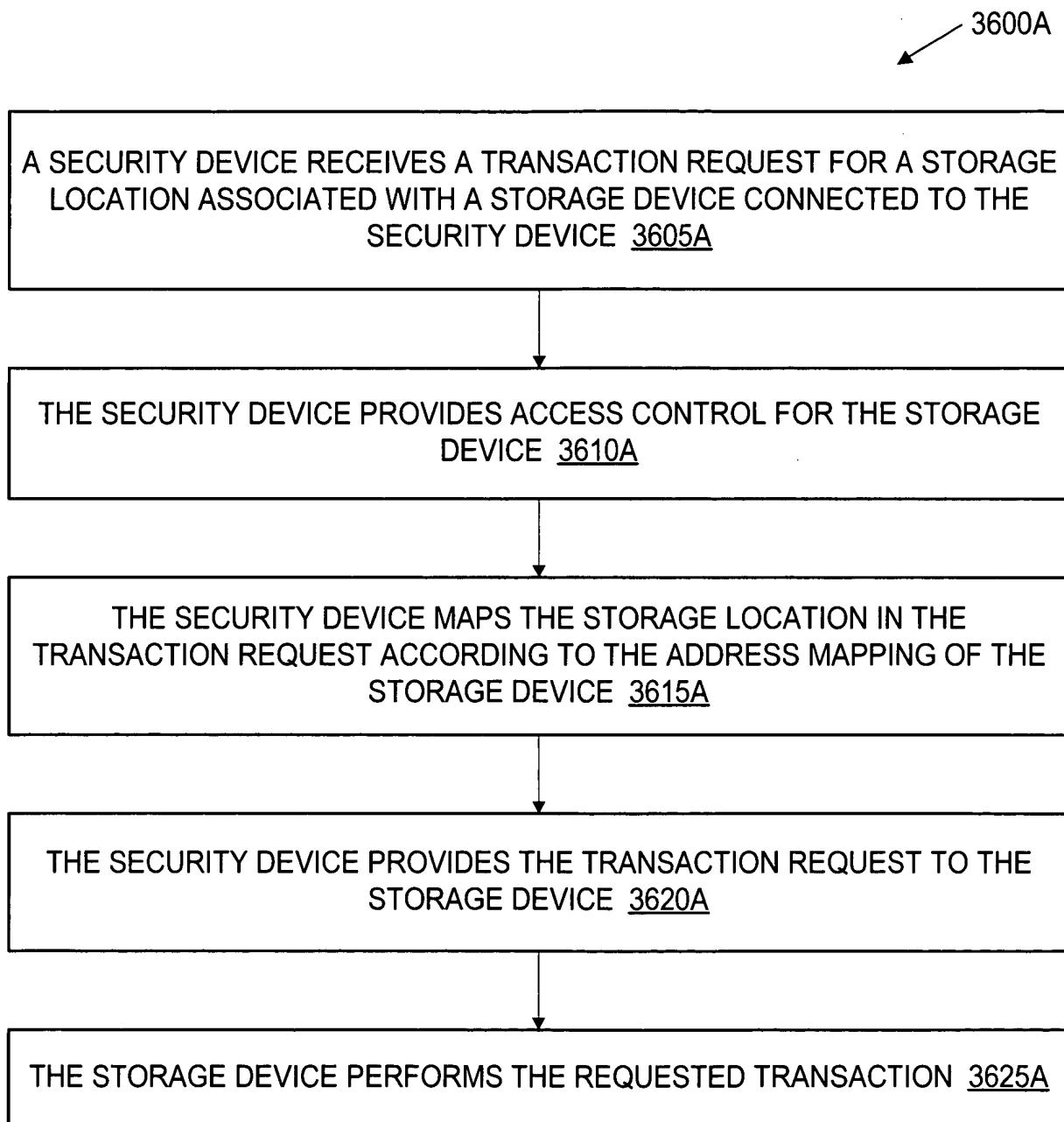
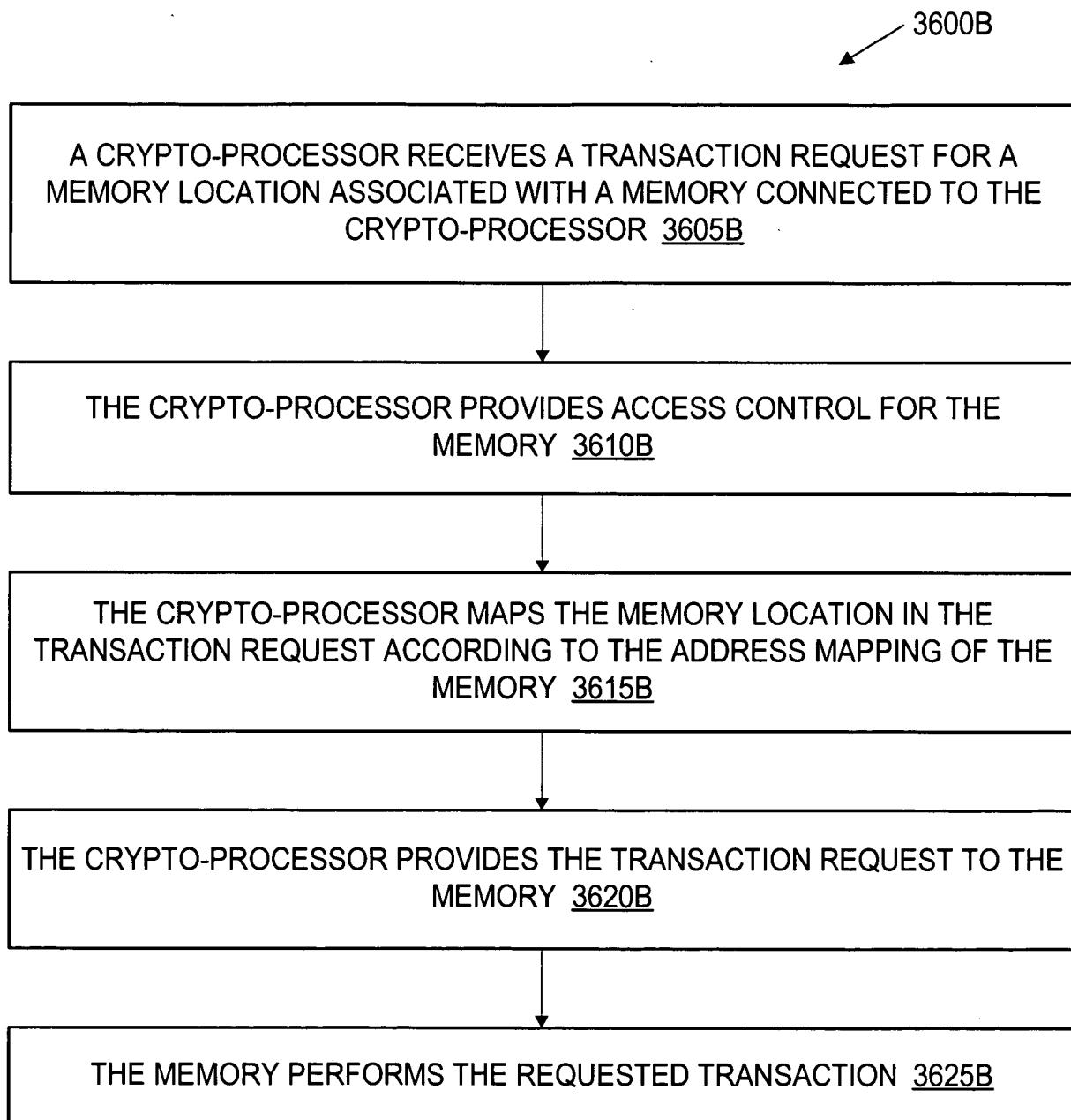


Fig. 24



**Fig. 25A**



**Fig. 25B**

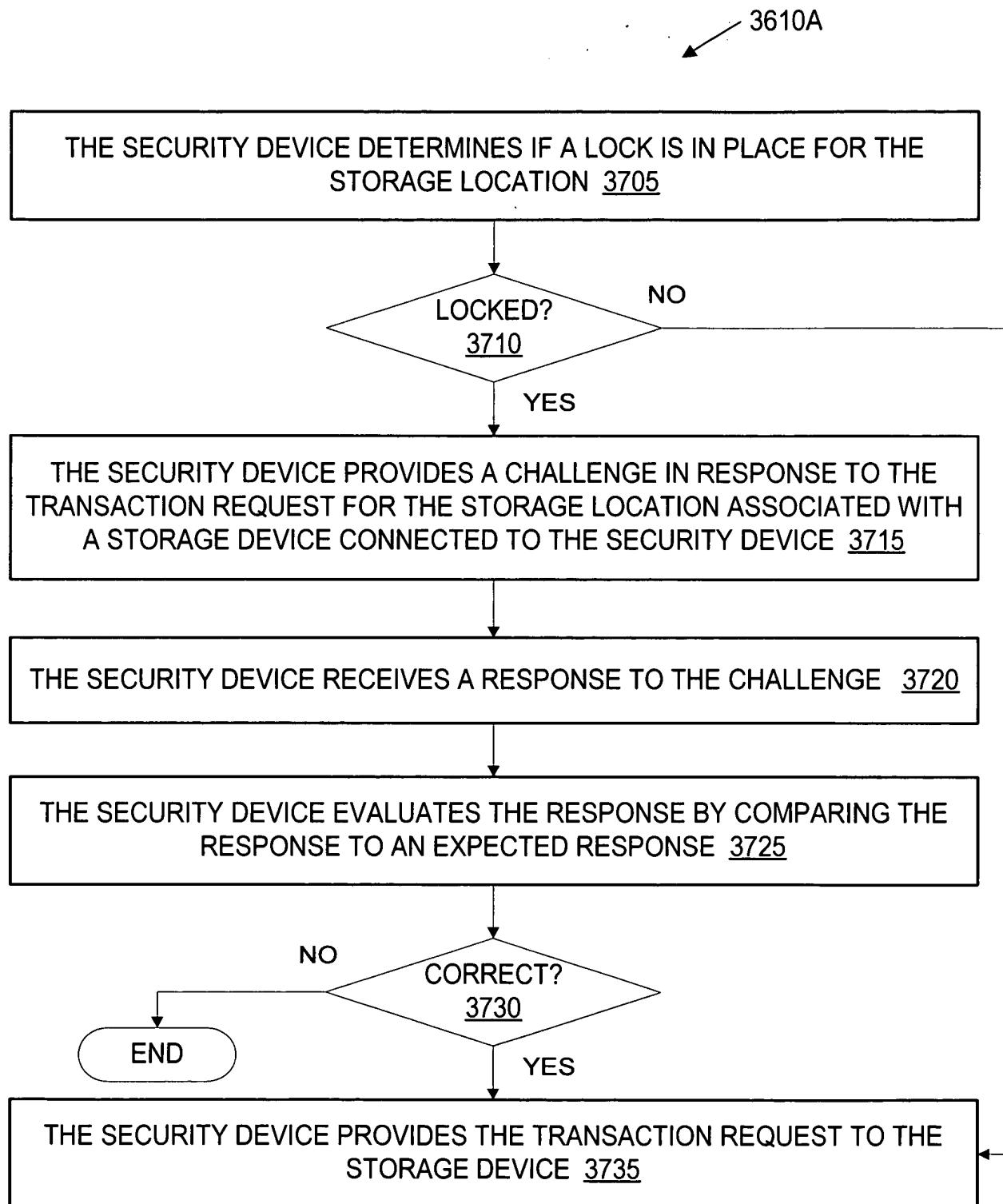


Fig. 26

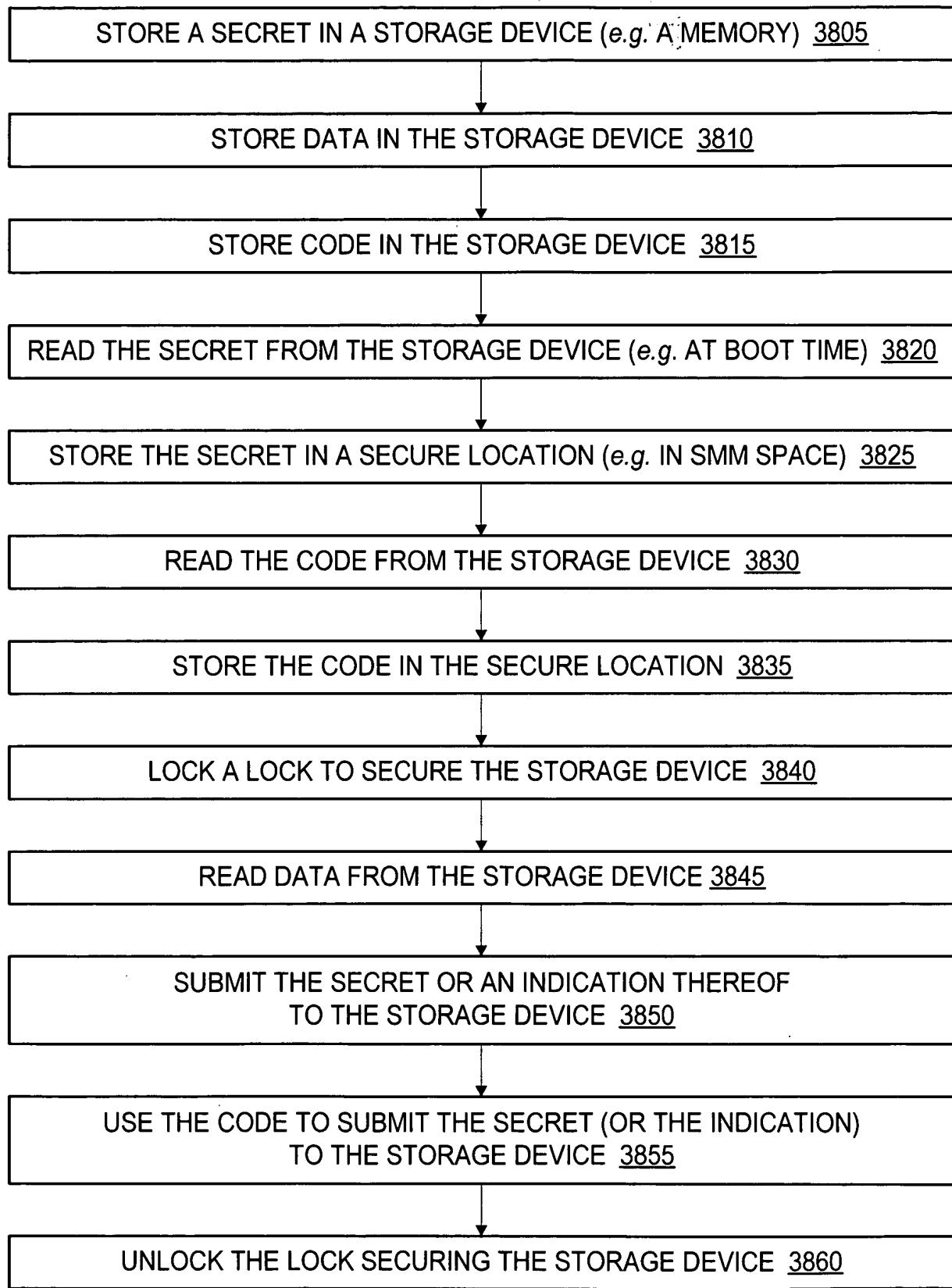
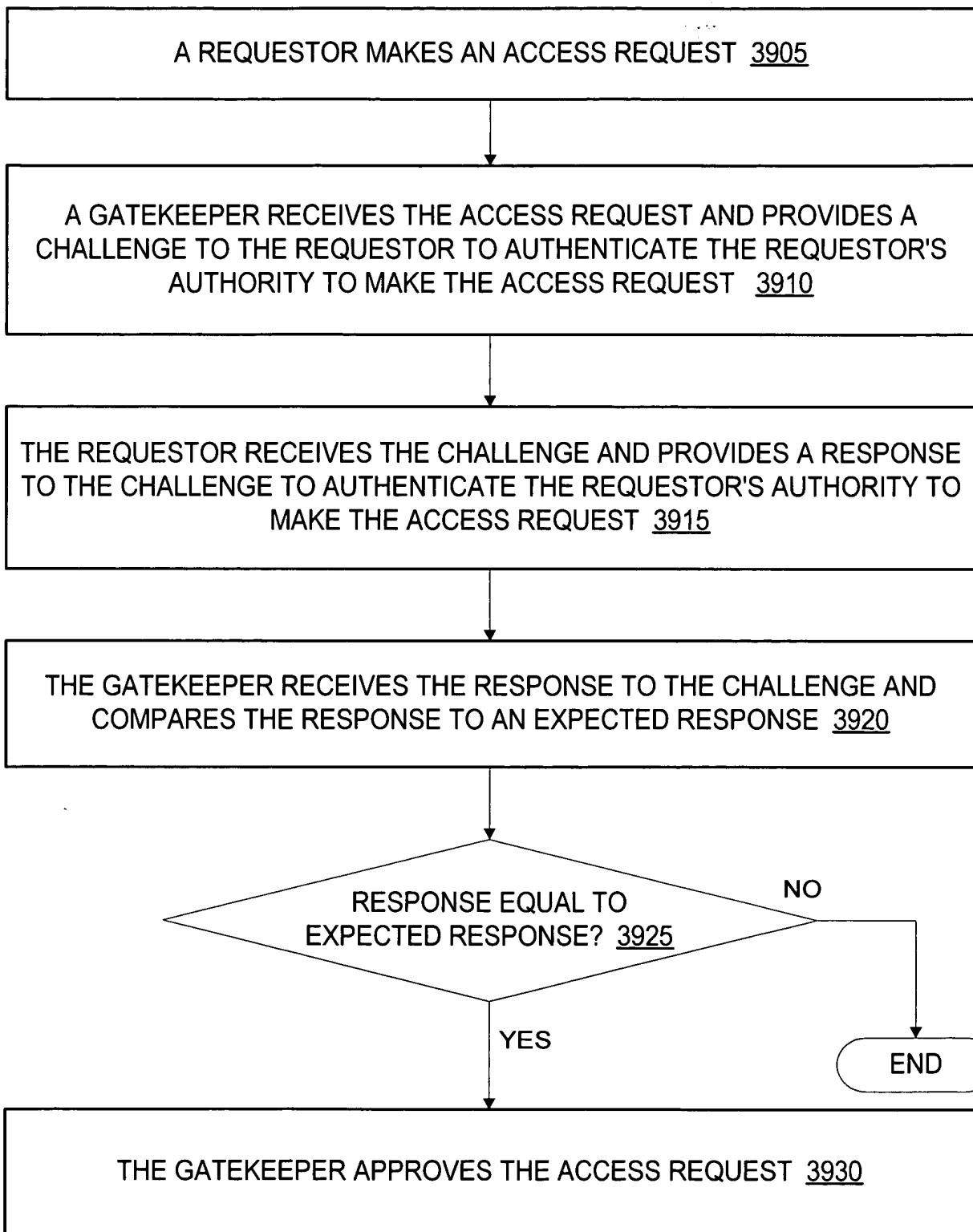


Fig. 27

3900



**Fig. 28**  
**(Prior Art)**

4000A

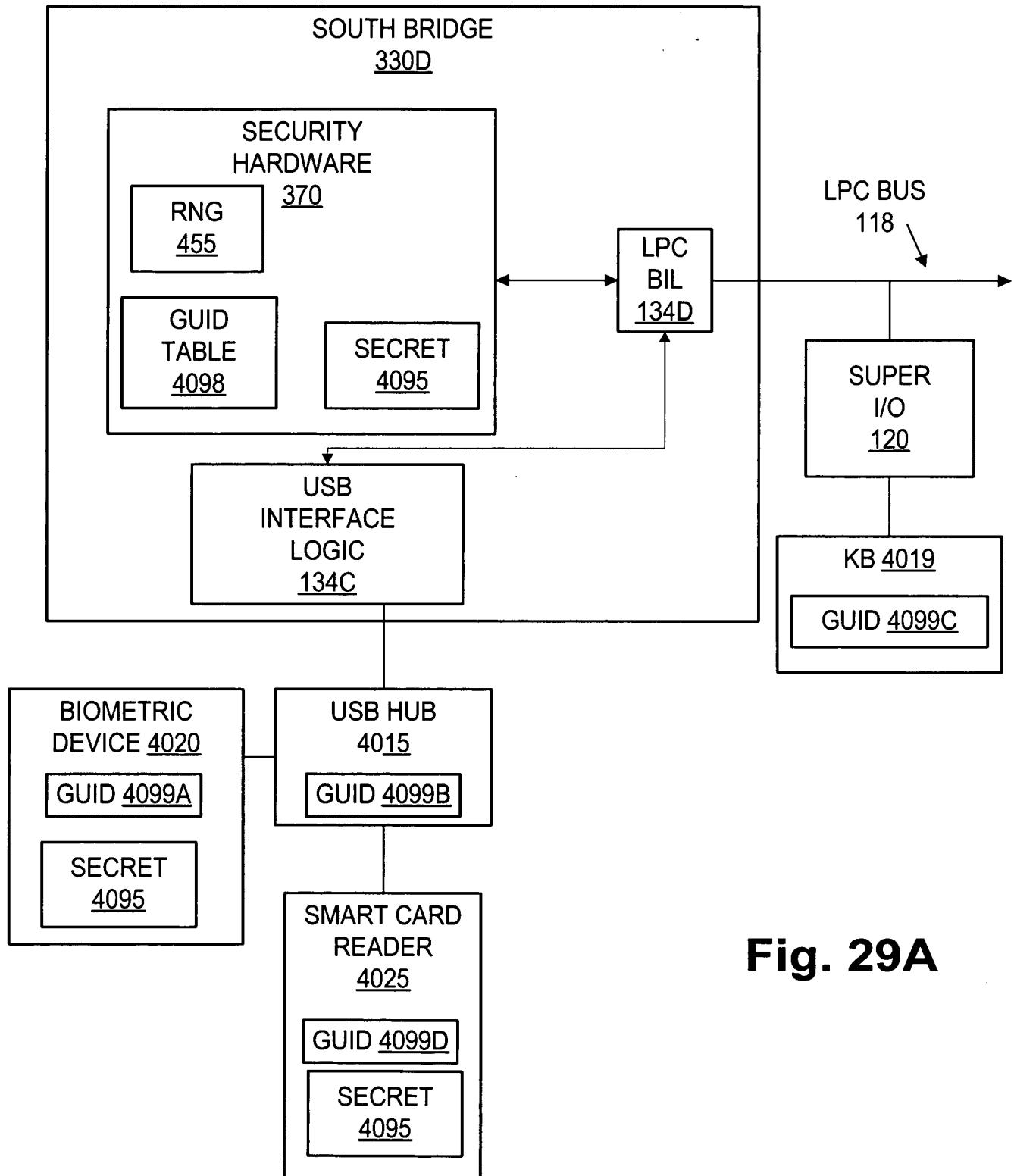
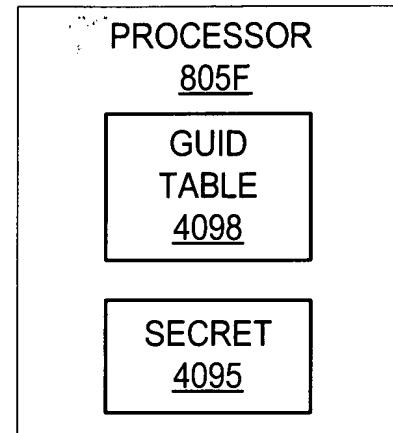
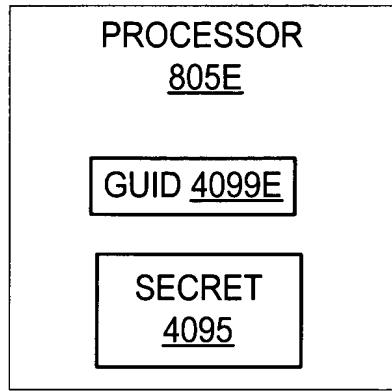
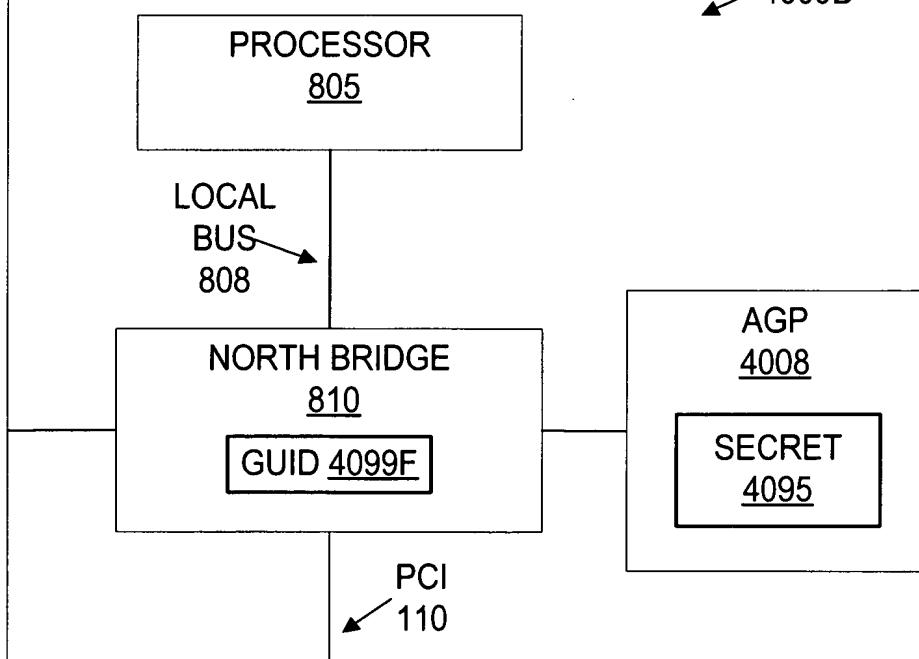
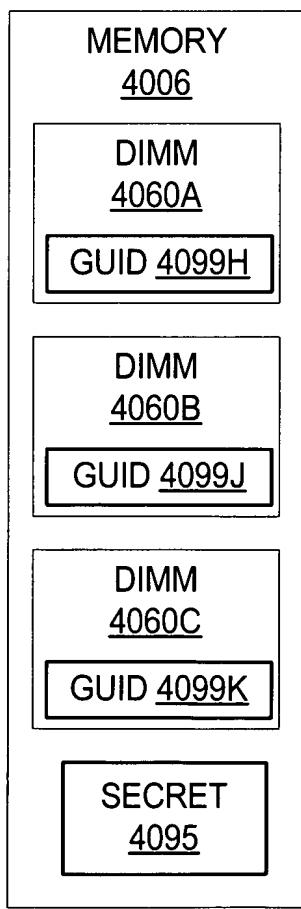


Fig. 29A

**Fig. 29B****Fig. 29C****Fig. 29D**

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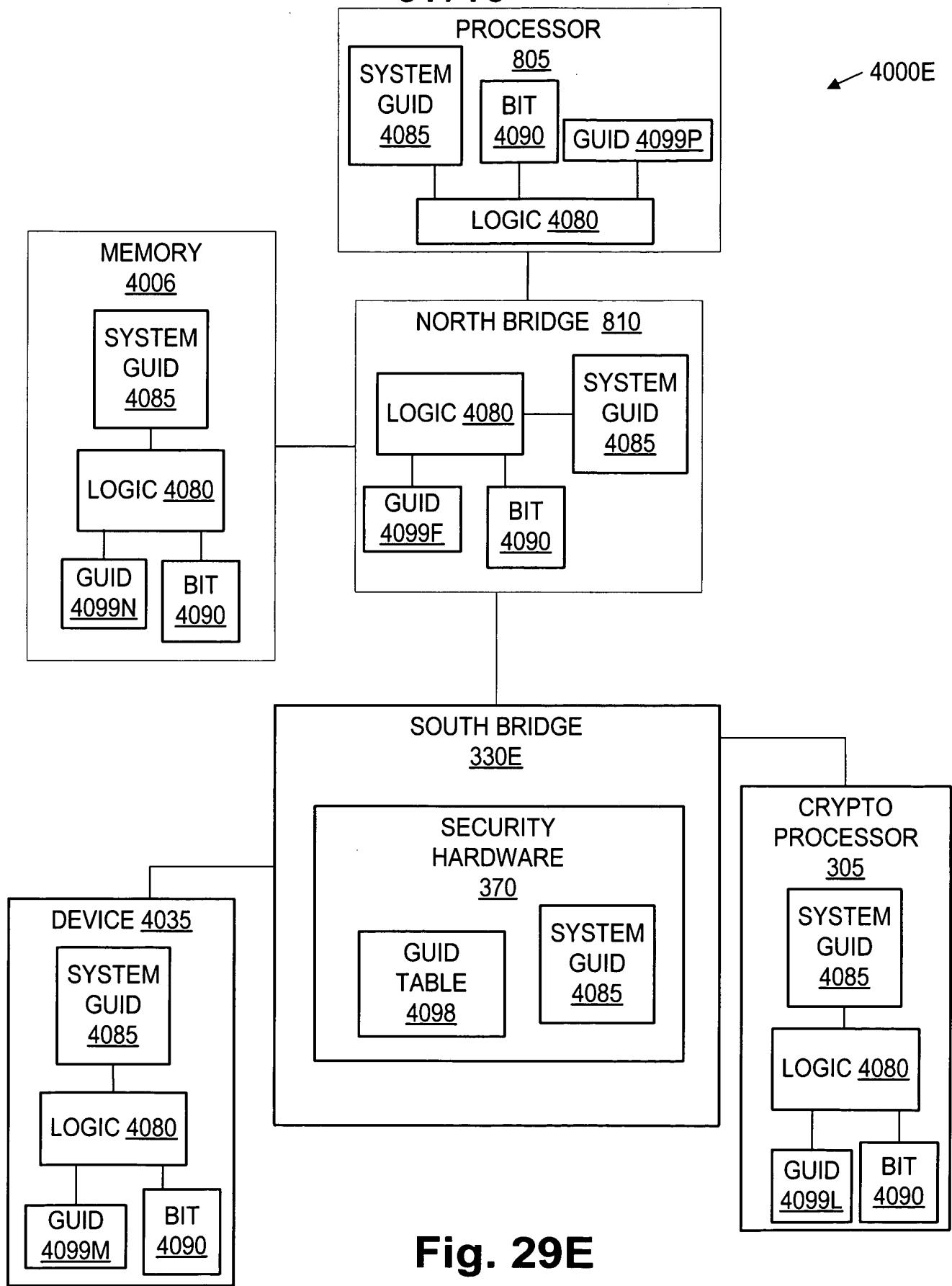


Fig. 29E

4100A

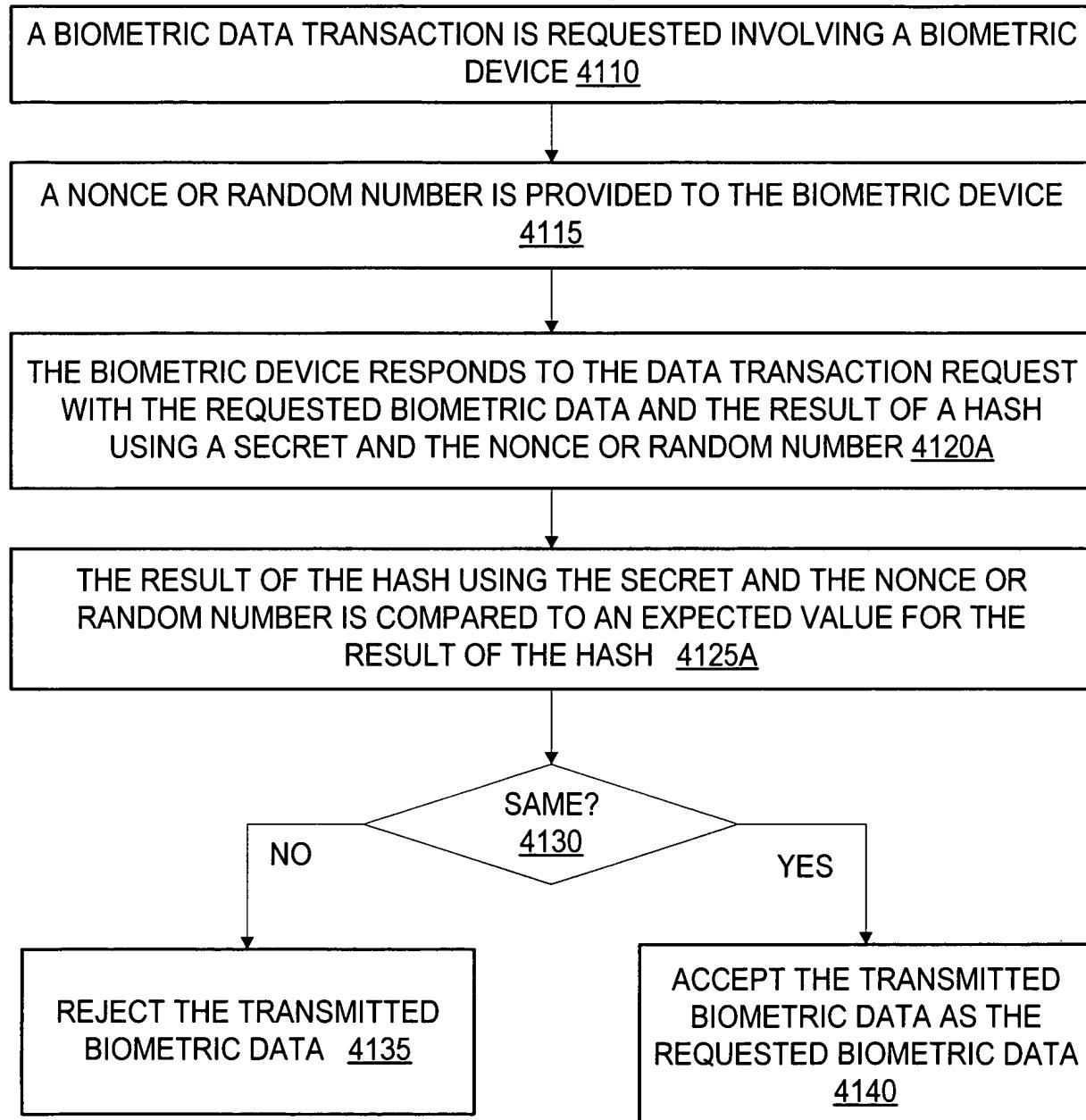


Fig. 30A

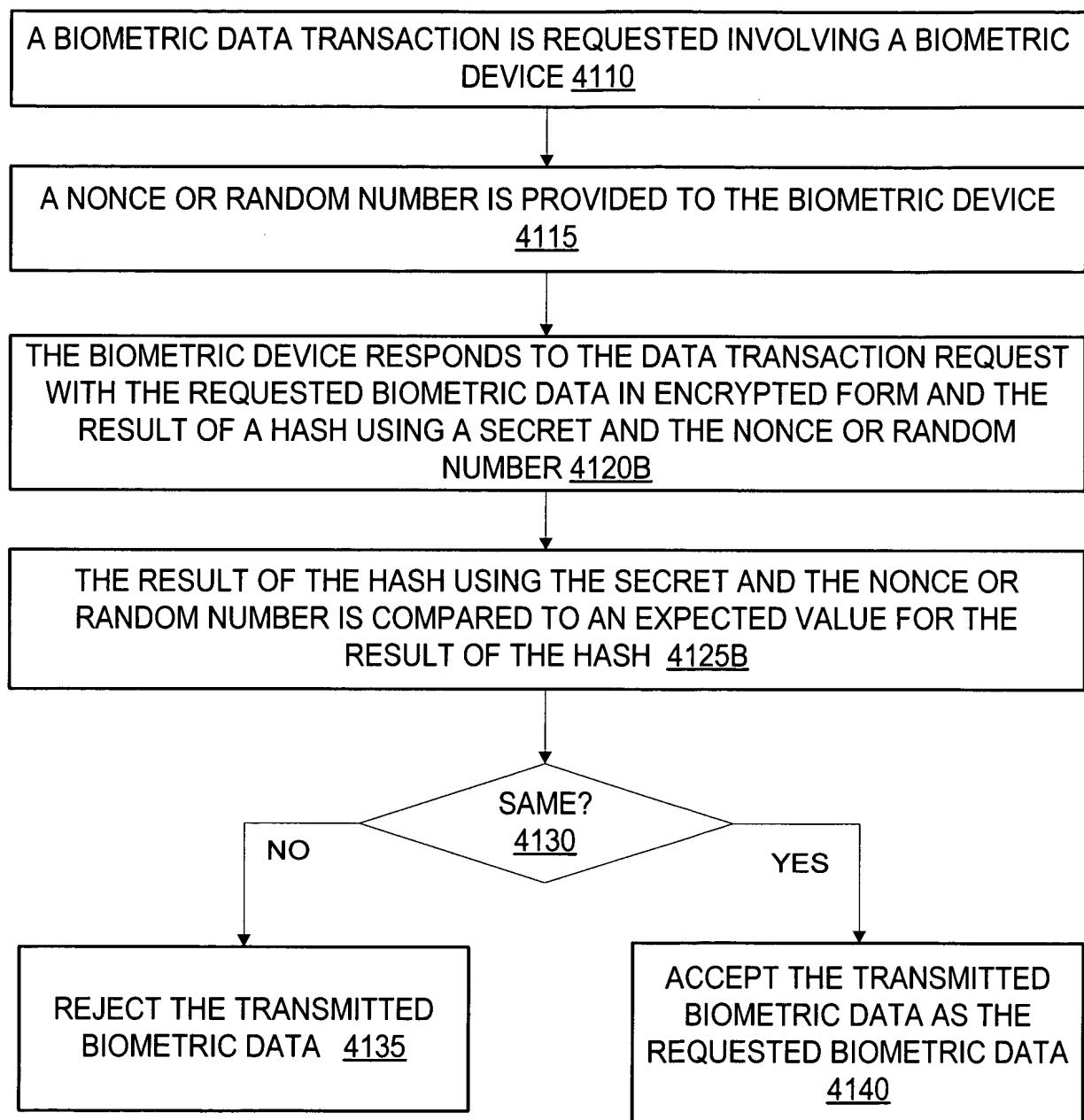


Fig. 30B

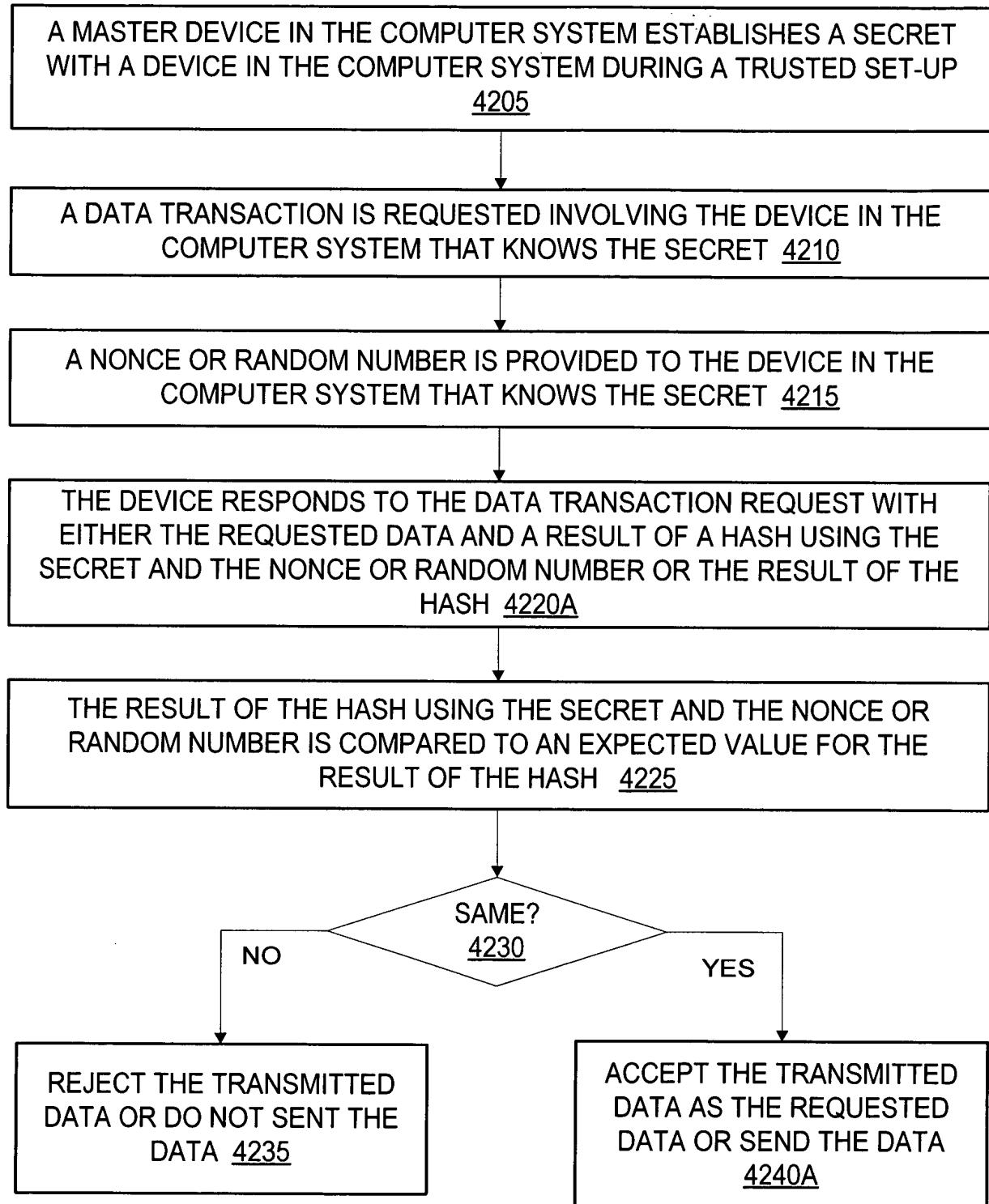


Fig. 31A

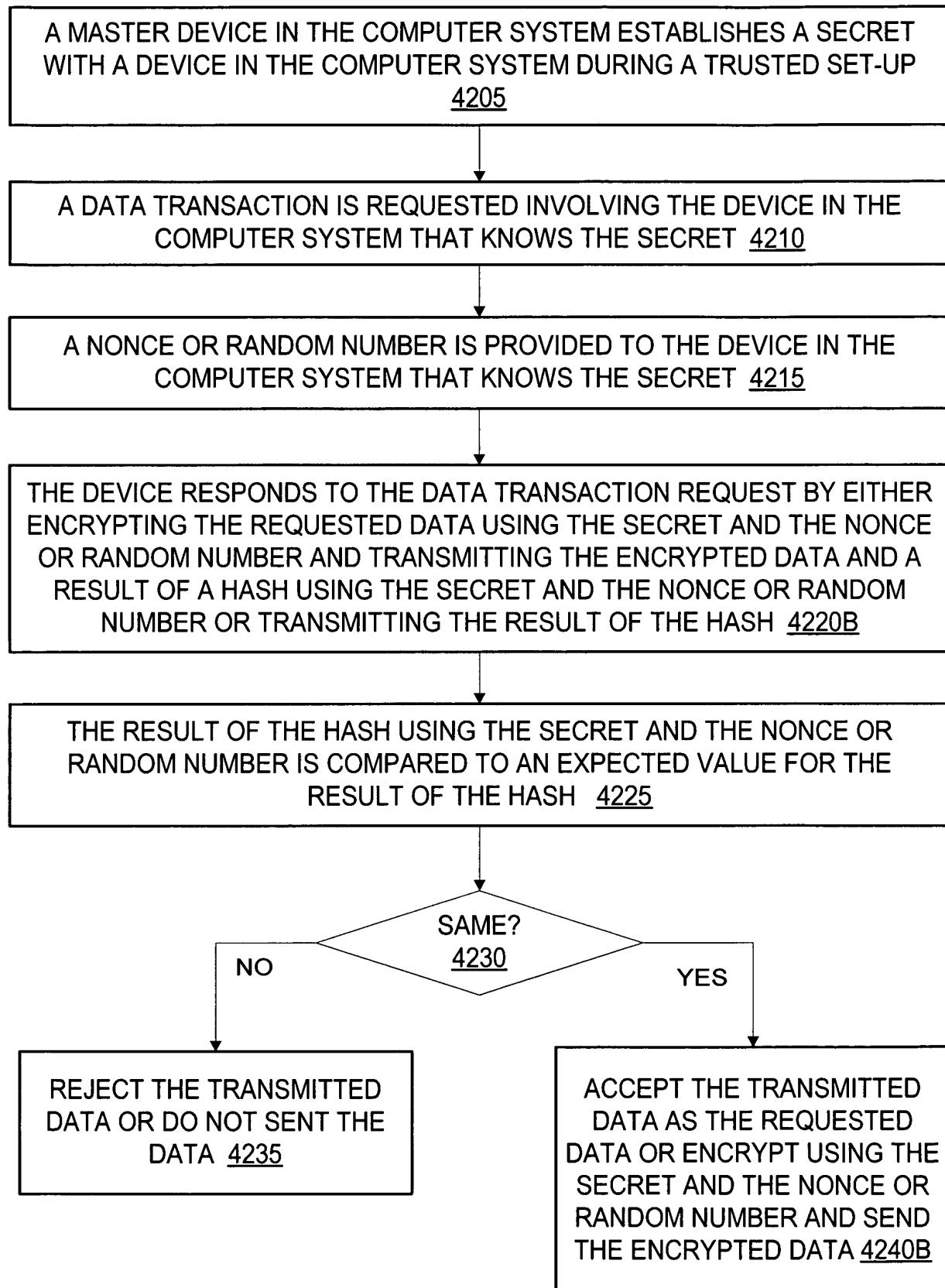


Fig. 31B

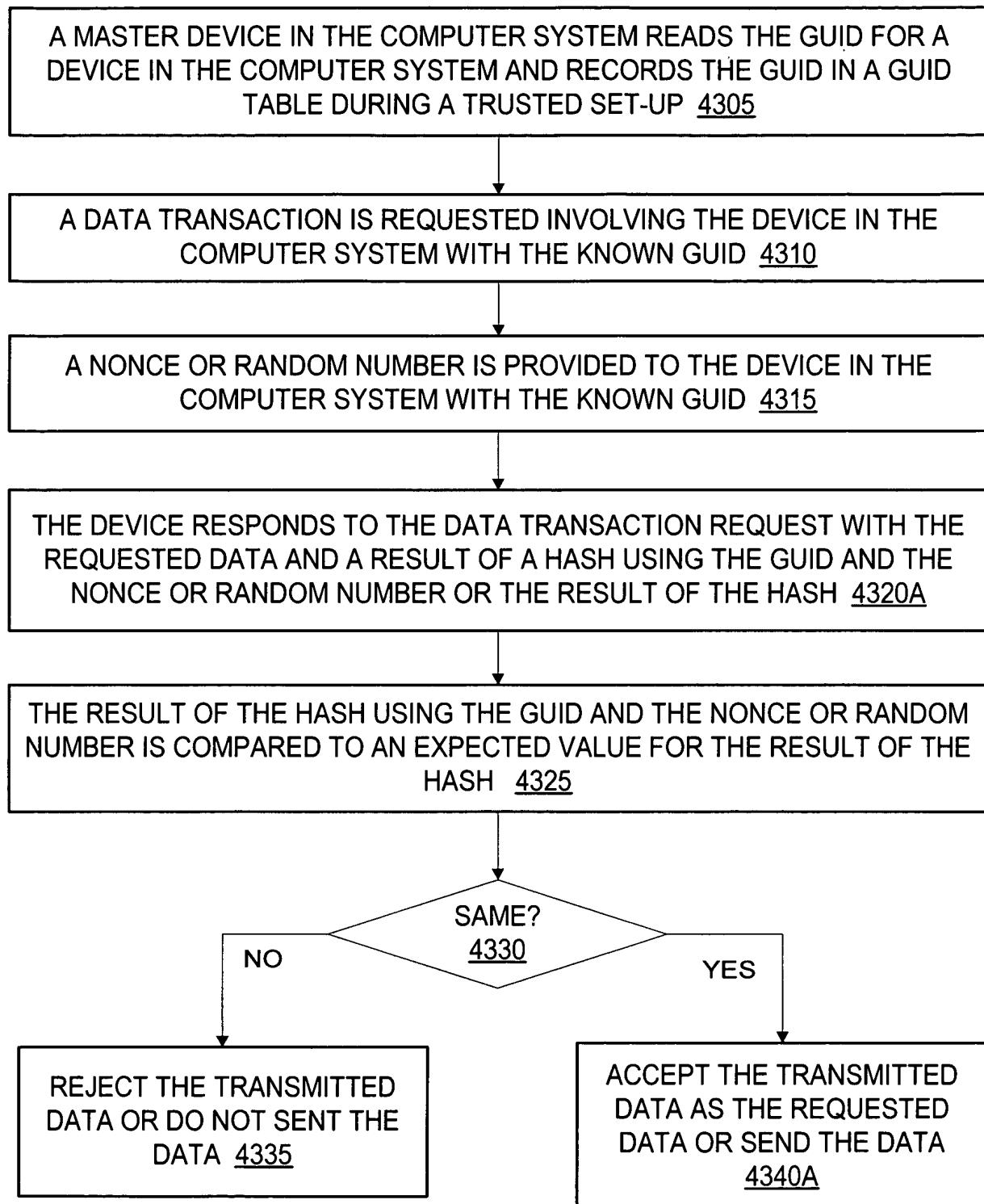


Fig. 32A

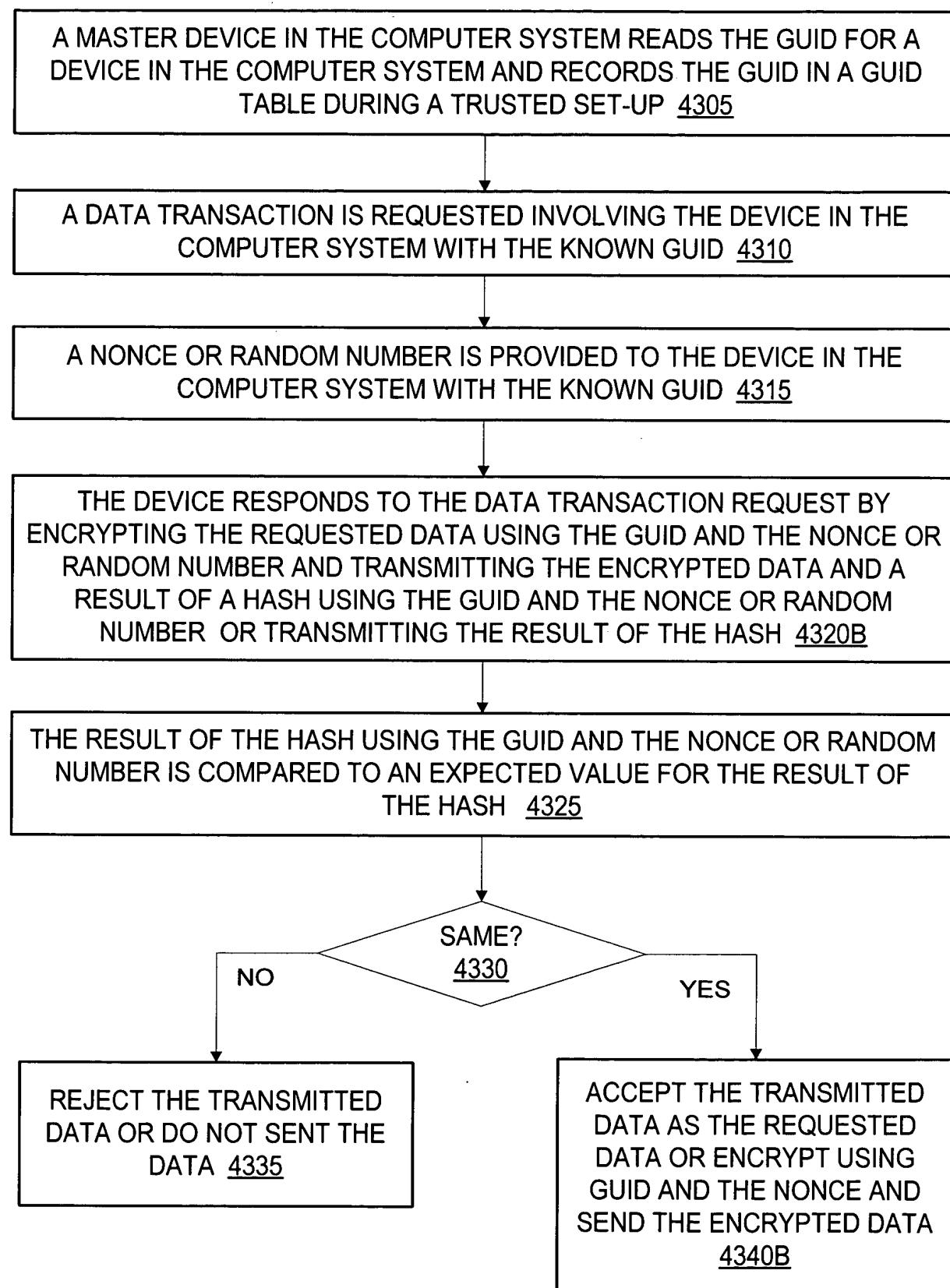


Fig. 32B

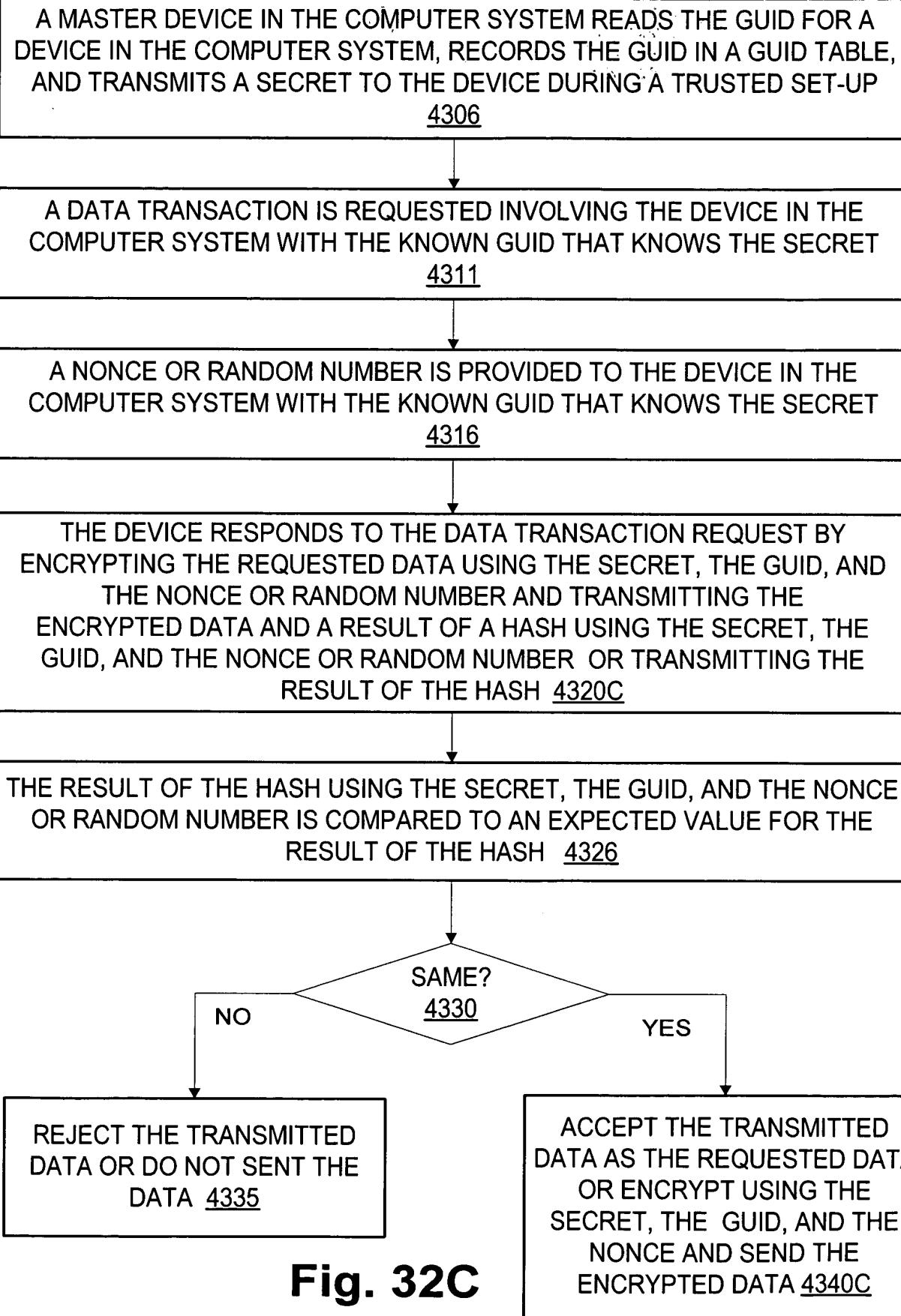


Fig. 32C

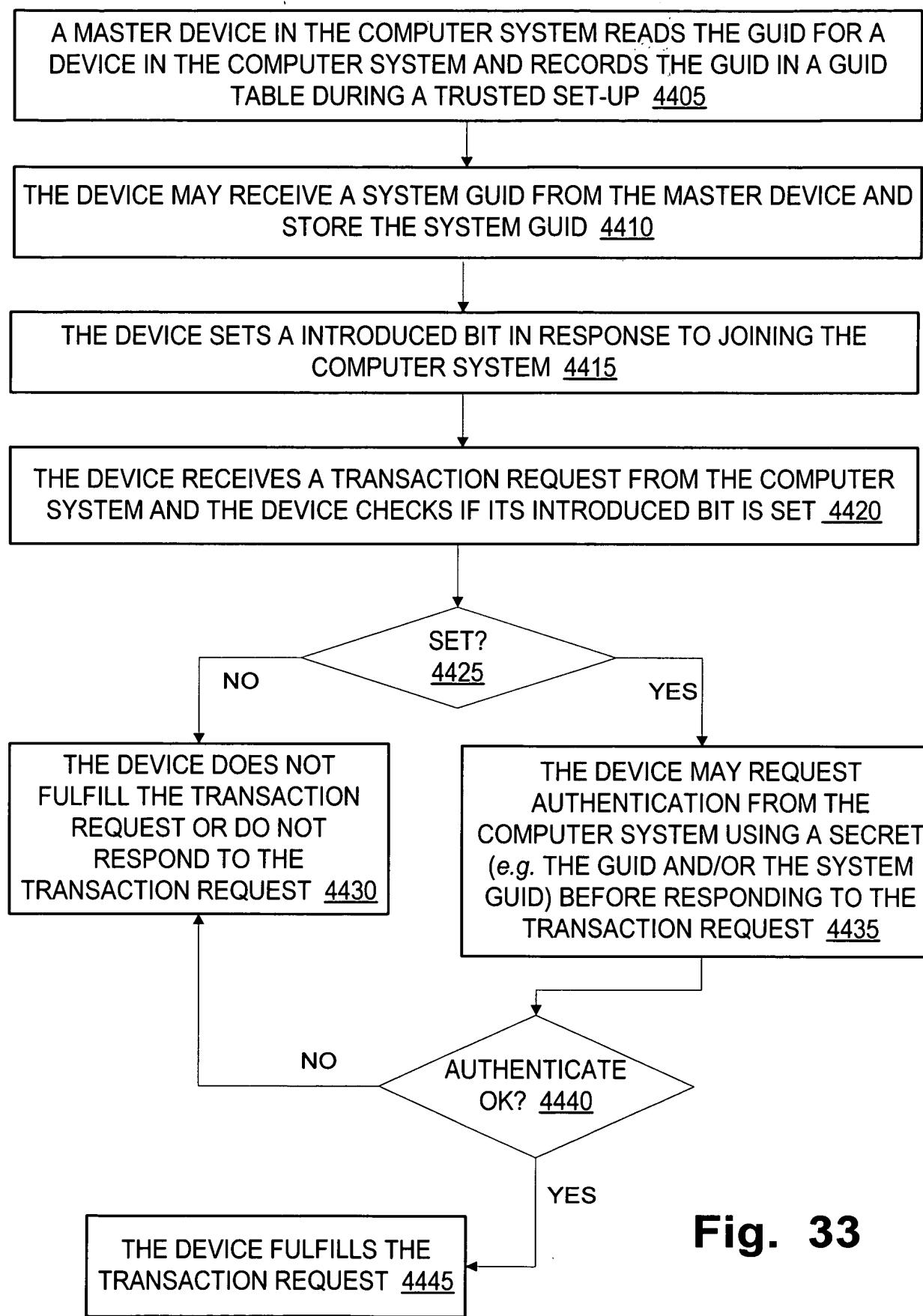


Fig. 33

THE DEVICE OR THE MASTER DEVICE INITIATES A REQUEST FOR THE DEVICE TO LEAVE THE COMPUTER SYSTEM 4505

THE DEVICE AND THE MASTER DEVICE AUTHENTICATE EACH OTHER USING THE GUID AND/OR THE SYSTEM GUID IN RESPONSE TO THE REQUEST FOR THE DEVICE TO LEAVE THE COMPUTER SYSTEM 4510

THE DEVICE RESETS THE INTRODUCED BIT IN RESPONSE TO THE DEVICE AND THE MASTER DEVICE SUCCESSFULLY AUTHENTICATING EACH OTHER 4515

**Fig. 34**

THE DEVICE RECEIVING A COMMAND FOR THE DEVICE TO LEAVE THE COMPUTER SYSTEM 4605

THE DEVICE RECEIVING A MAINTENANCE KEY THAT SUCCESSFULLY AUTHENTICATES 4610

THE DEVICE RESETS THE INTRODUCED BIT IN RESPONSE TO THE DEVICE RECEIVING THE MAINTENANCE KEY THAT SUCCESSFULLY AUTHENTICATES 4615

**Fig. 35**

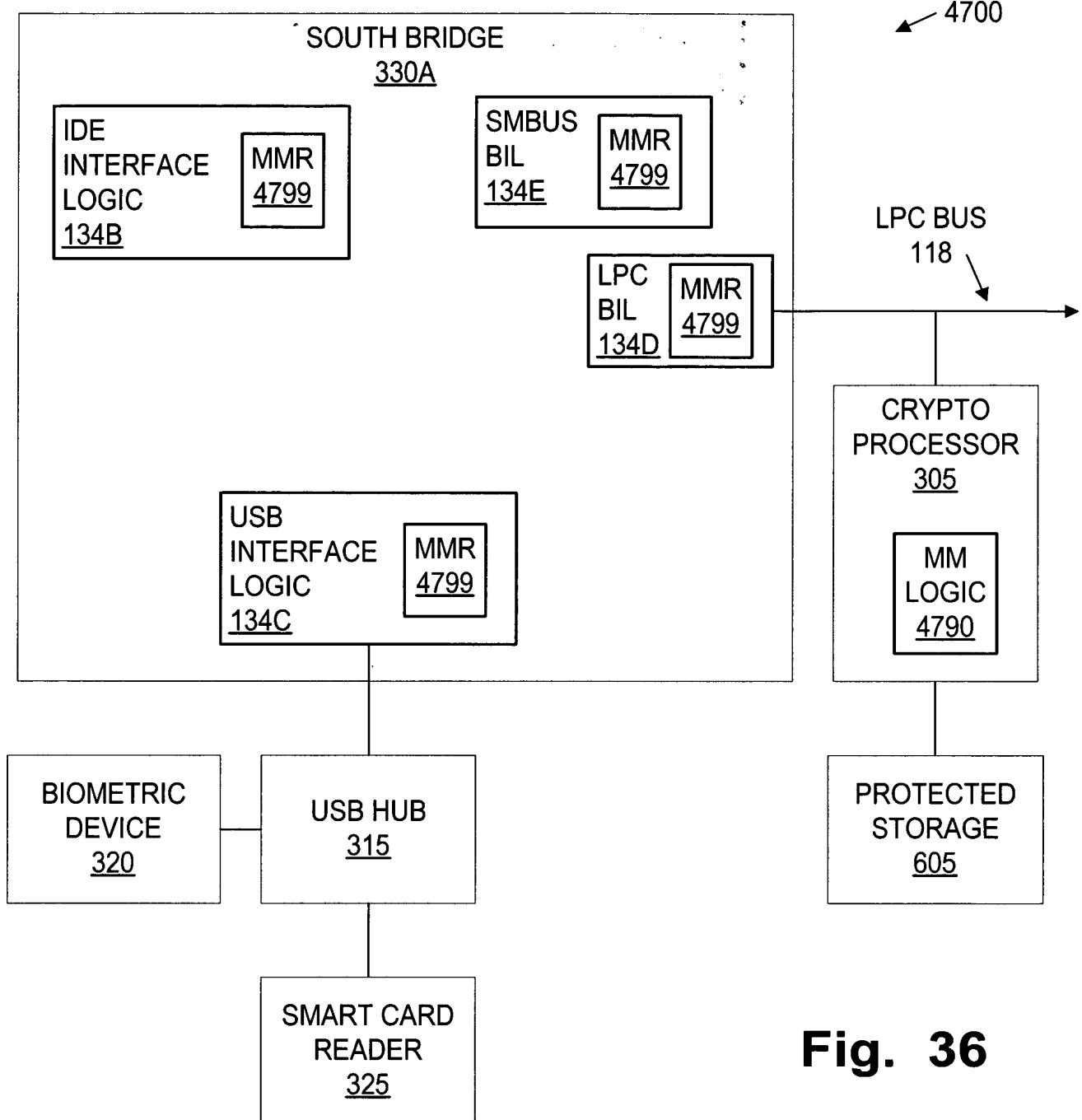


Fig. 36

TRANSMIT A MASTER MODE SIGNAL TO BUS INTERFACE LOGIC CONNECTED BETWEEN MASTER MODE LOGIC AND A DATA INPUT DEVICE, WHERE THE BUS INTERFACE LOGIC INCLUDES A MASTER MODE REGISTER 4805

SET A MASTER MODE BIT IN THE MASTER MODE REGISTER(S) TO ESTABLISH SECURE TRANSMISSION CHANNEL BETWEEN THE MASTER MODE LOGIC AND THE DATA INPUT DEVICE OUTSIDE THE OPERATING SYSTEM OF THE COMPUTER SYSTEM 4810

THE MASTER MODE LOGIC AND THE DATA INPUT DEVICE EXCHANGE DATA OUTSIDE THE OPERATING SYSTEM OF THE COMPUTER SYSTEM THROUGH THE BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER 4815

THE MASTER MODE LOGIC FLUSHES THE BUFFERS OF THE BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER AFTER CONCLUDING THE DATA TRANSMISSIONS 4820

THE MASTER MODE LOGIC SIGNALS THE BUS INTERFACE LOGIC(S) TO UNSET THE Maser MODE BITS AFTER FLUSHING THE BUFFERS OF THE BUS INTERFACE LOGIC(S) THAT INCLUDE THE MASTER MODE REGISTER 4825

Fig. 37

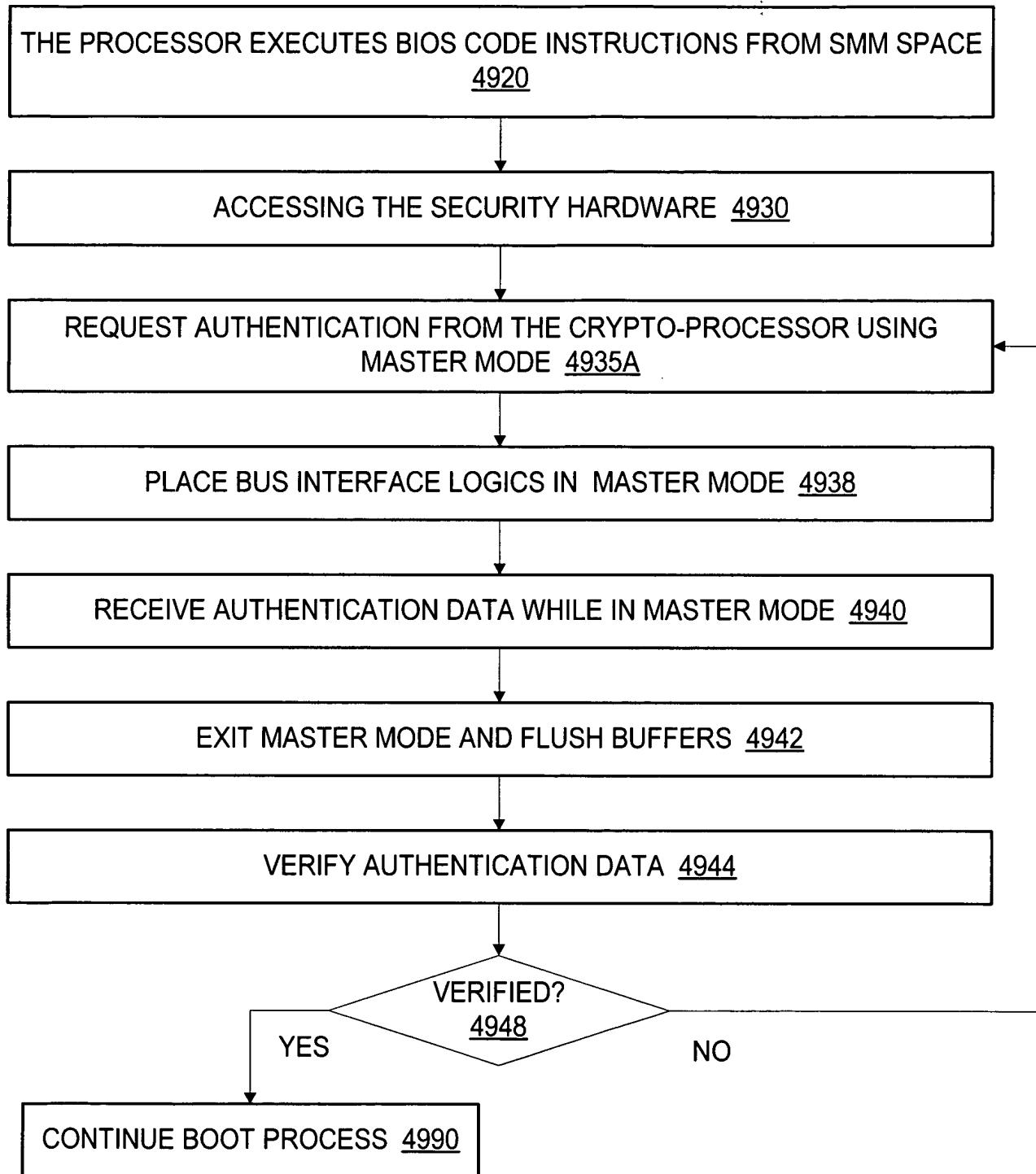


Fig. 38A

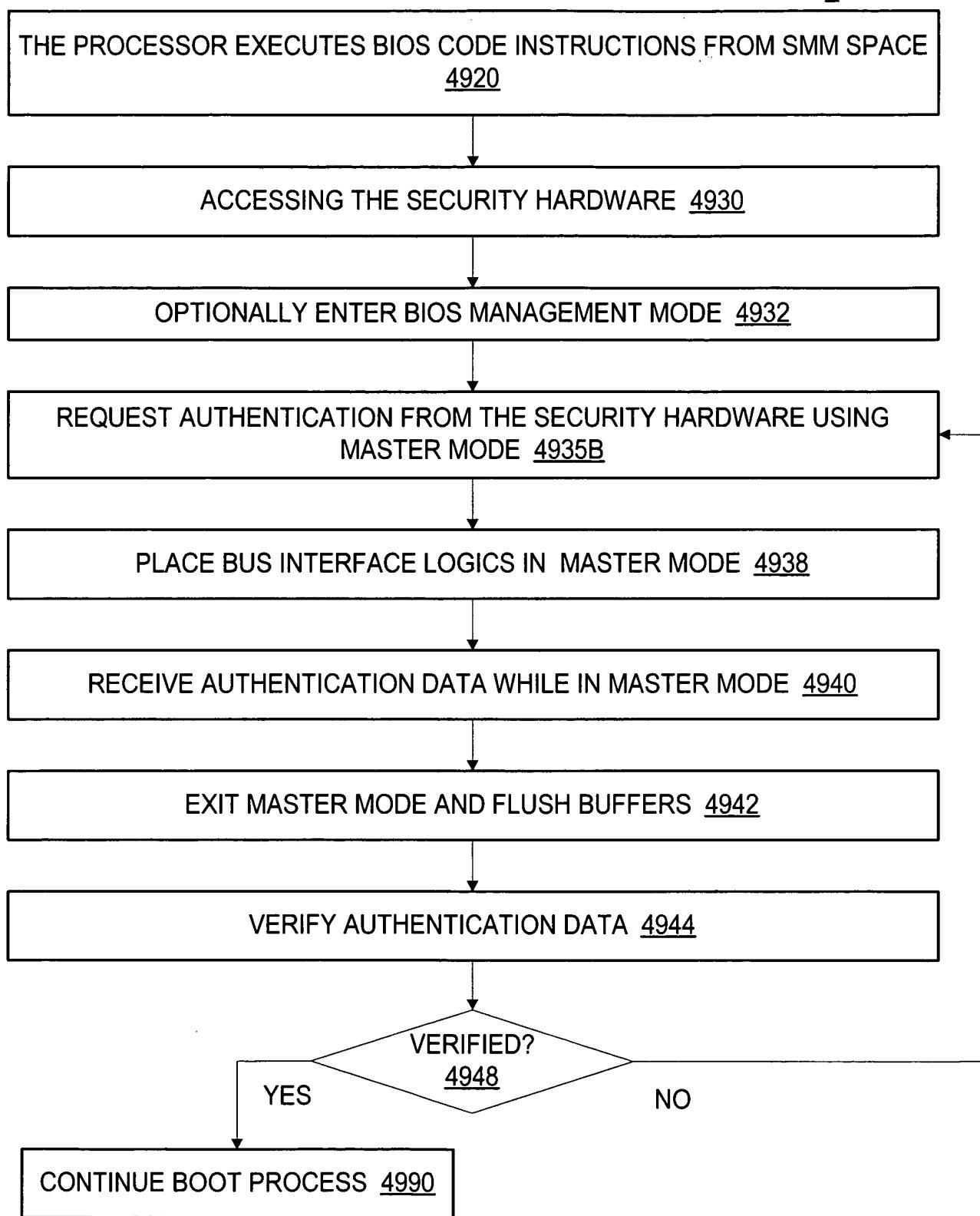
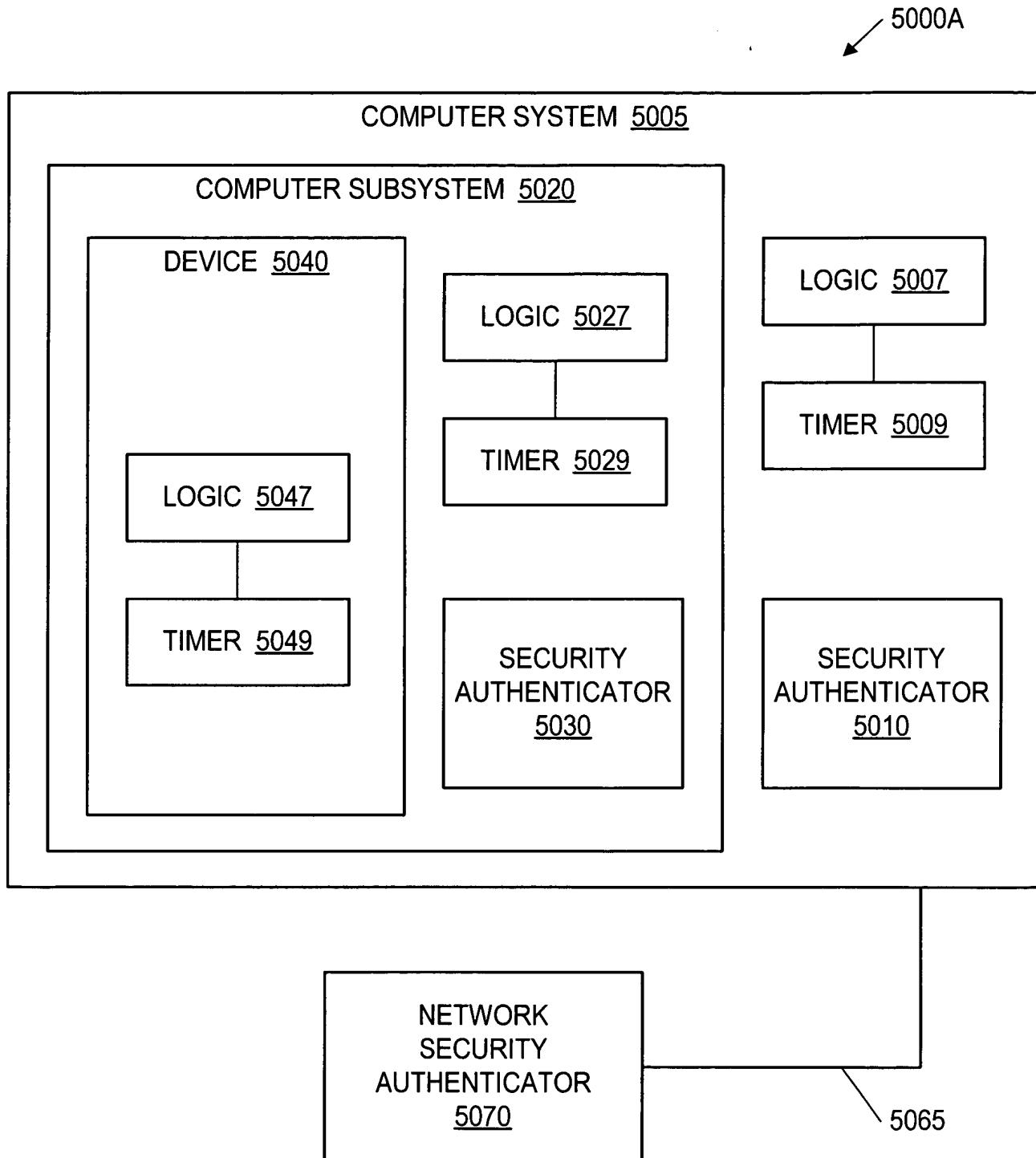


Fig. 38B



**Fig. 39A**

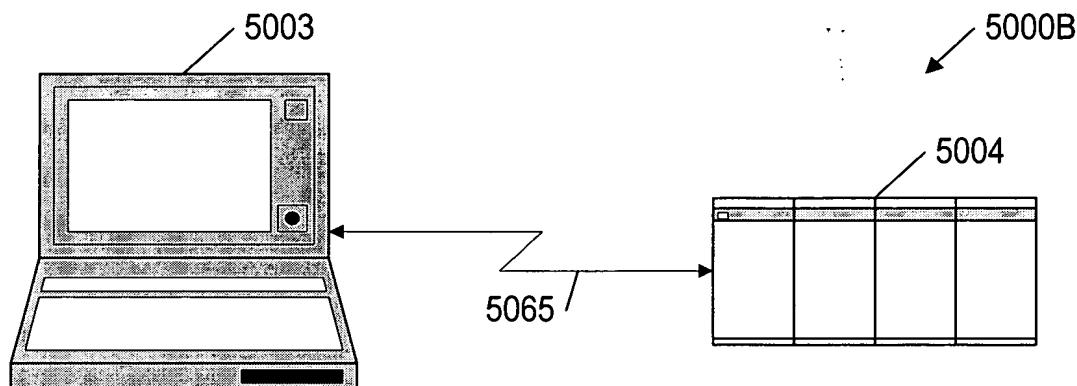


Fig. 39B

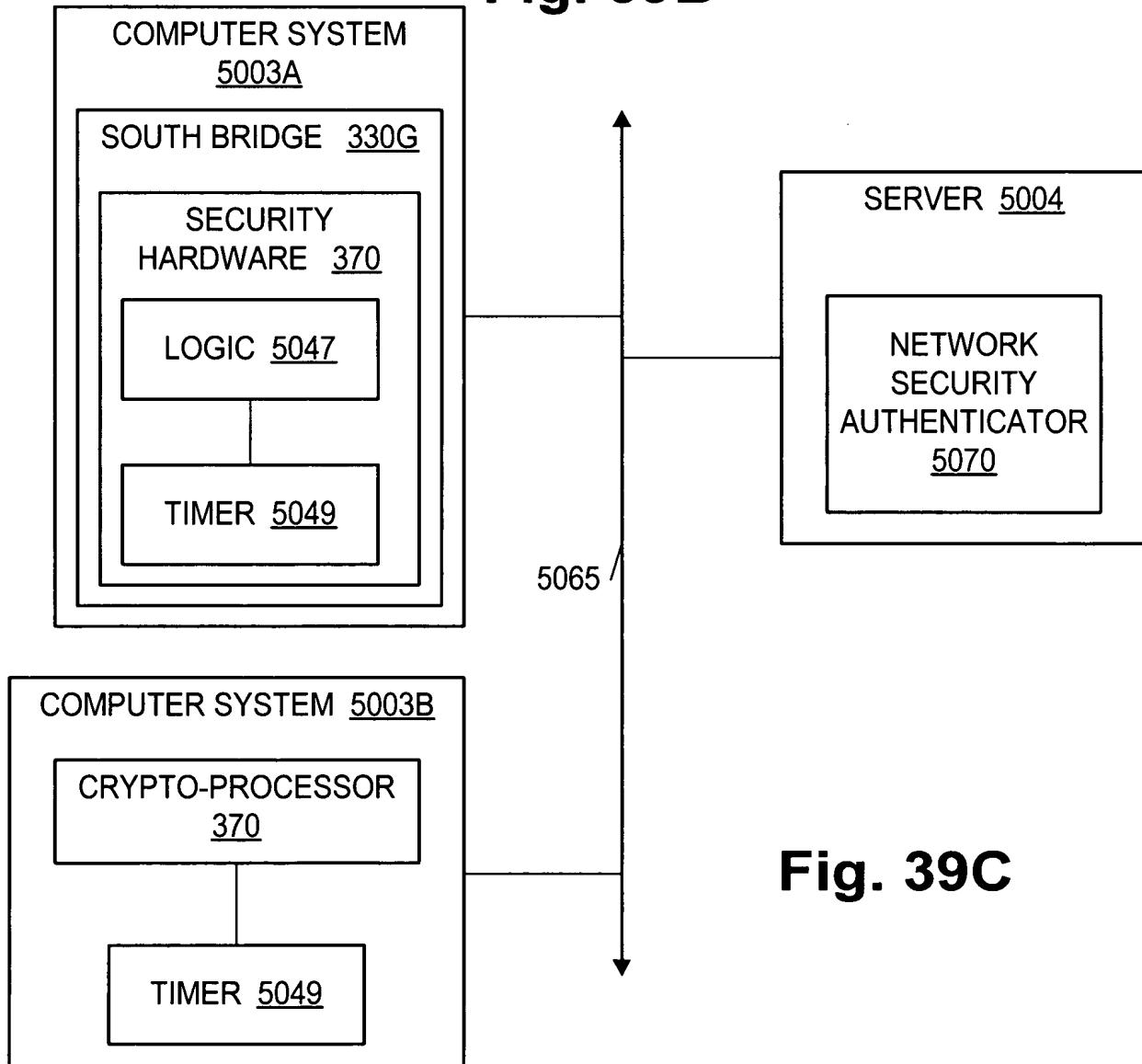


Fig. 39C

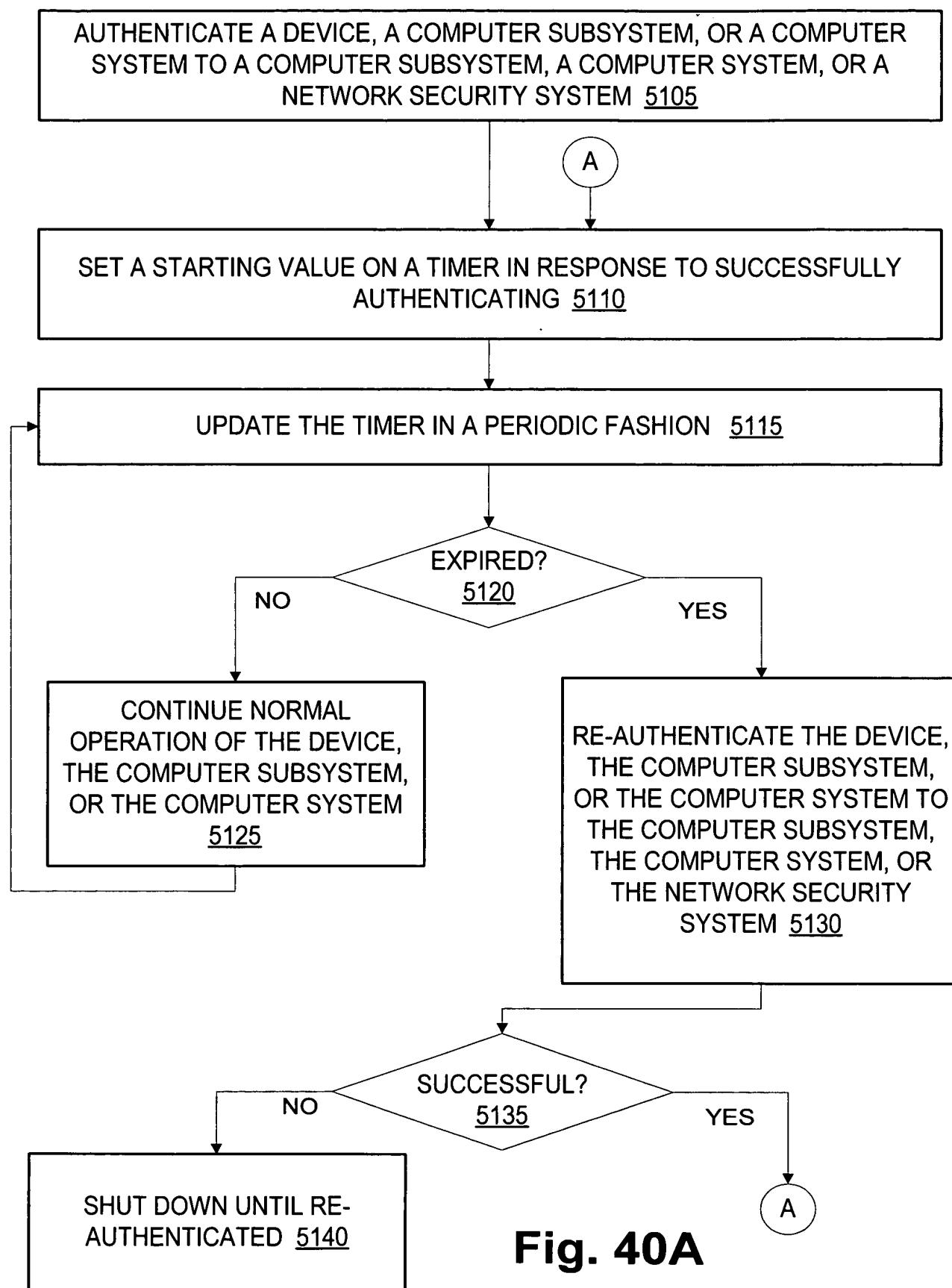


Fig. 40A

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5100B

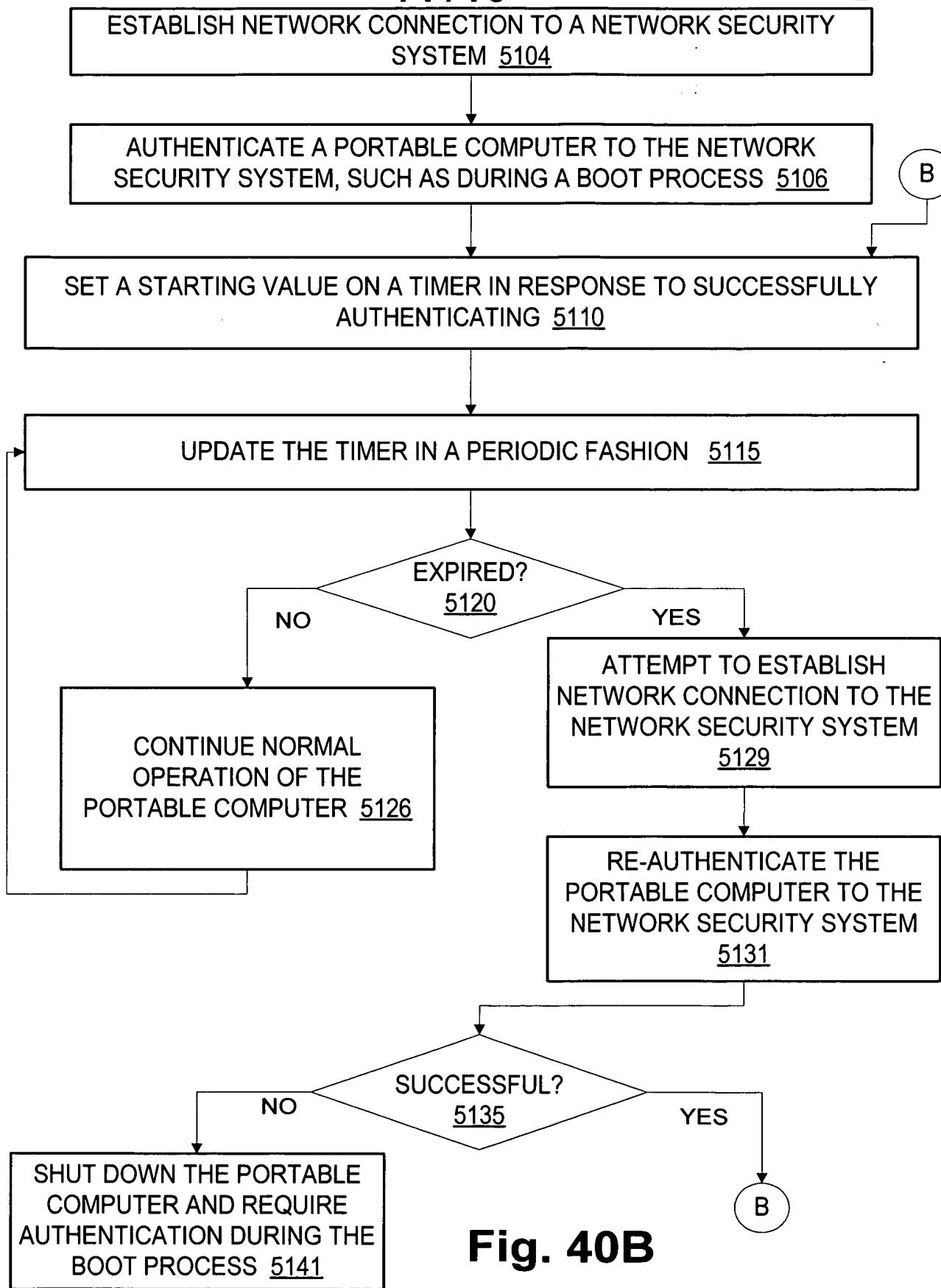


Fig. 40B

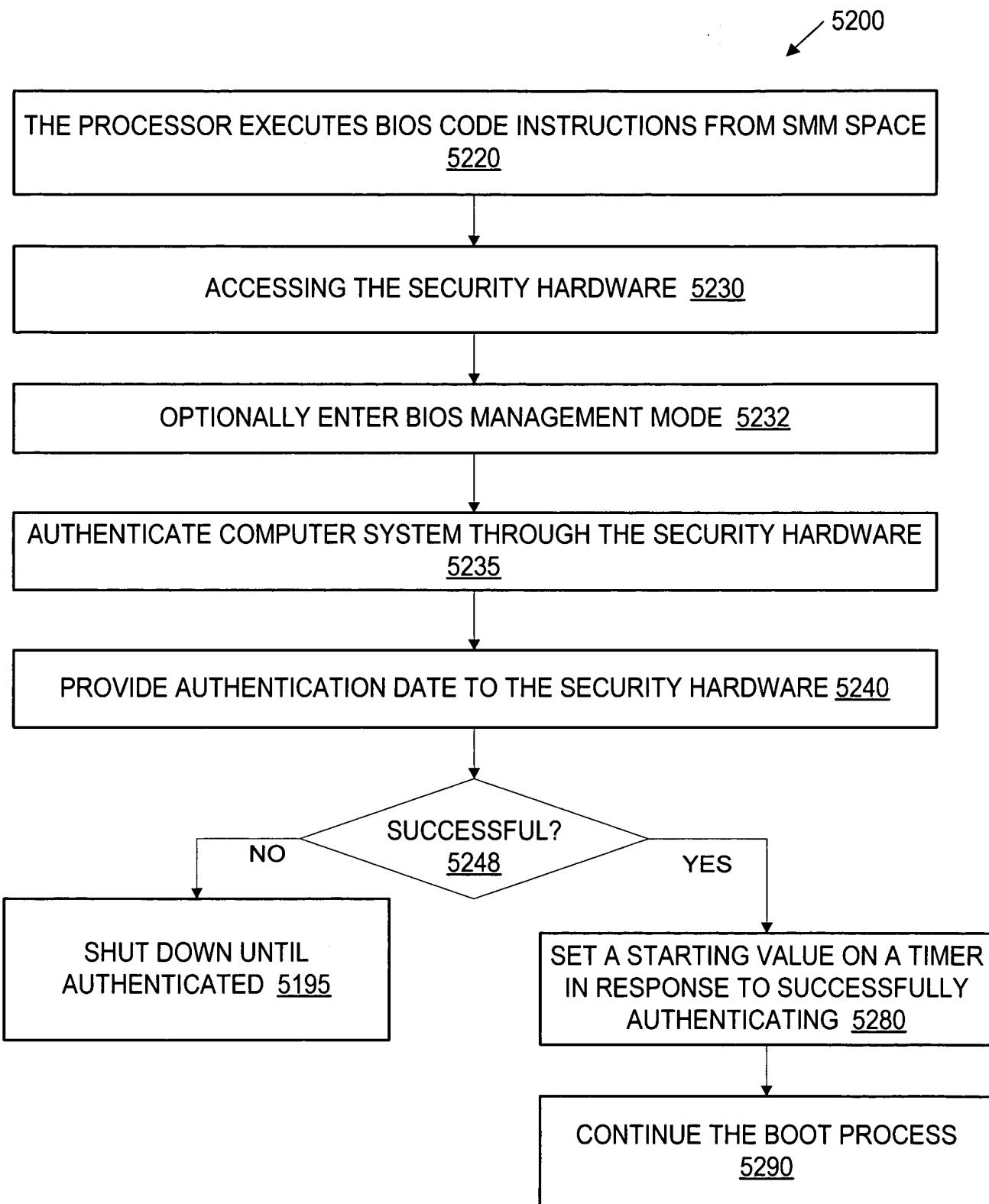
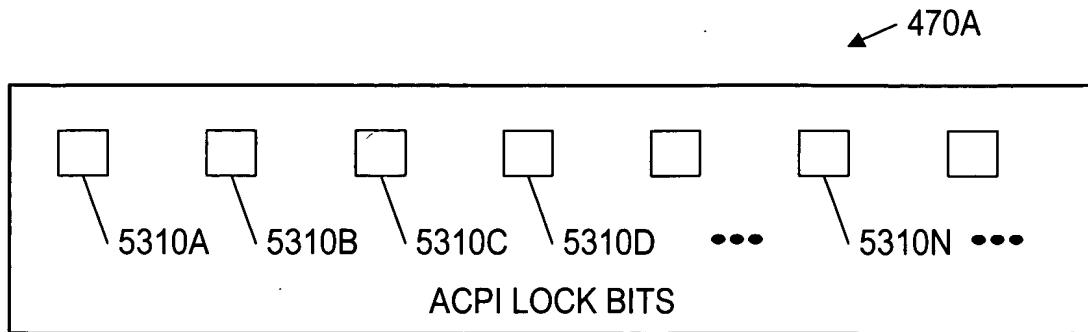
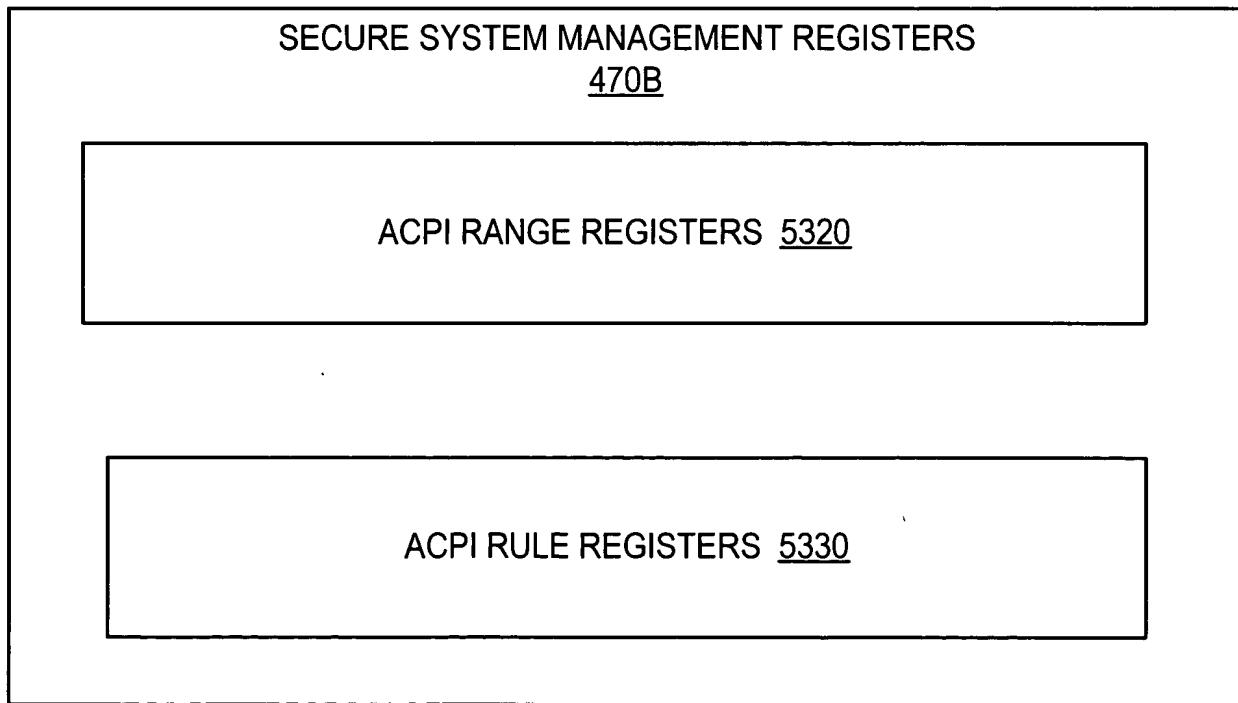


Fig. 41

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**Fig. 42A**



**Fig. 42B**